# BKW65N050HS1 650V 50A Trench FS IGBT



### **Description**

The BKW65N050HS1 is a Trench FS IGBT utilizing bestirpower's advanced technology, which achieves an exceptionally low gate charge.

Itachieves significantly higher efficiency through optimized gate charge management, while its user-friendly design offers designers advantages such as low EMI and reduced switching losses.

VCE	I <sub>C (TC = 100°C)</sub>	$V_{CEsat}$	$Q_{g,typ}$	
650 V	50 A	1.4 V	135 nC	

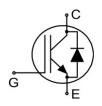
#### **Applications**

- · Resonant converters
- · Uninterruptible power supplies
- · Welding converters

#### **Features**

- Maximum junction temperature T<sub>J max</sub> = 175°C
- Low saturation voltage V<sub>CEsat</sub> = 1.4 V at T<sub>J</sub> = 25°C V<sub>CEsat</sub> is a positive temperature coefficient, suitable for parallel applications









#### **Absolute Maximum Ratings**

Symbol	Parameter		Value max	Unit	Note
$V_{CE}$	Collector-emitter voltage (T <sub>J</sub> ≥ 25 °C)		650	V	
$V_{GE}$	Gate-emitter voltage	Gate-emitter voltage		V	
	DC collector current limited by T	T <sub>C</sub> = 25°C	80	Α	
IC	I <sub>C</sub> DC collector current, limited by T <sub>J max</sub>	T <sub>C</sub> = 100°C	50	Α	
I <sub>C,pulse</sub>	Pulsed collector current, t <sub>p</sub> limited by T <sub>J m</sub>	200	Α		
l <sub>E</sub>	Diada famusand assumant limited by T	T <sub>C</sub> = 25°C	80	Α	
IF	Diode forward current, limited by T <sub>J max</sub>	T <sub>C</sub> = 100°C	50	Α	
I <sub>F,pulse</sub>	Diode pulsed current, t <sub>p</sub> limited by T <sub>J max</sub>	Diode pulsed current, t <sub>p</sub> limited by T <sub>J max</sub>		Α	
P <sub>tot</sub>	Power Dissipation	T <sub>C</sub> = 25°C	403	W	F: 0
rtot	Power Dissipation	T <sub>C</sub> = 100°C	161	VV	Fig.8
TJ	Junction temperature range		-40 ~ 175	°C	
T <sub>STG</sub>	Storage temperature range		-40 ~ 150	°C	

#### **Thermal Resistance**

Symbol	Parameter	Value	Unit
R <sub>thJC</sub>	IGBT thermal resistance, junction-case	0.35	°C/W
R <sub>thJC</sub>	Diode thermal resistance, junction-case	1.57	°C/W
R <sub>thJA</sub>	Thermal resistance, junction-to-ambient	31.42	°C/W
T <sub>sold</sub>	Soldering temperature, wave soldering only allowed at leads	260	℃

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#### **Electrical Characteristics** (T<sub>J</sub>= 25°C unless otherwise noted)

Symbol	Parameter	Test C	onditions	Min	Тур	Max	Unit	Note
Statistic (	Characteristics							
V <sub>(BR)</sub> CES	Collector-emitter Breakdown Voltage	V <sub>GE</sub> =0V, I <sub>C</sub> =200μA		650	-	-	٧	
Ices	Collector Cut-off Current	V <sub>CE</sub> =650V, V <sub>GS</sub> =0V		-	-	50	μA	
I <sub>GES</sub>	Gate-emitter Leakage Current	V <sub>GE</sub> =±20V, V	GE=0V	-	-	±100	nA	
V <sub>GE(TH)</sub>	Gate Threshold Voltage	Vce=Vge, Ic=500µA		3.2	4.0	4.8	V	Fig.5
.,		V <sub>GE</sub> =15V	T <sub>J</sub> =25°C	-	1.4	1.75	V	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage		T <sub>J</sub> =150°C	-	1.8	-		Fig.4
Dynamic	Characteristics							
Cies	Input Capacitance		-	4146	-			
Coes	Output Capacitance	Vce=25V, Vc f=1MHz	GE=UV,	-	166	-	pF	Fig.6
Cres	Reverse Transfer Capacitance	1-11/11/12		-	9.7	-		
Switching	Parameters							
	Turn-on Delay Time		T <sub>J</sub> =25°C	-	20.5	-	ns	Fig 20
$t_{d(on)}$			T <sub>J</sub> =150°C	-	20	-	ns	Fig.23
			TJ=25°C	-	29.8	-	ns	
t <sub>r</sub>	Rise Time		T <sub>J</sub> =150°C	-	31	-	ns	Fig.23
			TJ=25°C	-	155.4	-	ns	
$t_{d(off)}$	Turn-off Delay Time	V <sub>CE</sub> =400V, T <sub>J</sub> =150°C	-	184.2	-	ns	Fig.23	
		$I_{DC}$ =50A, R <sub>G</sub> =8 $\Omega$ , V <sub>GE</sub> =0/+15V	TJ=25°C	-	52	-	ns	
t <sub>f</sub>	Fall Time	VGE-0/ · 10 V	T <sub>J</sub> =150°C	-	90	-	ns	Fig.23
			TJ=25°C	-	1.3	-	mJ	
Eon	Turn-on Switching Energy		T <sub>J</sub> =150°C	-	1.44	-	mJ	Fig.24
			TJ=25°C	-	0.75	-	mJ	
E <sub>off</sub>	Turn-off Switching Energy		T <sub>J</sub> =150°C	_	1.13	-	mJ	Fig.24
T <sub>rr</sub>	Diode Reverse Recovery Time			_	73.8	-	ns	
	·	$V_R$ =400V, $R_G$ =3.3 $\Omega$ , $I_F$ =50A, di/dt=400A/ $\mu$ s						
Q <sub>rr</sub>	Diode Reverse Recovery Charge			-	0.95	-	uC	
I <sub>rrm</sub>	Diode Peak Reverse Recovery Current			-	19	-	Α	
	•							

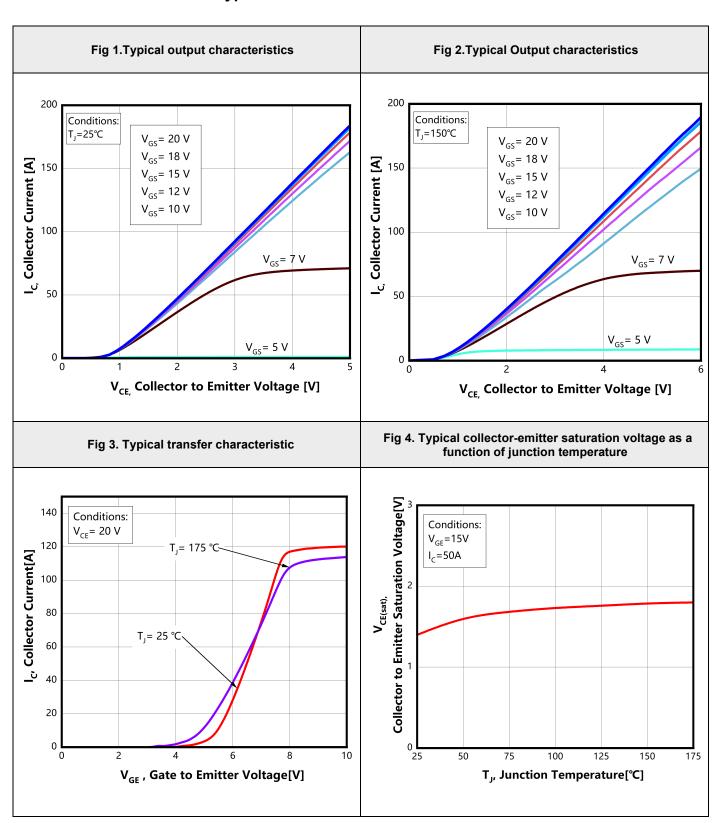
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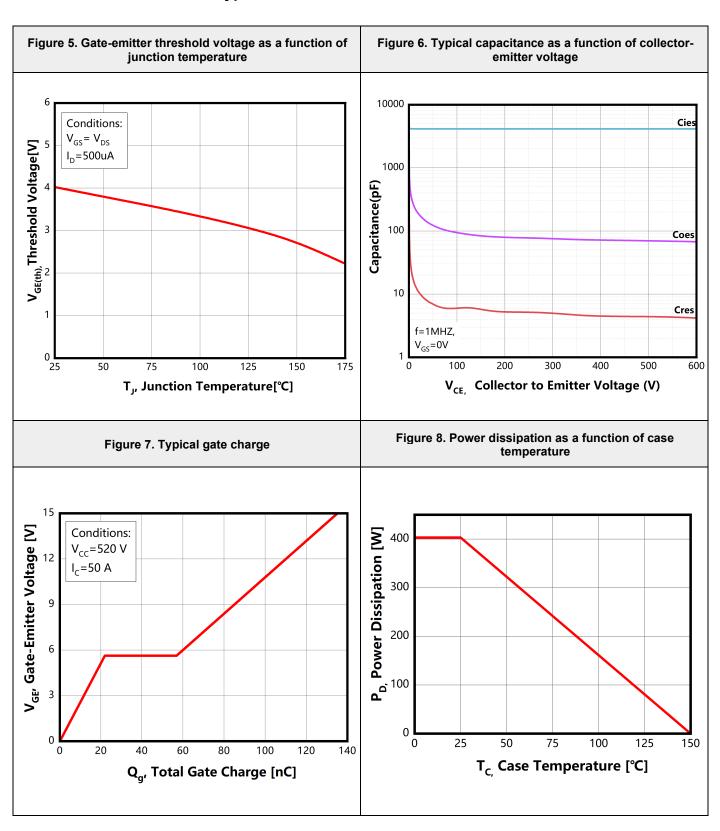
#### **Gate Charge Characteristics**

Qg	Gate Charge Total	V <sub>CC</sub> =520V, I <sub>C</sub> =50A V <sub>GE</sub> =0 to 15V	-	135	-		
$Q_{gc}$	Gate-emitter charge		-	22	-	nC	Fig.7
Q <sub>ge</sub>	Gate-collector charge		-	35	-		

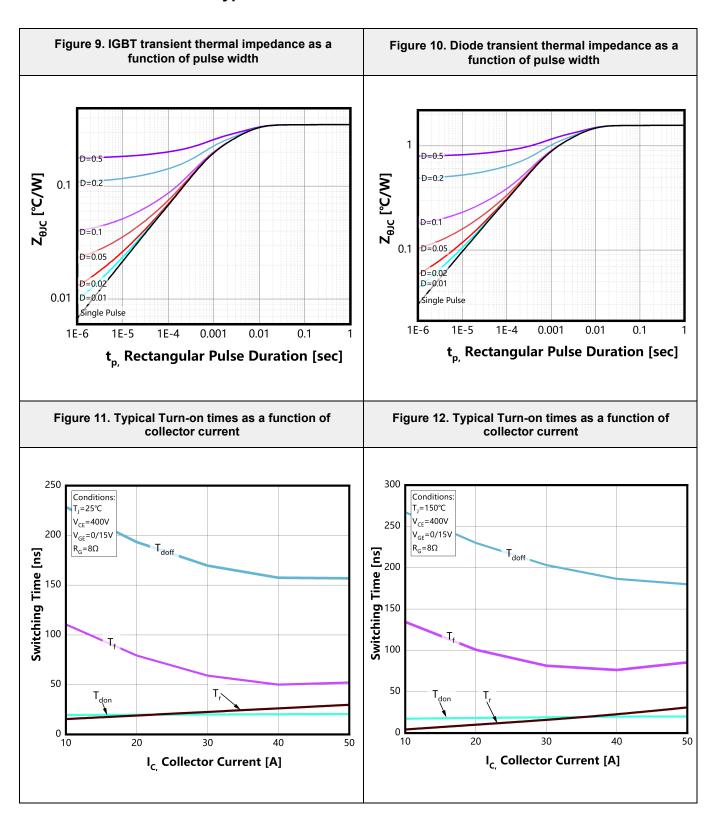




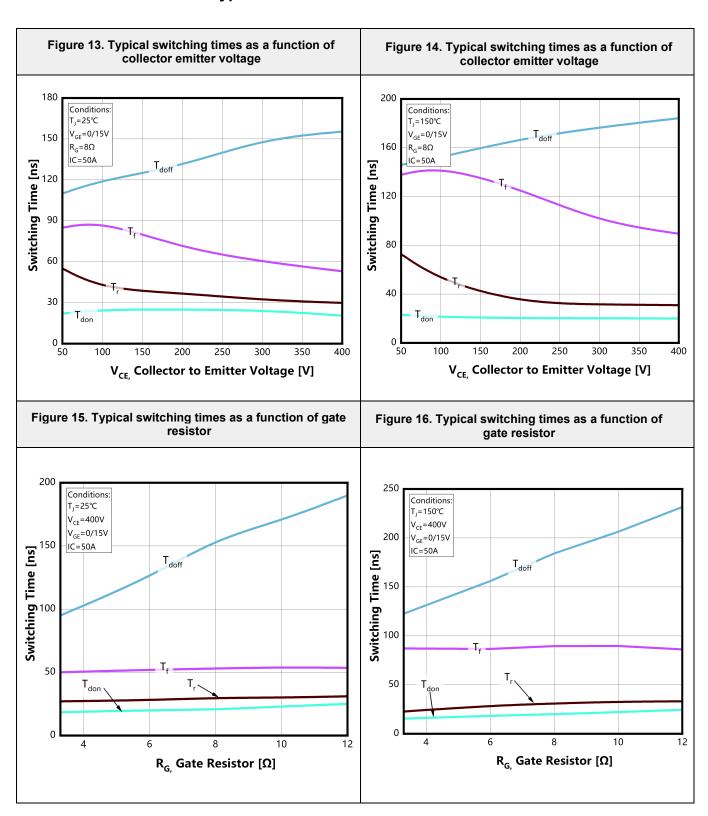




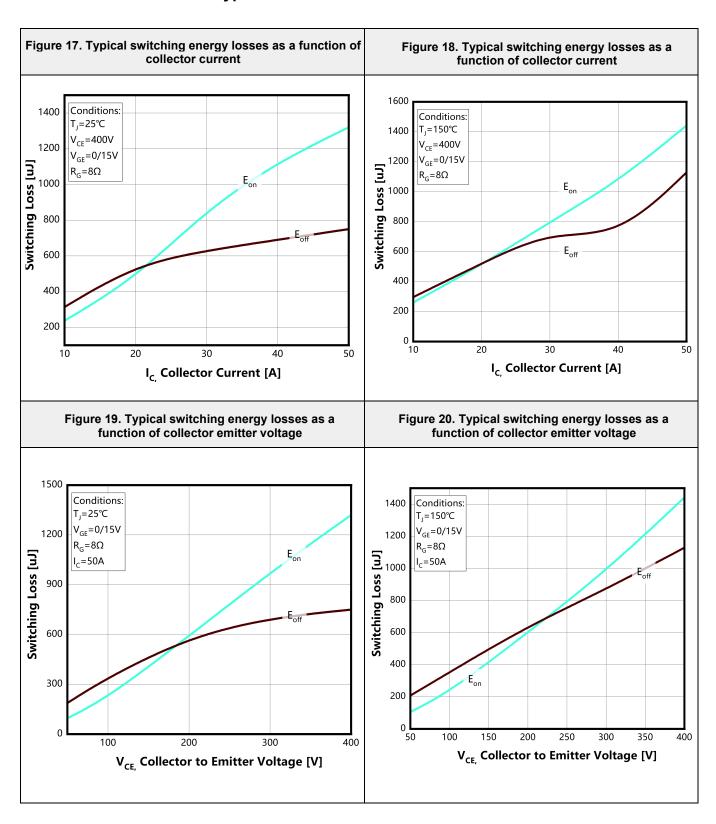




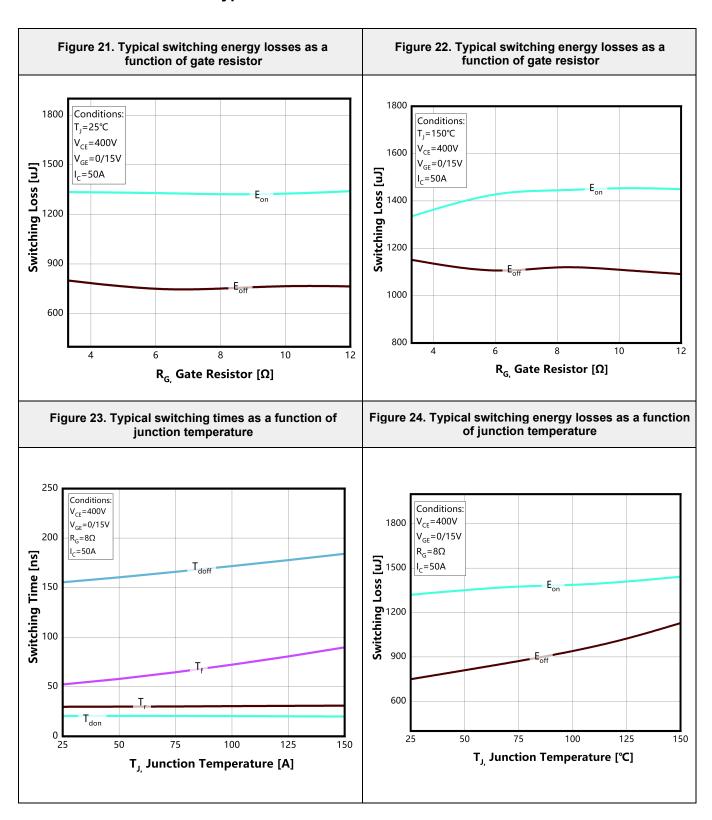




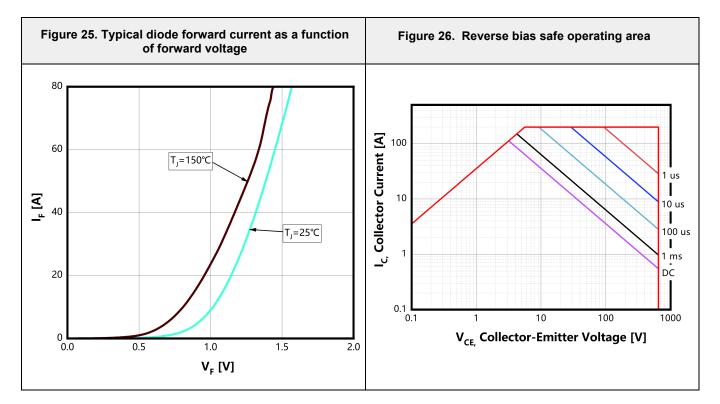










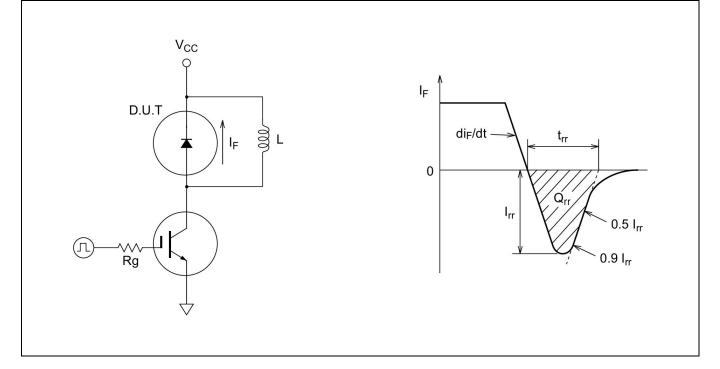




Diode clamp

Diode

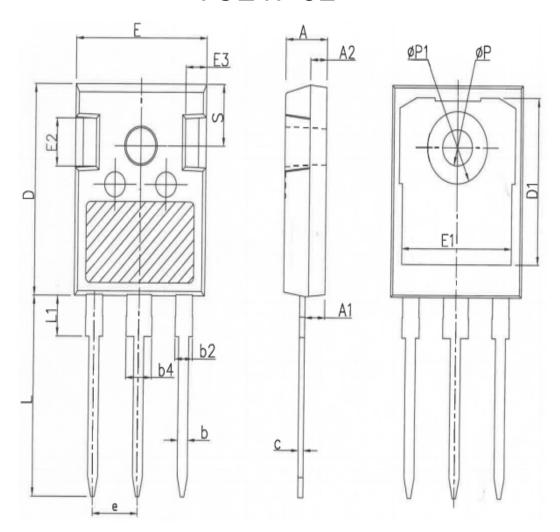
Figure 28. Reverse recovery time test circuit and waveforms





## **Package Outlines**

# TO247-3L



	COMMON	DIMENSI	ONS		
SYMBOL	tun				
SINDOL	MIN	NOM	MAX		
A	4.80	5.00	5, 20		
A1	2.21	2.41	2.59		
A2	1.85	2.00	2. 15		
b	1.11	1.21	1.36		
b2	1.91	2.01	2. 21		
b4	2.91	3.01	3. 21		
С	0.51	0.61	0.75		
D	20.70	21.00	21.30		
DI	16. 25	16.55	16.85		
E	15. 50	15.80	16.10		
E1	13.00	13.30	13.60		
E2	4.80	5.00	5. 20		
E3	2.30	2.50	2.70		
e		5. 44BSC			
L	19.62	19.92	20. 22		
L1	-	-	4. 30		
ФР	3.40	3.60	3.80		
ФР1	-	-	7.30		
S	100	6. 15BSC			

<sup>\*</sup> Dimensions in millimeters



#### Package Marking and Ordering Information

Part Number	Top Marking	Top Marking Package Package		Quantity
BKW65N050HS1	BKW65N050HS1	TO247-3L	Tube	50 units

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