

General Description :

XCH4N65N the silicon N-channel Enhanced VDMOSFETS, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package form is TO-251, which accords with the RoHS standard.

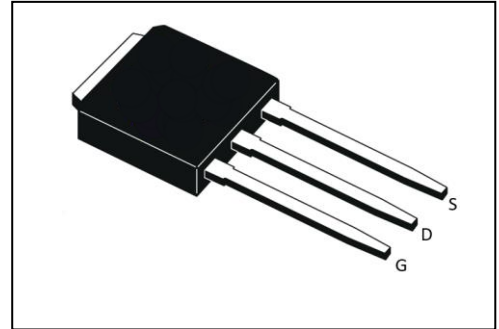
Features :

- Fast Switching
- Low Gate Charge and Rdson
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test

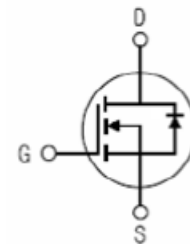
Applications :

- LED Lighting
- Charger
- Standby Power

V_{DSS}	800	V
I_D	4	A
$P_D(T_C=25^{\circ}C)$	45	W
$R_{DS(ON),TYP.}$	3.0	Ω



Inner Equivalent Principium Chart



Absolute (Tc=25°C unless otherwise specified) :

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-to-Source Voltage	800	V
I_D	Continuous Drain Current	4	A
	Continuous Drain Current $T_C=100^{\circ}C$	3.2	A
I_{DM}^{a1}	Pulsed Drain Current	16	A
V_{GS}	Gate-to-Source Voltage	± 30	V
E_{AS}^{a2}	Single Pulse Avalanche Energy	300	mJ
E_{AR}^{a1}	Avalanche Energy ,Repetitive	26	mJ
I_{AR}^{a1}	Avalanche Current	2.1	A
dv/dt^{a3}	Peak Diode Recovery dv/dt	5.0	V/ns
P_D	Power Dissipation	45	W
	Derating Factor above 25°C	0.36	W/°C
T_J, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	°C
T_L	Maximum Temperature for Soldering	300	°C

Caution Stresses greater than those in the "Absolute Maximum Ratings" may cause permanent damage to the device

Electrical Characteristics (Tc= 25°C unless otherwise specified):

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{DS}	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	800	--	--	V
$\Delta BV_{DS}/\Delta T_J$	Bvdss Temperature Coefficient	$I_D=250\mu A, \text{Reference } 25^\circ C$	--	0.65	--	V/°C
I_{DSS}	Drain to Source Leakage Current	$V_{DS}=800V, V_{GS}=0V, T_a=25^\circ C$	--	--	25	μA
		$V_{DS}=320V, V_{GS}=0V, T_a=125^\circ C$	--	--	250	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=+30V$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-30V$	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10V, I_D=2.4A$	--	3.0	3.6	Ω
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	--	4.0	V
g_{fs}	Forward Trans conductance	$V_{DS}=15V, I_D=2.4A$	--	7.5	--	S
Pulse width<380 μs ; duty cycle<2%.						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=25V$ $f=1.0MHz$	--	1350	--	pF
C_{oss}	Output Capacitance		--	100	--	
C_{rss}	Reverse Transfer Capacitance		--	15	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	$I_D=4A, V_{DD}=400V$ $V_{GS}=10V, R_g=12\Omega$	--	16	--	ns
t_r	Rise Time		--	18	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	50	--	
t_f	Fall Time		--	25	--	
Q_g	Total Gate Charge	$I_D=4A, V_{DD}=400V$ $V_{GS}=10V$	--	35	--	nC
Q_{gs}	Gate to Source Charge		--	10	--	
Q_{gd}	Gate to Drain ("Miller") Charge		--	16	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I_{SD}	Continuous Source Current (Body Diode)		--	--	4	A
I_{SM}	Maximum Pulsed Current (Body Diode)		--	--	16	A
V_{SD}	Diode Forward Voltage	$I_S=4A, V_{GS}=0V$	--	--	1.5	V
t_{rr}	Reverse Recovery Time	$I_S=4A, T_J=25^\circ C$	--	820	--	ns
Q_{rr}	Reverse Recovery Charge	$dI_F/dt=100A/\mu s, V_{GS}=0V$	--	4.95	--	μC

a1: Repetitive rating; pulse width limited by maximum junction temperature

a2: $L=10mH, I_D=7.7A$, Start $T_J=25^\circ C$

a3: $I_{SD}=4A, di/dt \leq 100A/\mu s, V_{DD} \leq BV_{DS}$, Start $T_J=25^\circ C$

Characteristics Curve:

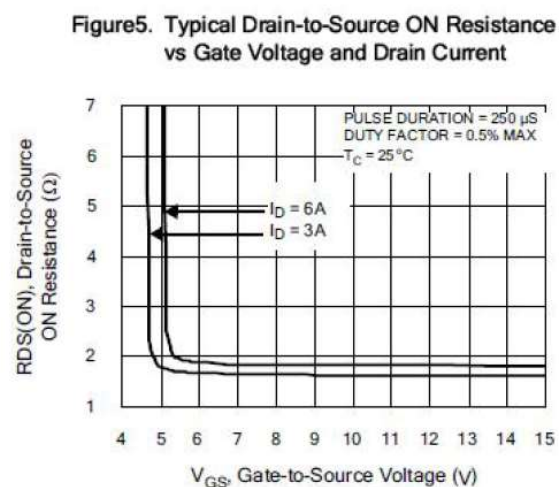
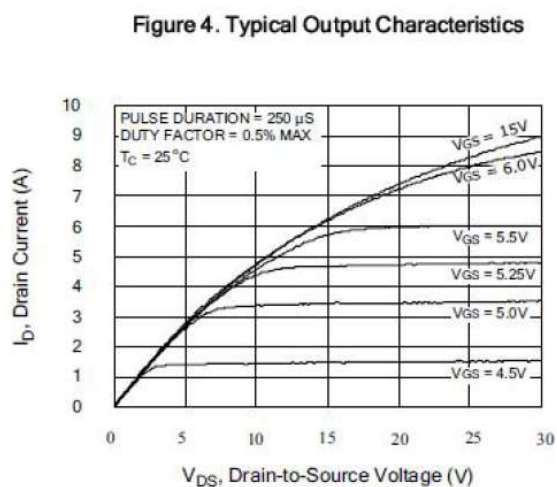
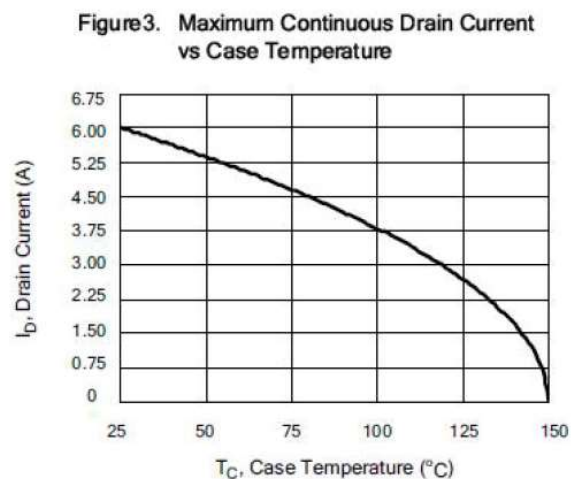
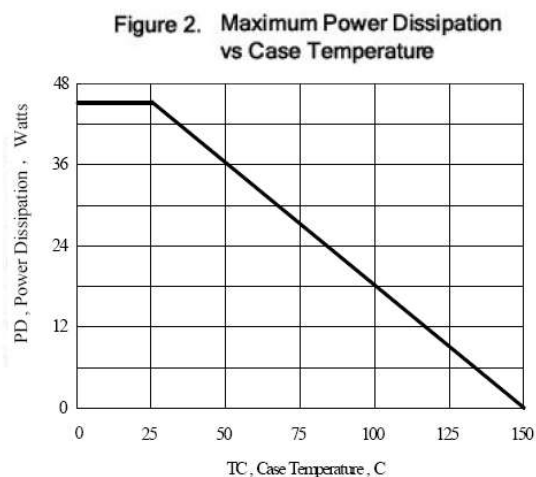
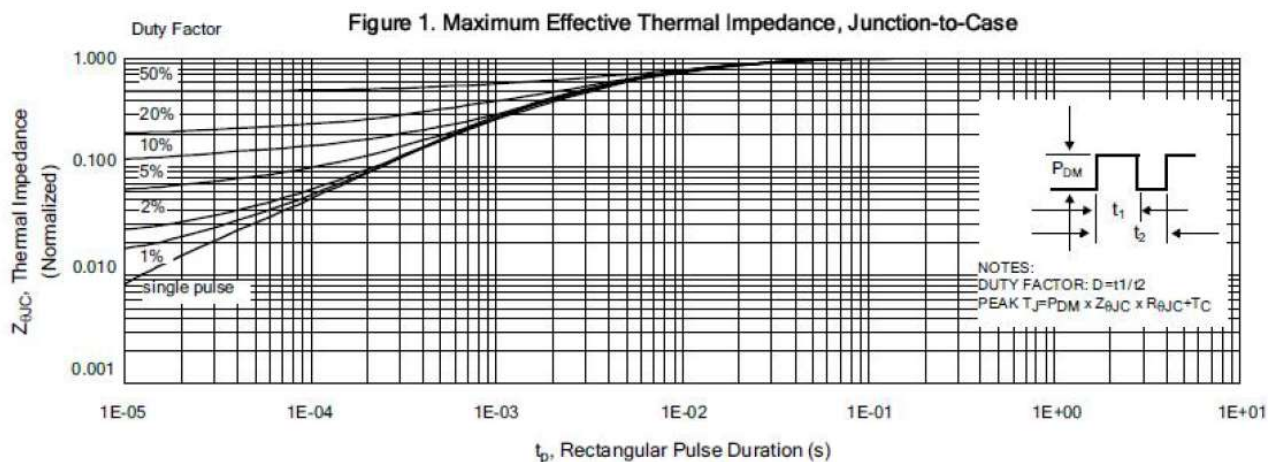


Figure 6. Maximum Peak Current Capability

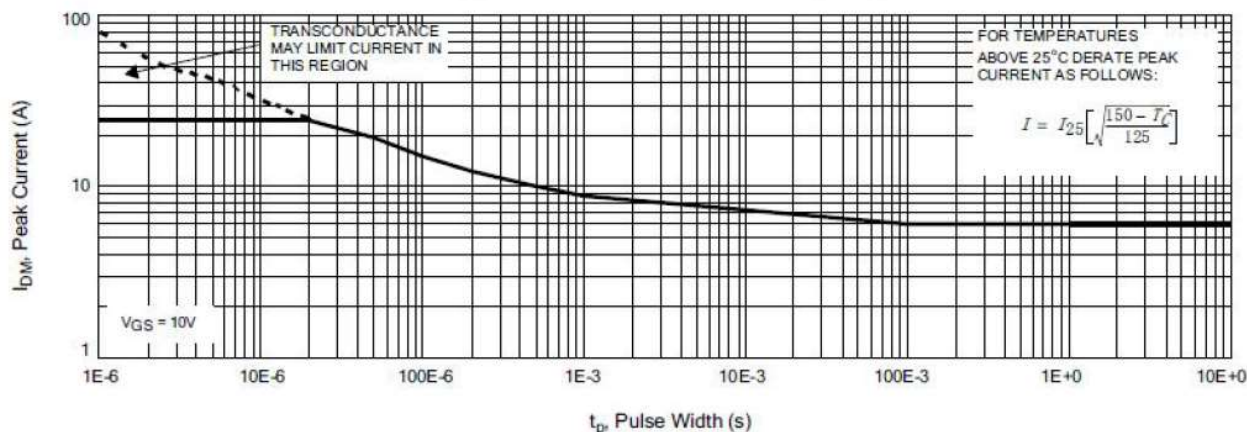


Figure 7. Typical Transfer Characteristics

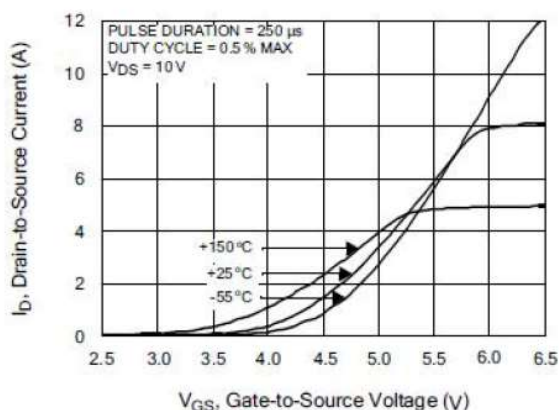


Figure 8. Unclamped Inductive Switching Capability

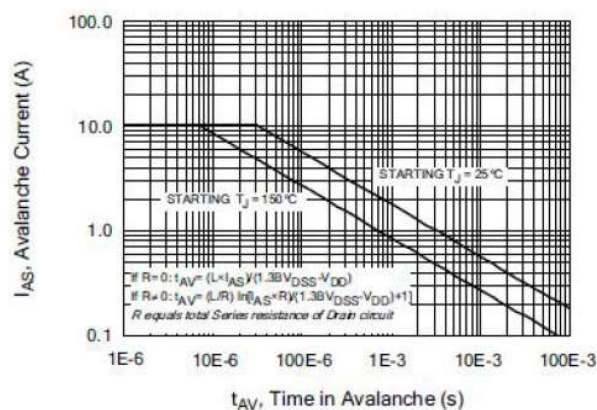


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

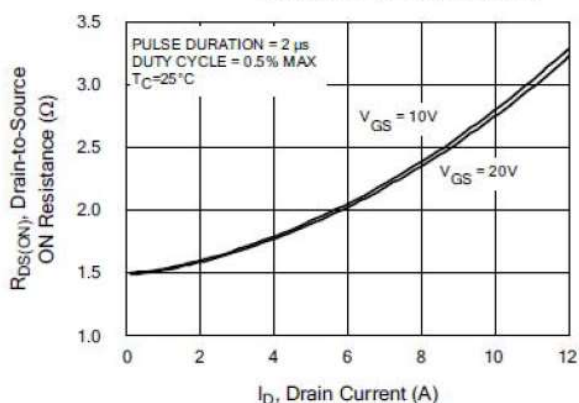


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature

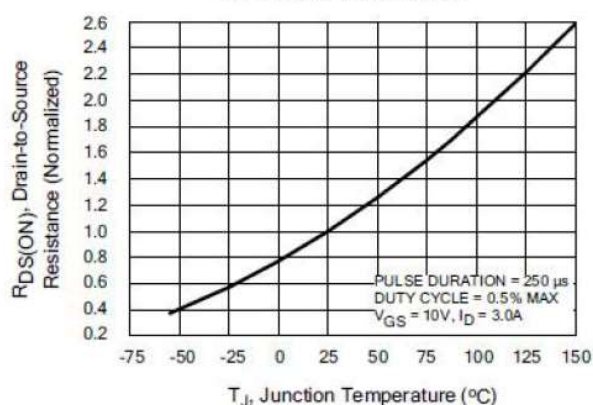


Figure 11. Typical Breakdown Voltage vs Junction Temperature

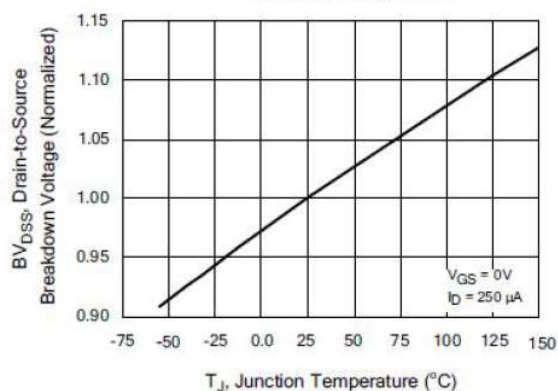


Figure 12. Typical Threshold Voltage vs Junction Temperature

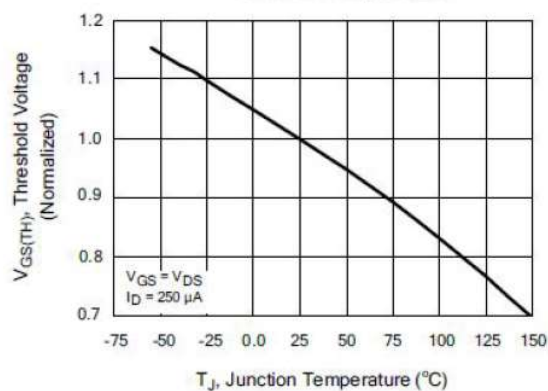


Figure 13. Maximum Forward Bias Safe Operating Area

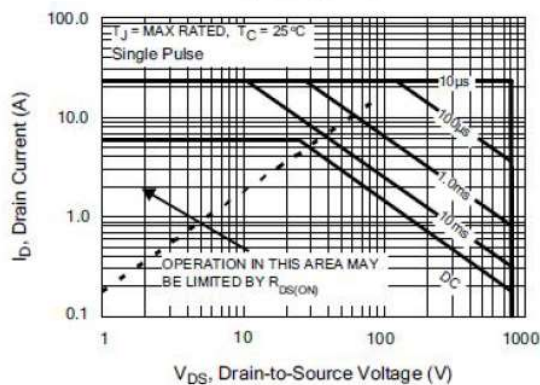


Figure 14. Typical Capacitance vs

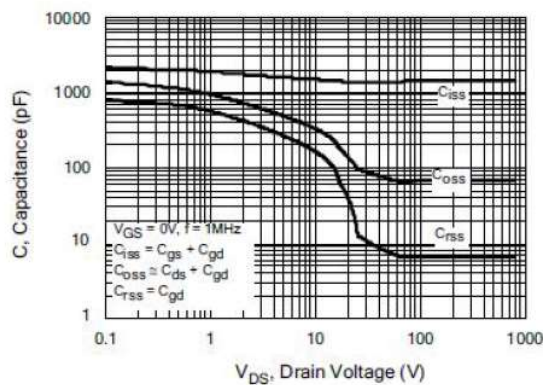


Figure 15. Typical Gate Charge vs Gate-to-Source Voltage

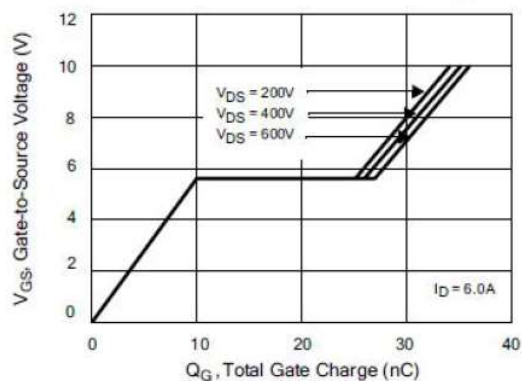


Figure 16. Typical Body Diode Transfer Characteristics

