CRYSTAL OSCILLATOR (SPXO) **OUTPUT: LV-PECL, LVDS**

SG3225EEN / VEN SG5032EEN / VEN

SG7050EEN / VEN

•Frequency range : 25 MHz to 500 MHz Supply voltage 2.5 V Typ. / 3.3 V Typ. Output LV-PECL or LVDS Function Output enable (OE)

 Phase jitter 50 fs Typ. (fo = 156.25 MHz, LV-PECL)

Operating temperature: -40 °C to +105 °C



SG3225VEN (3.2 × 2.5 × 1.05 mm)



Product Number SG3225EEN: X1G005221xxxx00 (fo ≤ 200 MHz) X1G005511xxxx00 (fo > 200 MHz) SG5032EEN: X1G005531xxxx00

SG7050EEN: X1G005131xxxx00 (fo ≤ 200 MHz) X1G005551xxxx00 (fo > 200 MHz)

SG3225VEN: X1G005351xxxx00 (fo ≤ 200 MHz) X1G005521xxxx00 (fo ≤ 200 MHz) SG5032VEN: X1G005541xxxx00 SG7050VEN: X1G005331xxxx00 (fo ≤ 200 MHz)

X1G005561xxxx00 (fo > 200 MHz)





SG5032VEN (5.0 × 3.2 × 1.3 mm)

Specifications (characteristics)

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	Symbol	Specifications				
Item		LV-PECL LVDS		Conditions / Remarks		
item	Symbol	SG3225EEN / SG5032EEN	SG3225VEN / SG5032VEN	Conditions / Remarks		
		/ SG7050EEN / SG7050VEN				
Output fraguancy range	fo	25 MHz to 500 MHz		Except for SG5032EEN / SG5032VEN	Please contact us for available	
Output frequency range	10	200.1 MHz	to 500 MHz	SG5032EEN / SG5032VEN	frequencies.	
Supply voltage	Vcc	D: 2.5 V ± 0.125 V,	C: 3.3 V ± 0.165 V			
Storage temperature range	T_stg	-55 °C to	+125 °C			
Operating temperature range	T_use	G: -40 °C to +85 °C, H: -40 °C to +105 °C				
		D: ±35 × 10-6 May		Includes initial frequency tolerance, temp		
Frequency tolerance	f tol	1, 150	40 6 M	supply voltage change and 5 years aging		
	_	J: ±50 × 10 ⁻⁶ Max. L: ±100 × 10 ⁻⁶ Max.		Includes initial frequency tolerance, temperature variation, figure *		
Current consumption		60 mA Max.	10° Max. 25 mA Max.	supply voltage change and 10 years aging (+25 °C)		
Current consumption	lcc	• • • • • • • • • • • • • • • • • • • •		$OE = V_{CC}$, L_ECL = 50 Ω or L_LVDS = 100 Ω		
Disable current	I_dis SYM	25 mA Max.	15 mA Max.	OE = GND		
Symmetry		45 % to	0 55 %	At output crossing point		
Output voltage (LV-PECL)	Voh	Vcc - 1.1 V Min.	П	DC characteristics		
3 (- 7	V _{OL}	V _{CC} - 1.5 V Max.	-	D'''		
	VoD	_	250 mV to 450 mV	Differential output voltage, V _{OD1} , V _{OD2}		
Output voltage (LVDS)	dV _{OD}	_	50 mV Max.	$dV_{OD} = V_{OD1} - V_{OD2} $ DC characteristi		
	Vos	_	1.15 V to 1.35 V	Offset voltage, Vos1, Vos2		
	dVos	-	50 mV Max.	$dV_{OS} = V_{OS1} - V_{OS2} $		
Output load condition	L_ECL	50 Ω	_	Terminated to V _{CC} - 2.0 V		
L_LVDS -		100 Ω	Connected between OUT to OUT			
Input voltage	V _{IH}	70 % V _{CC} Min.		OE terminal		
pat remage	V _{IL}	30 % V _{CC} Max.				
Rise/Fall times	tr / tf	0.3 ns Max.	0.3 ns Max.		en 20 % and 80 % of (VoH - VoL) en 20 % and 80 % of Differential	
		0.35 ns Max.		All other Output peak to peak voltage		
Startup time	t_str	10 ms	Max.	Time at minimum supply voltage to be 0 s		

Phase Jitter

Product Name	100 MHz	125 MHz	156.25 MHz	200 MHz	312.5 MHz	491.52 MHz	Conditions
SG3225EEN / SG5032EEN / SG7050EEN	75 fs Typ.	60 fs Typ.	50 fs Typ.	40 fs Typ.	30 fs Typ.	20 fs Typ.	Offset frequency:
SG3225VEN / SG5032VEN / SG7050VEN	90 fs Tvp.	70 fs Tvp.	60 fs Tvp.	50 fs Tvp.	40 fs Tvp.	30 fs Tvp.	12 kHz to 20 MHz

Product Name (Standard form) SG3225 EEN 156.250000MHz C D G A 3

(56: Unavailable code DH, DG and JH at fo > 200 MHz, Refer to figure *1)

①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage 5Frequency tolerance 6Operating temperature ①Internal identification code("A" is default)

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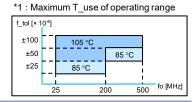
OUT

С	3.3 V Typ.		
D	2.5 V Typ.		

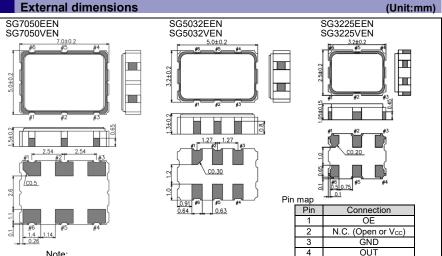
⑤Frequency tolerance			
D	±25 × 10-6		
J	±50 × 10-6		
L	±100 × 10-6		

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6 Operating temperature		
G	-40 to +85 °C	
Н	-40 to +105 °C	



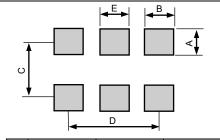
External dimensions



OE pin = HIGH or "Open": Specified frequency output.

OE pin = LOW: Output is high impedance

Footprint (Recommended) (Unit:mm)



	SG3225EEN	SG5032EEN	SG7050EEN
	SG3225VEN	SG5032VEN	SG7050VEN
Α	1.05	1.60	2.00
В	0.92	0.89	1.80
С	1.85	2.60	4.20
D	2.58	2.54	5.08
Ε	0.80	0.89	1.80

In order to achieve optimum jitter performance, it is recommended that 0.1 µF and 10 µF bypass capacitors should be connected between V_{CC} and GND and placed as close to the V_{CC} pin as possible

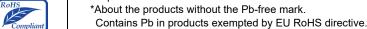
Explanation of the mark that are using it for the catalog



▶Pb free.



▶ Complies with EU RoHS directive.



(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive general equipment.



▶ Designed for automotive applications related to driving and safety.

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