



钛迪半导体  
Tudi Semiconductor

## Product Specification

TUDI-SN65HVD37

Low-power, 3.3V supply full-duplex RS-485 driver/  
receiver

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## Features

- Low current standby mode
- Typical value for working static current
- High receiver hysteresis to avoid noise interference
- 1/8 unit load (up 256 nodes on the bus)
- ESD protection for bus pins exceeding 15 kV HBM
- Optimized driver output slew rate for signal transmission speeds up 15 Mbps
- No waveform distortion power-up and power-down protection for hot-insertion applications
- 5V-tolerant logic inputs
- Fault-safe bus idle, open, and short circuit
- Driver current limiting and thermal shutdown
- Full compliance with all TIA-485-A specifications

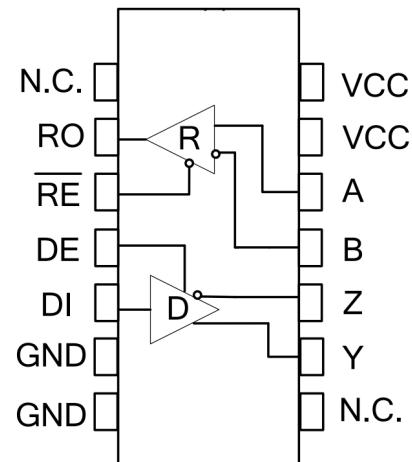


Figure 1 Pin diagram

## Description

The SN65HVD37 combines a rugged differential driver and a receiver with high noise immunity to meet the needs of demanding industrial applications. The outputs of the driver and the differential inputs of the receiver are separate pins, thus forming a bus port for full-duplex (quad-wire) communication. The driver and receiver are independently enabled and feature a wide common-mode voltage range, making this device suitable for multi-point applications running over long cable runs. The SN65HVD37 is rated for operation over the temperature range of -40C to 85C.

## Applications

- Telecommunications equipment
- Industrial automation
- Process automation
- Building automation
- Point of sale (POS) terminal



## Pin description

Pin number	Pin name	Pin function
1	NC	No internal connection, no need to connect;
2	RO	Receiver output. When /RE is low, if A-B≥-10mV, RO output is high; if AB≤-200mV, RO output is low.
3	/RE	Receiver output enable control. When /RE is low, the receiver output is enabled and RO is valid; when /RE is high, the receiver is disabled and RO is high impedance; /RE is high and DE is low, the device enters low-power off mode.
4	DE	Driver output enable control. The driver output is active when DE is high, and the output is high-impedance when DE is low; / is high and DE is low, the device enters low-power shutdown mode.
5	DI	DI driver input. A low on DI with DE high causes the driver's A output to be low and B output to be high; a high DI will cause the A output to be high and the B output to be low.
6	GND	grounding
7	GND	grounding
8	NC	No internal connection, no need to connect;
9	Y	Drivers in-phase output
10	Z	Inverting output of the driver
11	B	Inverting input of the receiver
12	A	Receiver in-phase input terminal
13	VCC	This pin can be connected to the power supply (3V ≤ VCC ≤ 5.5V) or can be left unconnected
14	VCC	Power supply: 3V ≤ VCC ≤ 5.5V

## Extreme parameter

Parameter	Symbol	Unit	size
Continuous power consumption	SOP14	mW	600
	DIP14	mW	700
Power supply voltage	VCC	V	+7
working temperature range		°C	-40~125
Storage temperature range		°C	-60~150
Welding temperature range		°C	300
Control port voltage	DI	V	-0.3~VCC+0.3
Bus side input voltage	A、B	V	-8~13
Receiver output voltage	RO	V	-0.3~VCC+0.3

The maximum limit parameters are values beyond which the device can be damaged in an irreversible manner. Operation of the device under these conditions is not intended to be normal and may affect the reliability of the device if operated continuously at the maximum rated limit. All voltages are referenced to ground.



Parameter	Symbol	Test condition	Minimum	Typical case	Maximum	Unit
Supply current						
Supply current	Icc1	/RE=0V,DE=0V		220	400	uA
	Icc2	/RE=VCC,DE=		240	400	uA
Turn off the current	ISHDN	RE=VCC,DE=0V		0.5	10	uA
ESD protect						
A, B, Y, Z		Human body model(HBM)		±15		KV
Other ports		Human body model(HBM)		±6		KV
The DC electrical characteristics of the receiver						
Input current (A,B)	IN2	VCC=0 or 3.3 V VIN=12 V			125	uA
		VCC=0 or 3.3 V VIN =-7V	-100			uA
Forward input threshold voltage	VIT+	-7V≤VCM≤12 V			-10	mV
Reverse input threshold voltage	VIT-	-7V≤VCM≤12 V	-200			mV
Input the hysteresis voltage	Vhys	-7V≤VCM≤12 V	10	30		mV
High level output voltage	VoH	IoUT=-4mA, VID =+200 mV	VCC-1.5			V
Low-level output voltage	VoL	IoUT=+4mA, VID=-200 mV			0.4	V
Three-state input leakage current	IoZR	0.4V<Vo<2.4V			±1	uA
Receiver input resistance	RN	-7V≤VCM≤12 V	96			kΩ
Receiver short-circuit current	IosR	0V≤Vo≤VCC	±7		±95	mA

(If not otherwise, VCC=3V~5.5V, Temp = TMIN ~ TMAX, typical value at Temp = 25 ) NOTE 1: VO D and? VOC is the change in VOD and VOC amplitude caused when the DI state of the input signal changes, respectively.



Parameter	Symbol	Test condition	Minimum	Typical case	Maximum	Unit
The DC electrical characteristics of the drive device						
Drive differential output(no load)	VoD?		3		5.5	V
Drive differential output	VoD2	Graph 2,RL=27 Ω	1.5		VCC	V
		Graph 2,RL=50 Ω	2		VCC	
Changes in the output voltage amplitude (NOTE1)	△VoD	Graph 2,RL=27 Ω			0.2	V
Output common mode voltage	Voc	Graph 2,RL=27 Ω			3	V
Change in common mode output voltage amplitude(NOTE 1)	△Voc	Graph 2,RL=27 Ω			0.2	V
High-level input	VH	DI	2.0			V
Low level input	Vπ	DI			0.8	V
Logical input cur-rent	IN?	DI	-2		2	uA
Output short circuit current, short circuit to high	IosD?	Short-circuit to OV~12V	35		250	mA
Output short circuit current, short circuit to low	IosD2	Short-circuit to -7V~OV	-250		-35	mA
Drive switch characteristics						
Drive input to output propagation delay(low to high)	tDPLH	RDIFF=54Ω, CLi= CL?=100 pF (see Figure 3 and Figure 4)		15	35	ns
Drive input to output propagation delay(high to low)	tDPHL			15	35	ns
tDPLH-tDPHL	tsKEW1			7	10	ns
Up along time / down along time	tDR,tDF			10	25	ns
Amision to output high	tpZH	RL =1109,(see Figure 5,6)		20	90	ns
The enabling to output is low	tpZL			20	90	ns
Input low to no energy	tPLz	RL =110 Ω, (see Figure		20	80	ns
Input high to no energy	tPHZ			20	80	ns
Under off conditions,the output is high	tDSH	RL =1109,(see Figure 5,6)		500	900	ns
Under off conditions,enabling output low	tDSL	RL=1109,(see Figure 5,6)		500	900	ns



Parameter	Symbol	Test condition	Minimum	Typical case	Maximum	Unit
Receiver Switch characteristics						
Ento output high time	tRPZH	C=15 pF is shown in Figure		20	50	ns
From low output to energy-forbidden time	tpRLZ	For CL =15 pF, see Figure 7		20	45	ns
From high output to energy forbidden time	tPRHZ	For CL =15 pF, see Figure 7		20	45	ns
Enables high output time in the off state	tRPSH	For CL =15 pF, see Figure 7		200	1400	ns
Ento output low time in off state	tRPSL	For CL =15 pF, see Figure 7		200	1400	ns
Time in the off state	tsHDN	NOTE2	80		300	ns
Acceptor	tRPLH	See Figure 7 and Figure 8	20	60	90	ns
Input to output propagation latency is from low to high		VID 2.0V;rise and fall along time VID 15ns				
The receiver input to output propagation latency is obtained from high to low	tRPHL		20	60	90	ns
tRPLH-tRPHL	tsKEW2			7	10	ns
Ability to reach the output for a low time	tRPZL	For CL =15 pF, see Figure 7		20	50	ns

## Function table

Receiving function table			
control		input	output
/RE	DE	A-B	RO
0	X	≥-10mV	H
0	X	≤-200mV	L
0	X	Open/short circuit	H
1	X	X	Z
X: any level; Z: high impedance.			

Send function table				
control		input	output	
/RE	DE	DI	Y	Z
X	1	1	H	L
X	1	0	L	H
0	0	X	Z	Z
1	0	X	Z(shutdown)	
X: any level; Z: high impedance.				



## Test circuit

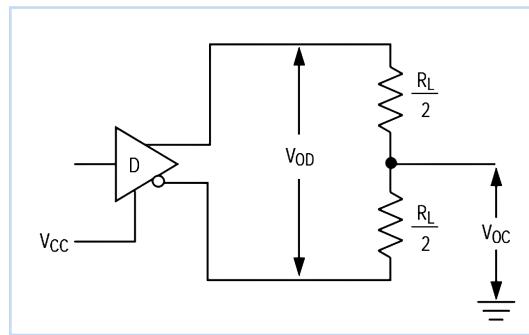


Figure 2: DC test load for the drive

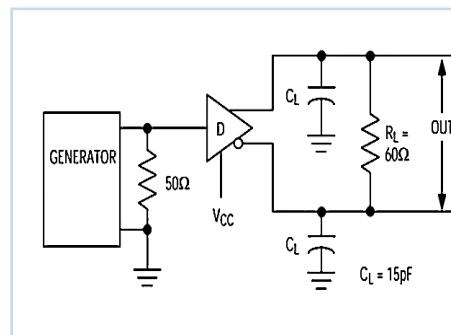


Figure 3 Drive-line Differential Delay and Transit Time

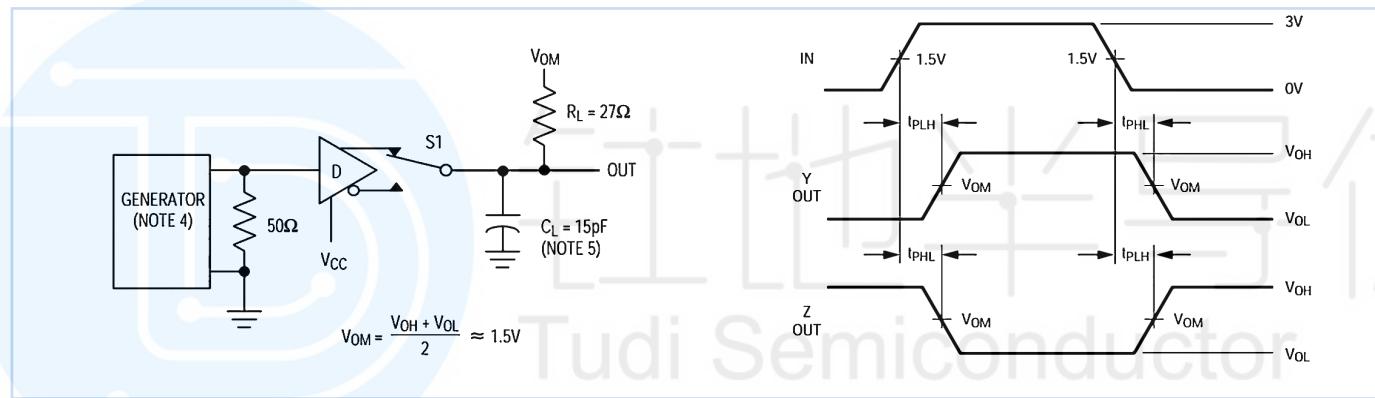


Figure 4 Drive propagation delay

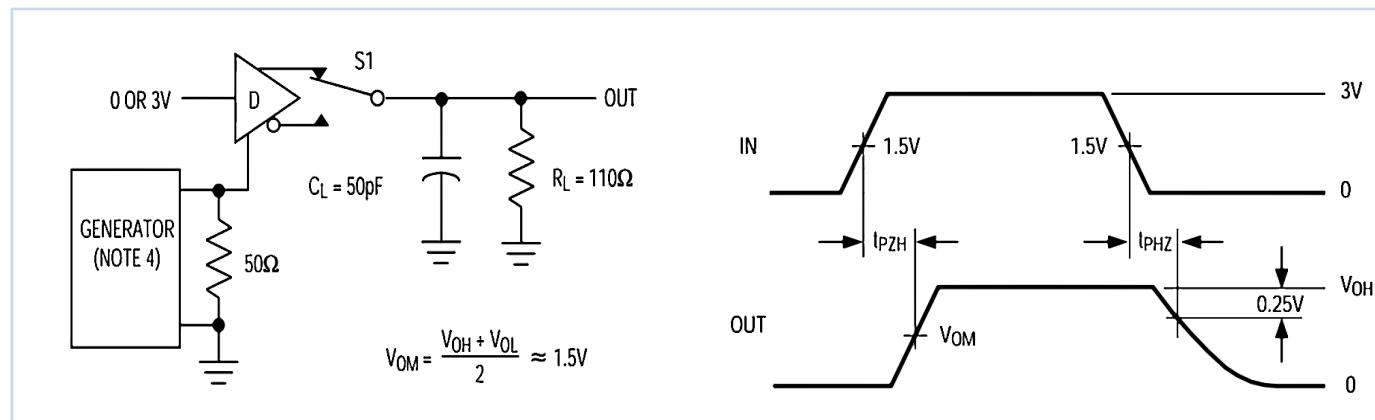


Figure 5 Drive enable and disable time

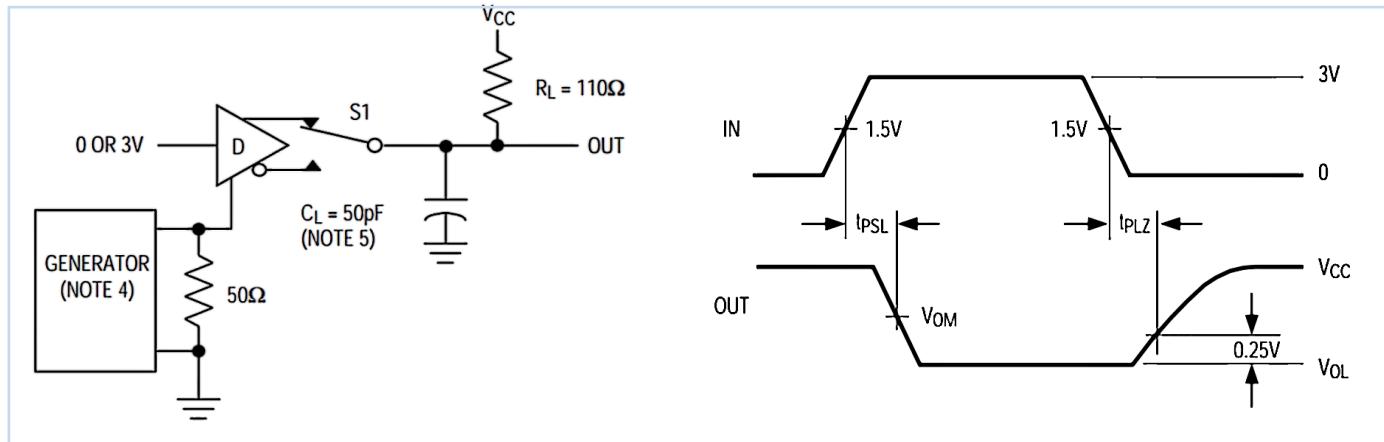


Figure 6 Drive enable and disable time

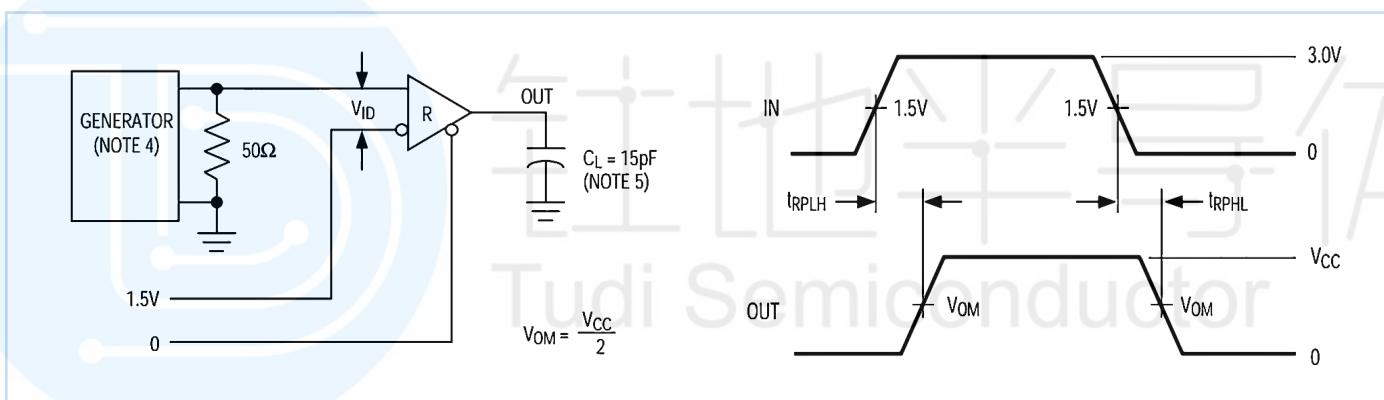
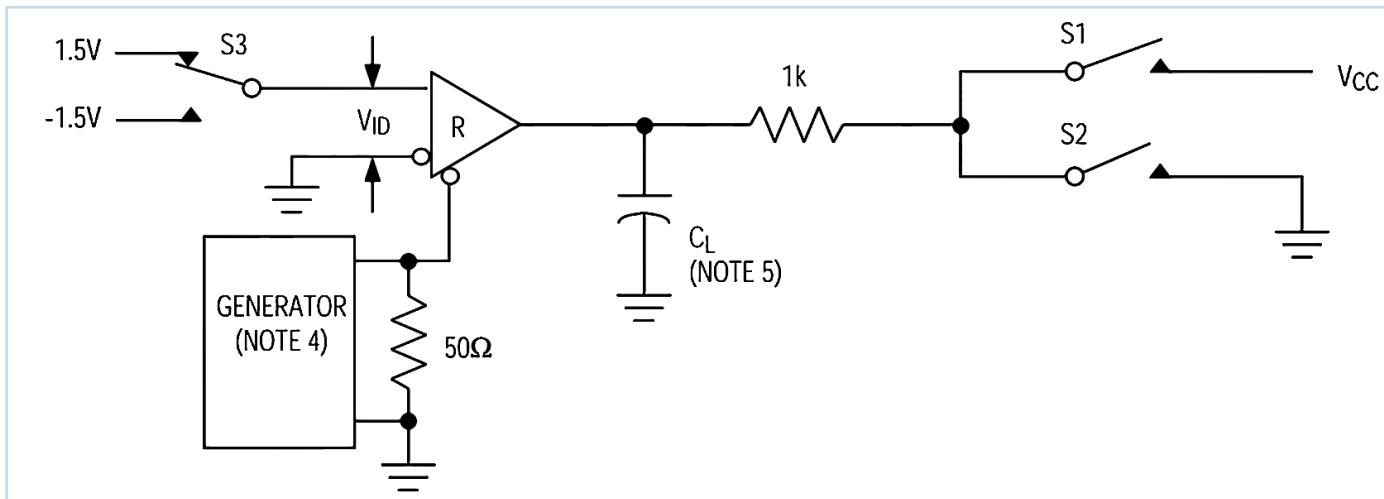


Figure 7: Receiver Propagation Delay Test Circuit



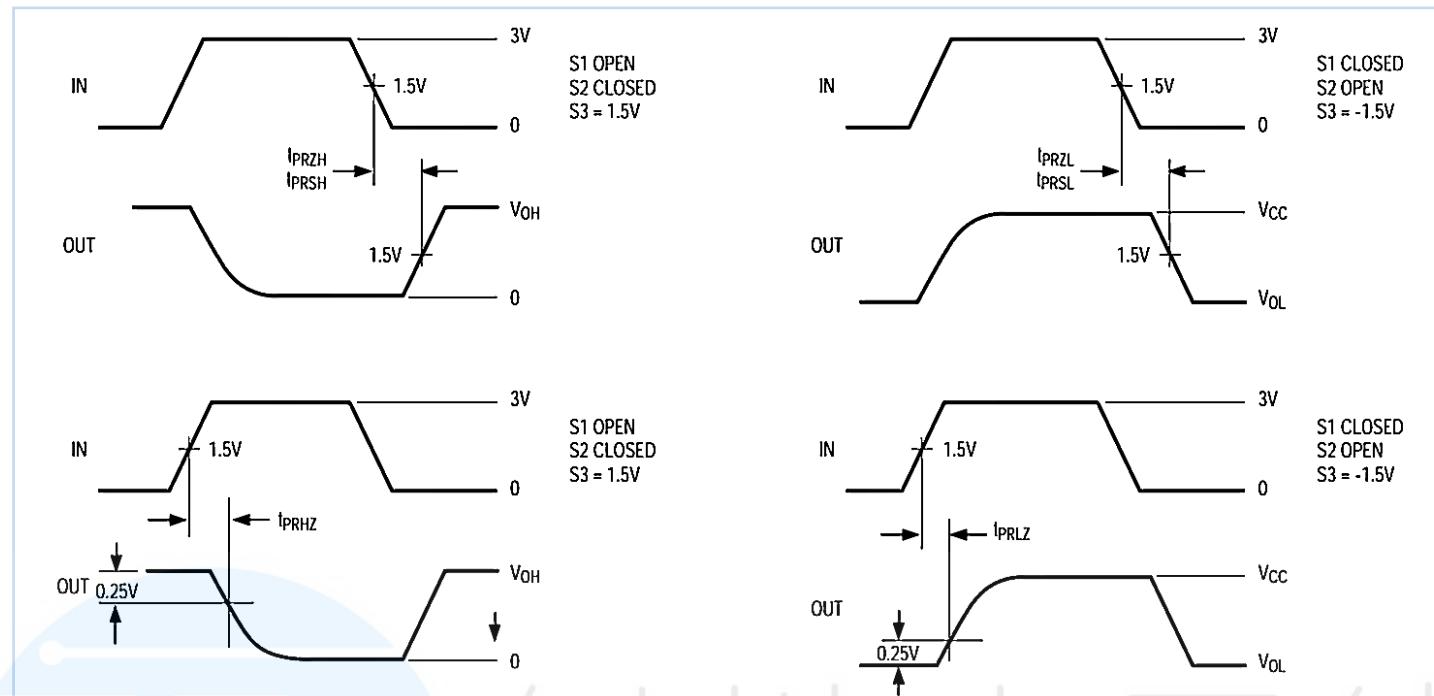


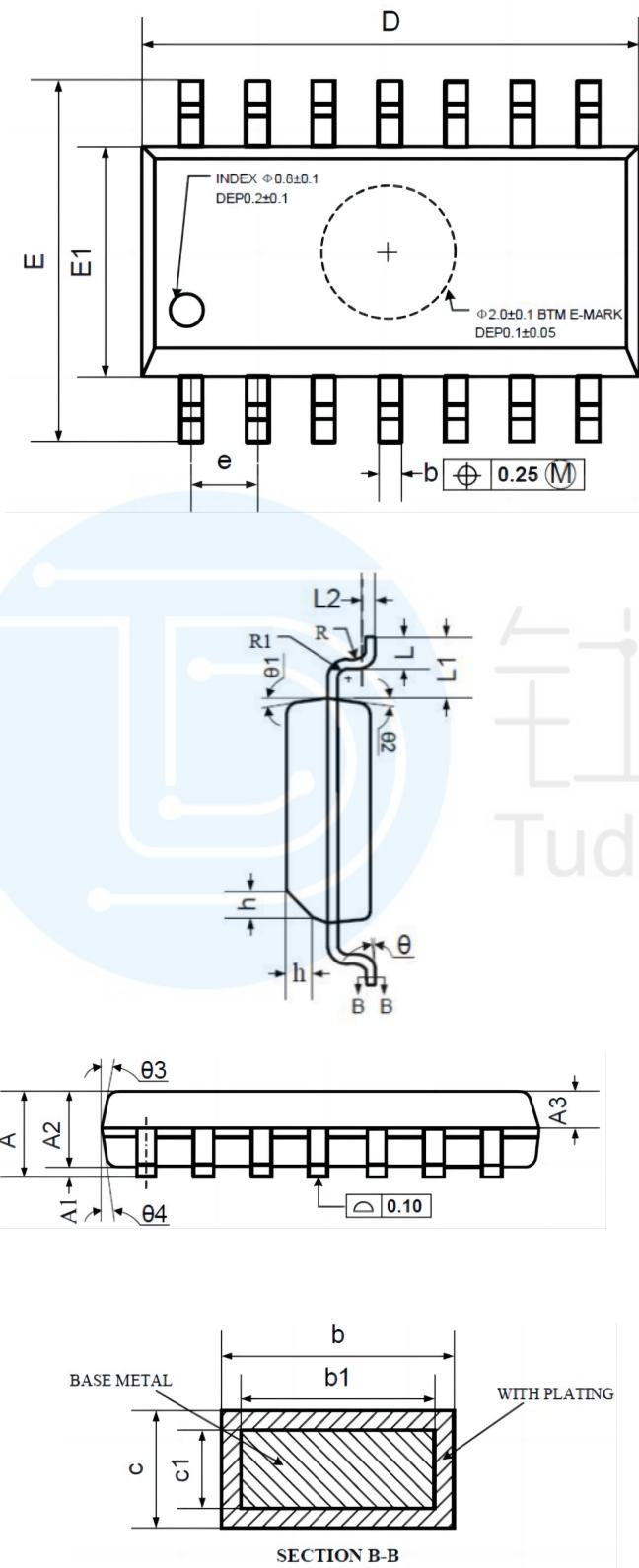
Figure 8 Receive enable and disable time

## Order information

Order Number	Package	Package Quantity	Marking On The park	Temperature
SN65HVD37DR-TUDI	SOP14	Tape,Reel,2500	HVD37	- 40°C to 85°C
SN65HVD37N-TUDI	DIP14	Tube,25,A box of 1000	65HVD37	



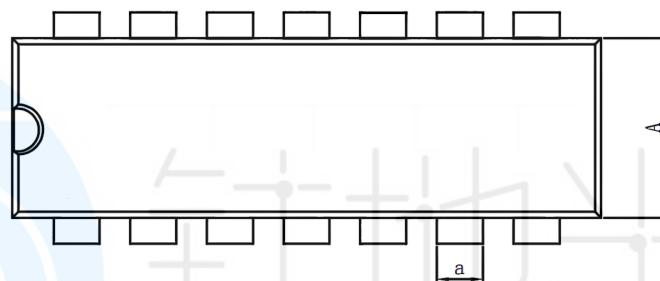
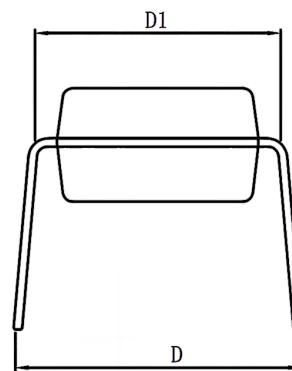
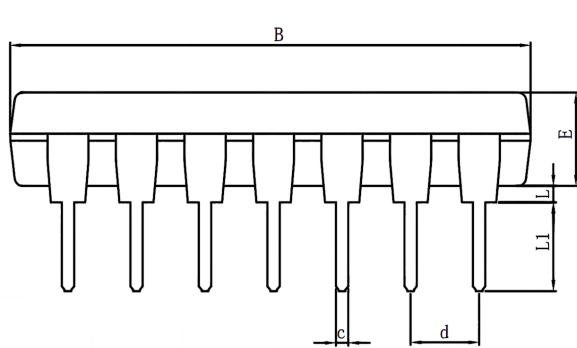
## Package SOP14



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	1.35	1.60	1.75
A1	0.10	0.15	0.25
A2	1.25	1.45	1.65
A3	0.55	0.65	0.75
b	0.36		0.49
b1	0.35	0.40	0.45
C	0.16		0.25
c1	0.15	0.20	0.25
D	8.53	8.63	8.73
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e		1.27 BSC	
L	0.45	0.60	0.80
L1		1.04 REF	
L2		0.25 BSC	
R	0.07		
R1	0.07		
h	0.30	0.40	0.50
$\theta$	$0^\circ$		$8^\circ$
$\theta_1$	$6^\circ$	$8^\circ$	$10^\circ$
$\theta_2$	$6^\circ$	$8^\circ$	$10^\circ$
$\theta_3$	$5^\circ$	$7^\circ$	$9^\circ$
$\theta_4$	$5^\circ$	$7^\circ$	$9^\circ$



## Package DIP14



DIM.	MIN	TYP	MAX	DIM.	MIN	TYP	MAX
A	6.100	6.300	6.680	a	1.504	1.524	1.544
B	18.940	19.200	19.560	C	0.437	0.457	0.477
D	8.200	8.700	9.200	d	2.530	2.540	2.550
D1	7.42	7.62	7.82	L	0.500	—	0.800
E	3.100	3.300	3.550	L1	3.000	3.200	3.600



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