

Description

- High current carrying capacity, Low core losses
- Magnetically shielded, low EMI
- 125 °C maximum total temperature operation
- Halogen Free
- RoHS compliant
- Powder iron core material
- Frequency range up to 5 MHz



Applications

- Voltage Regulator Module (VRM)
- Multi-phase regulators
- Point-of-load modules
- Smart phone POL modules
- SSD modules
- Notebook regulators
- Battery power systems
- Graphics cards
- Data networking and storage systems

Environmental Data

- Storage temperature range: -55°C to +125°C
- Operating temperature range: -55°C to +125 ° C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 compliant

Packaging

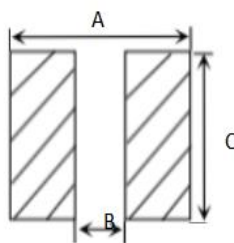
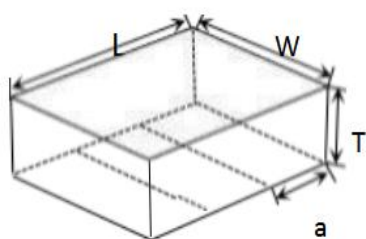
- Supplied in tape and reel packaging.

Product Identification

<u>GXTC</u>	<u>252012</u>	<u>xxx</u>	<u>M</u>
①	②	③	④

- ① Series name
- ② Dimensions
- ③ Inductance Value
- ④ Inductance Tolerance (M= ± 20%)

Dimensions-mm



单位: mm

Series	L	W	T	a	A	B	C
GXTC-252012	$2.5 \pm 0.$	$2.0 \pm 0.$	1.2MAX	0.9 ± 0.2	2.6	0.7	2.1

Electrical Characteristics

Part Number	Inductance	DC Resistance		Heating Rating Current	Saturation Current
	L (uH)	DCR(mΩ)		A	A
	$\pm 20\%, 1\text{MHz}, 0.1\text{V}$	Typ	Max	Typ	Typ
GXTC-252012-R10-M	0.1	6	10	12.0	13.5
GXTC-252012-R15-M	0.15	7	11	11.5	13.0
GXTC-252012-R22-M	0.22	9	14	8.2	9.6
GXTC-252012-R24-M	0.24	10	15	8.0	9.3
GXTC-252012-R33-M	0.33	11	17	6.8	8.3
GXTC-252012-R47-M	0.47	11	13	8.0	8.5
GXTC-252012-R68-M	0.68	15	18	7.5	6.7
GXTC-252012-R82-M	0.82	19	24	5.8	6.5
GXTC-252012-1R0-M	1.0	16	22	5.2	6.5
GXTC-252012-1R2-M	1.2	40	45	3.8	4.5
GXTC-252012-1R5-M	1.5	27	32	4.6	4.7
GXTC-252012-2R2-M	2.2	55	65	3.0	3.8
GXTC-252012-3R3-M	3.3	80	97	2.3	3.0
GXTC-252012-4R7-M	4.7	150	170	1.8	2.4
GXTC-252012-6R8-M	6.8	245	270	1.6	2.0
GXTC-252012-100-M	10.0	330	400	1.2	1.6

Notes:

1. All test data is referenced to 25 °C ambient
2. Operating temperature range - 55°C to + 125 °C
3. Idc(A):DC current (A) that will cause an approximate ΔT of 40 °C
4. Isat(A):DC current (A) that will cause L0 to drop approximately 30 %
5. 50V DC shall be applied for 60s between the terminal and the Core
6. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.