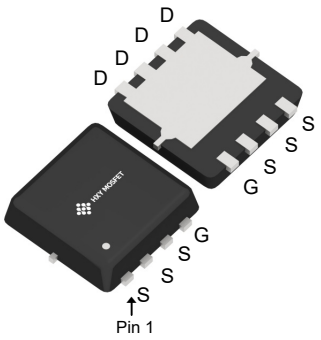
NVTFS4C05NWFTAG

N-Channel Enhancement Mode MOSFET

Description

The NVTFS4C05NWFTAG uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

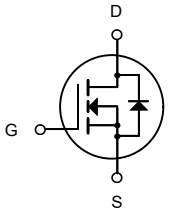


DFN3X3-8L

General Features

V_{DS} = 30V I_D = 85A

$R_{DS(ON)}$ < 3.1 mΩ @ V_{GS}=10V



N-Channel MOSFET

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
NVTFS4C05NWFTAG	DFN3X3-8L	HXY MOSFET	5000

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
VGS	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V[1]	85	A
I_D@T_C=75℃	Continuous Drain Current, V_{GS} @ 10V[1]	78	A
IDM	Pulsed Drain Current[2]	480	A
EAS	Single Pulse Avalanche Energy[3]	333	mJ
IAS	Avalanche Current	25	A
P_D@T_C=25℃	Total Power Dissipation[4]	88	W
TSTG	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃
R_{θ}JC	Thermal Resistance Junction-Case[1]	1.4	℃/W

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	30	---	---	V
$\triangle BV_{DSS}/\triangle T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C, I_D=1mA	---	---	---	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=20A	---	2.9	3.1	mΩ
		V_{GS}=4.5V , I_D=15A	---	3.9	4.5	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1	1.5	2.2	V
$\triangle V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	---	---	mV/°C
I_{DSS}	Drain-Source Leakage Current	V_{DS}=1.V , V_{GS}=0V , T_J=25°C	---	---	1	uA
		V_{DS}=1.V, V_{GS}=0V , T_J=100°C	---	---	---	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=1.V , I_D=20A	---	---	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	---	---	Ω
Q_g	Total Gate Charge	V_{DS}=15V , V_{GS}=10V , I_D=30A	---	72	---	nC
Q_{gs}	Gate-Source Charge		---	46	---	
Q_{gd}	Gate-Drain Charge		---	13	---	
$T_{d(on)}$	Turn-On Delay Time	V_{GS}=10V, V_{DS}=15V, I_D=30A, R_L=3Ω	---	14	---	ns
T_r	Rise Time		---	18	---	
$T_{d(off)}$	Turn-Off Delay Time		---	40	---	
T_f	Fall Time		---	12	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , f=1MHz	---	4000	---	pF
C_{oss}	Output Capacitance		---	437	---	
C_{rss}	Reverse Transfer Capacitance		---	396	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1,4]	V_G=V_D=0V , Force Current	---	---	85	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=30A , T_J=25°C	---	---	1.2	V
t_{rr}	Reverse Recovery Time	IF=30A , di/dt=100A/μs , T_J=25°C	---	48	---	nS
Q_{rr}	Reverse Recovery Charge		---	80	---	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: T_J=25°C, V_{DD}=15V, V_G=10V, R_G=25Ω, L=0.5mH, I_{AS}=25A
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Typical Characteristics

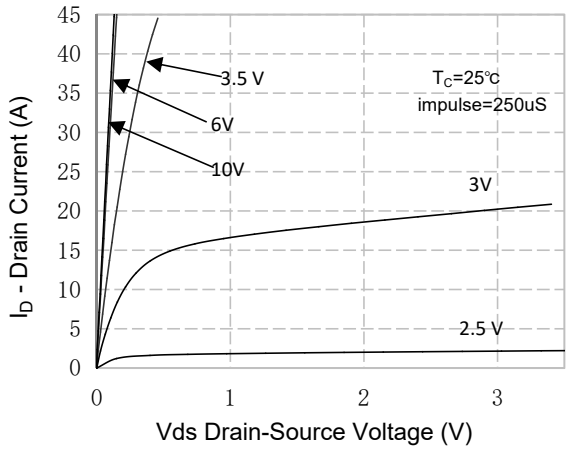


Figure 1. On-Region Characteristics

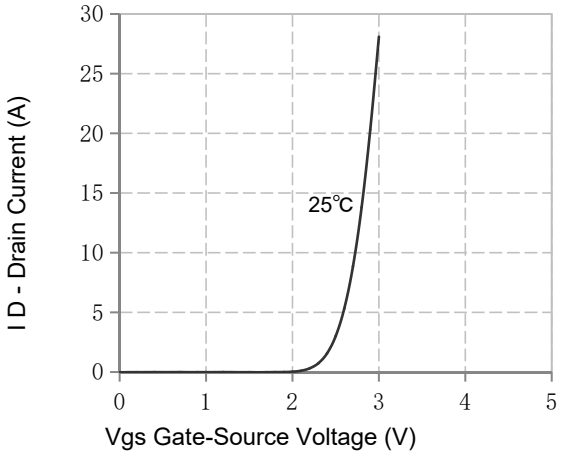


Figure 2. Transfer Characteristics

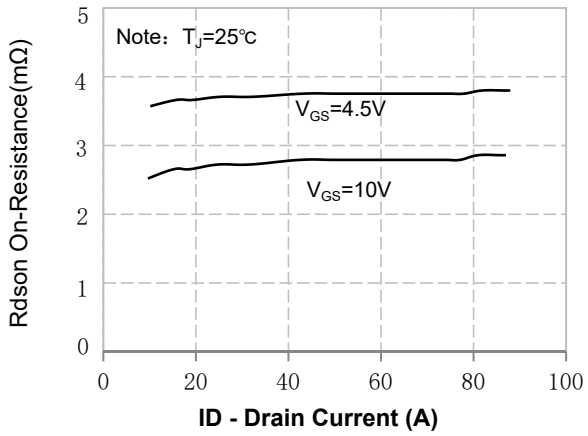


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

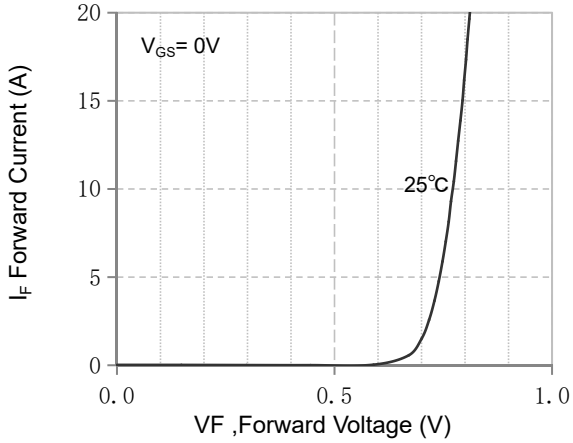


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

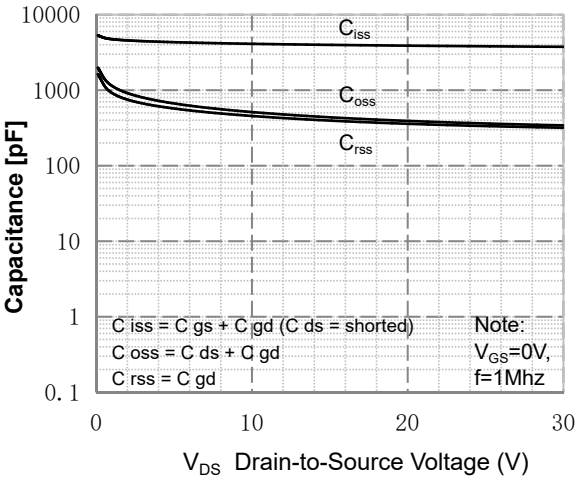


Figure 5. Capacitance Characteristics

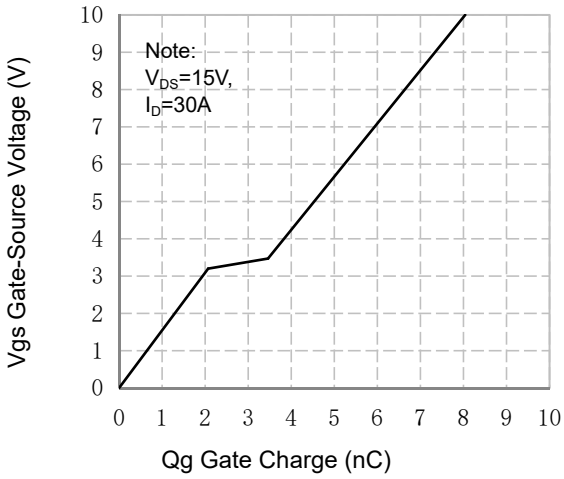


Figure 6. Gate Charge Characteristics

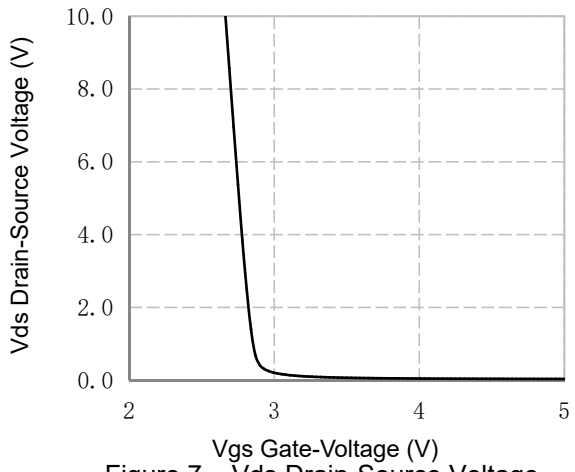


Figure 7. Vds Drain-Source Voltage vs Gate Voltage

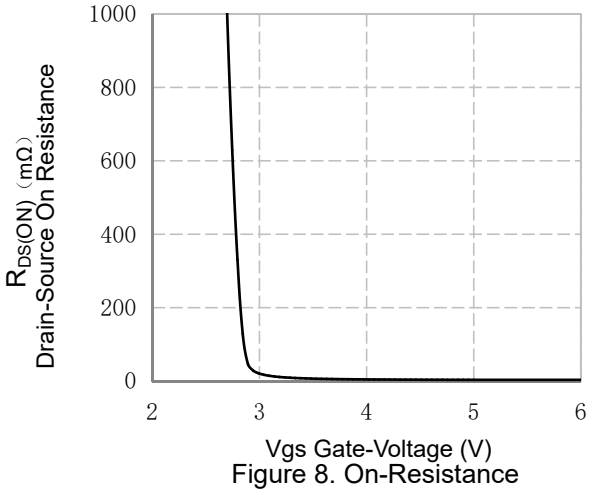


Figure 8. On-Resistance vs Gate Voltage

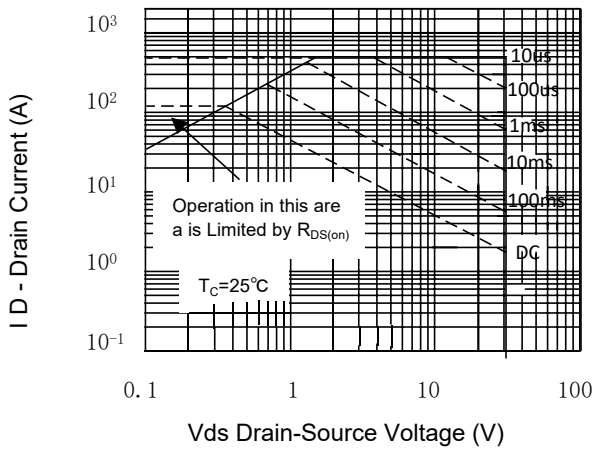


Figure 9. Maximum Safe Operating Area

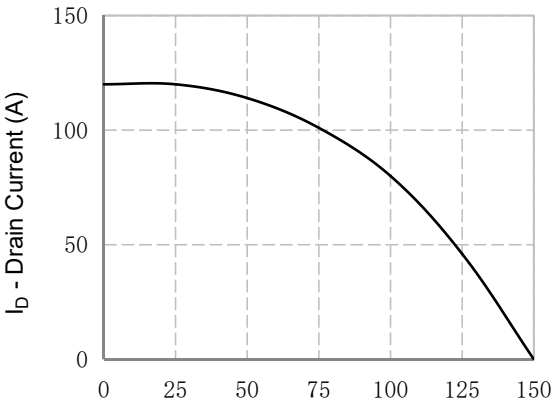


Figure 10. Maximum Continuous Drain Current vs Temperature

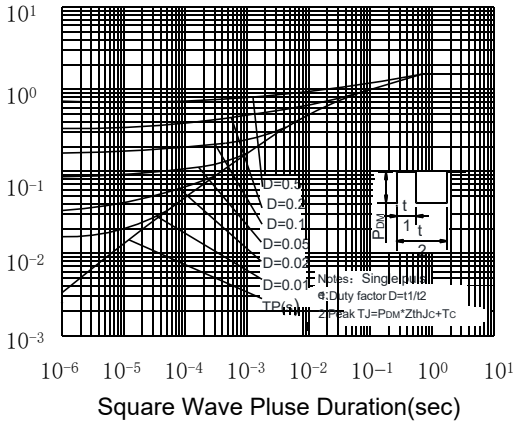
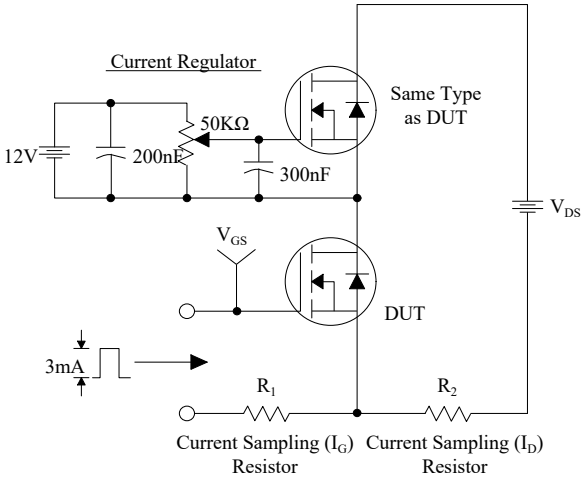
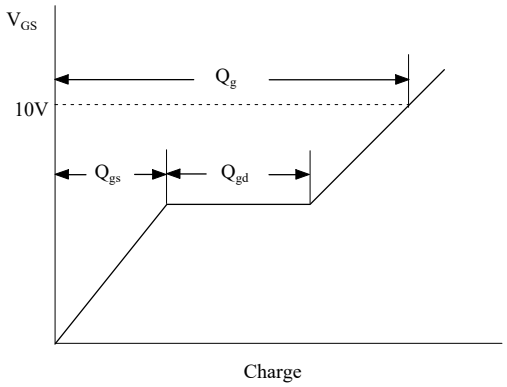


Figure 11. Transient Thermal Response Curve

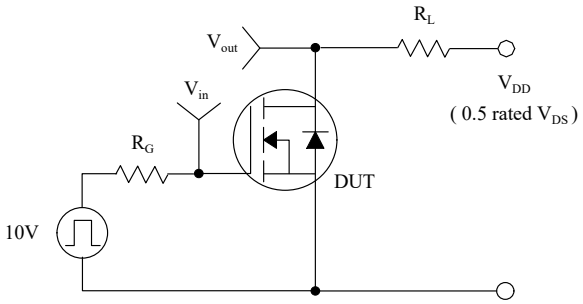
Test Circuit

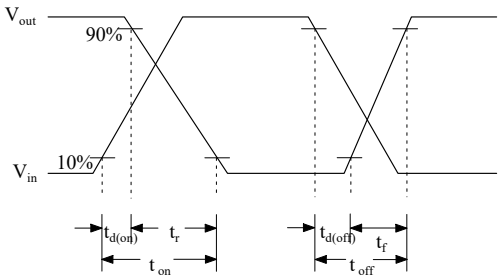
Gate Charge Test Circuit & Waveform



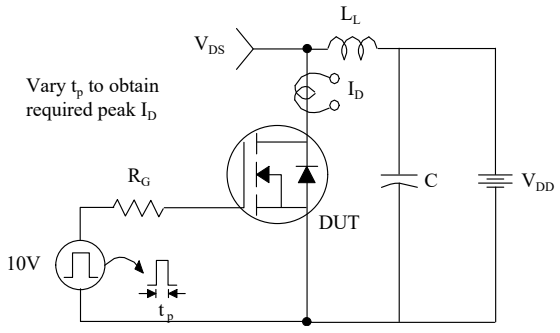


Resistive Switching Test Circuit & Waveforms

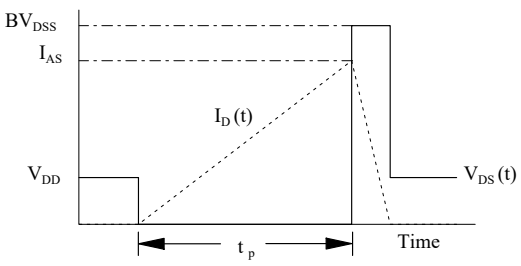




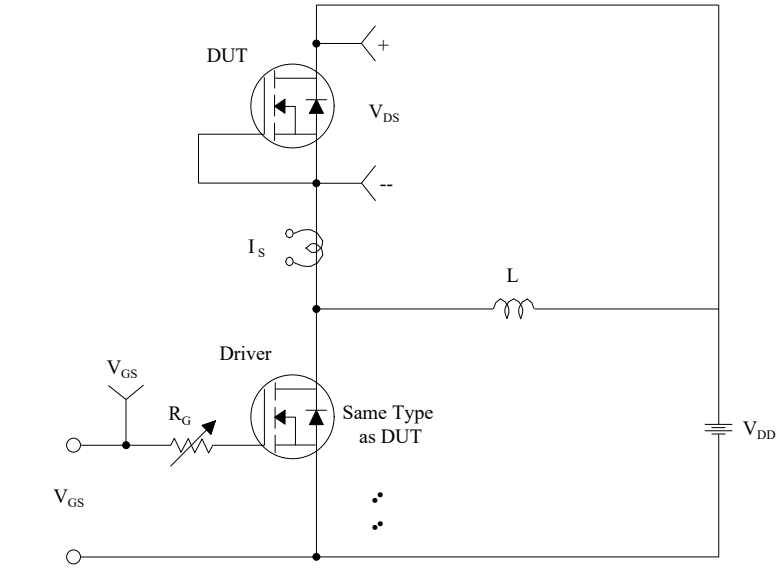
Unclamped Inductive Switching Test Circuit & Waveforms

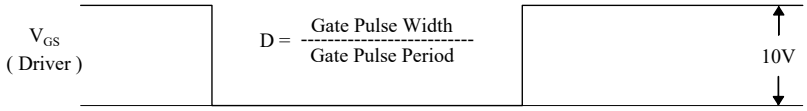


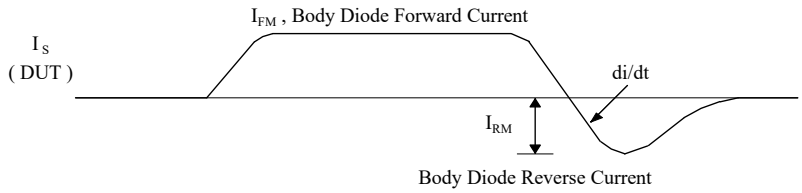
$$E_{AS} = \frac{1}{2} L_L I_{AS}^2$$

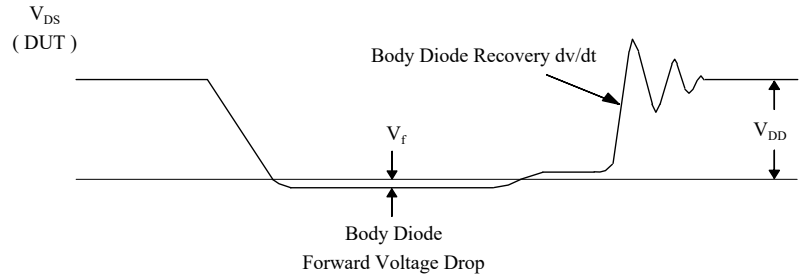


Peak Diode Recovery dv/dt Test Circuit & Waveforms

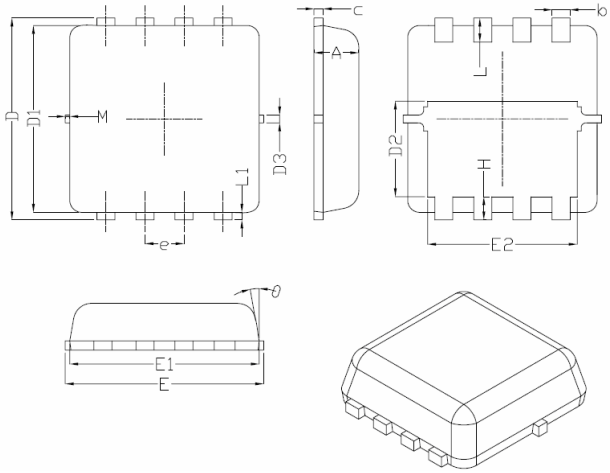








DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°

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