

Low Power Low Offset Voltage Dual Comparators

1.Features

■ Wide Single-Supply Range: 2.0V to 32V

■ Split-Supply Range: ±1.0V to ±16V

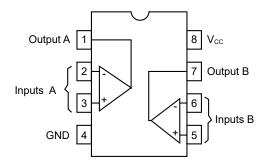
Very Low Current Drain Independent of

Supply Voltage: 0.4mA

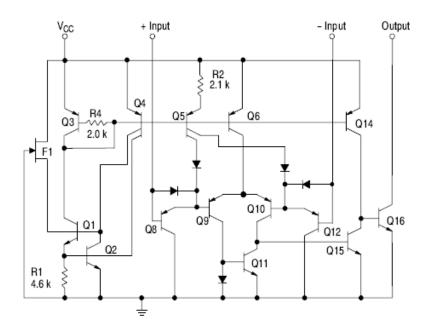
■ Low Input Bias Current: 25nA

- Low Input Offset Current: 5.0nA
- Low Input Offset Voltage: 5.0mV (max)
- Input Common Mode Range to Ground Level
- Differential Input Voltage Range Equal to Power Supply Voltage

2.Pinning Information



3. Representative Schematic Diagram







4.Absolute Maximum Ratings T_A=25°C

Parameter	Symbol	Ratings	Units
Power Supply Voltage	V _{cc}	+32 or ±16	V
Input Differential Voltage Range	V _{IDR}	32	V
Input Common Mode Voltage Range	V _{ICR}	-0.3 to +32	V
Output Short Circuit-to-Ground	I _{sc}	Continuous	mA
Output Sink Current*	I _{Sink}	20	mA
Power Dissipation @ T _A =25°C	P _D	570	mW
Derate above 25°C	1/R _{θJA}	5.7	mW/°C
Operating Ambient Temperature Range	T _A	-40 to +105	°C
Maximum Operating Junction Temperature	$T_{J(max)}$	150	°C
Storage Temperature Range	T _{STG}	-65 to +150	°C
ESD Protection at any Pin		-	-
- Human Body Mode	V _{ESD}	2000	V
-Machine Model		200	V

 $^{^{\}star}$ The maximum output current may be as high as 20mA, independent of the magnitude of V_{CC} , output short circuits to V_{CC} can cause excessive heating and eventual destruction.

Low Power Low Offset Voltage Dual Comparators



5.Electrical Characteristics

 V_{CC} =5.0V, 0°C \leq T_A \leq 70°C ,unless otherwise noted.

Parameter	Symbol	Conditions		Тур	Max	Units
Input Offset Voltage*1	V _{IO}	T _A =25°C		±1	±5	mV
input Offset voltage 1	V _{IO}	0°C≤T _A ≤70°C			9	mV
In must Office to Commont		T _A =25°C		±5	±50	nA
Input Offset Current	I _{IO}	0°C≤T _A ≤70°C			±150	nA
Input Bigg Current *2	I _{IB}	T _A =25°C		25	250	nA
Input Bias Current *2		0°C≤T _A ≤70°C			400	nA
Input Common	V	T _A =25°C	0		V _{cc} -1.5	V
Mode Voltage Range *2	V _{ICR}	0°C≤T _A ≤70°C	0		Vcc-2	V
Voltage Gain	A _{VOL}	R _L ≥15kΩ, V _{CC} =15V, T _A =25°C	50	200		V/mV
. O: 1D T		V _{in} =TTL, Logic Swing, V _{ref} =1.4V		300		ns
Large Signal Response Time		V_{RL} =5.0V, R_{L} =5.1k Ω , T_{A} =25°C		300		
Response Time *4	t _{TLH}	V _{RL} =5.0V, R _L =5.1kΩ, T _A =25°C		1.3		μs
Input Differential Voltage *5	V _{ID}	All V _{in} ≥GND or V-Supply (if used)			V _{cc}	V
Output Sink Current	I _{Sink}	V _{in} ≥1.0V, V _{in+} =0V, V _O ≤1.5V, T _A =25°C	6	16		mA
Output Saturation Voltage	V _{OL}	V _{in} ≥1.0V, V _{in+} =0V, I _{Sink} ≤4.0mA		150	400	mV
		T _A =25°C, 0°C≤T _A ≤70°C			700	mV
		V _{in-} =0V, V _{in+} ≥1.0V, V _O =5.0V, T _A =25°C		0.1		nA
Output Leakage Current	I _{OL}	V _{in-} =0V, V _{in+} ≥1.0V, V _O =30V, 0°C≤T _A ≤70°C			1000	nA
Cumulu Cumant	I _{cc}	R _L =∞Both Comparators, T _A =25°C		0.4	1	mA
Supply Current		R _L =∞Both Comparators, V _{CC} =30V			2.5	mA







Low Power Low Offset Voltage Dual Comparators

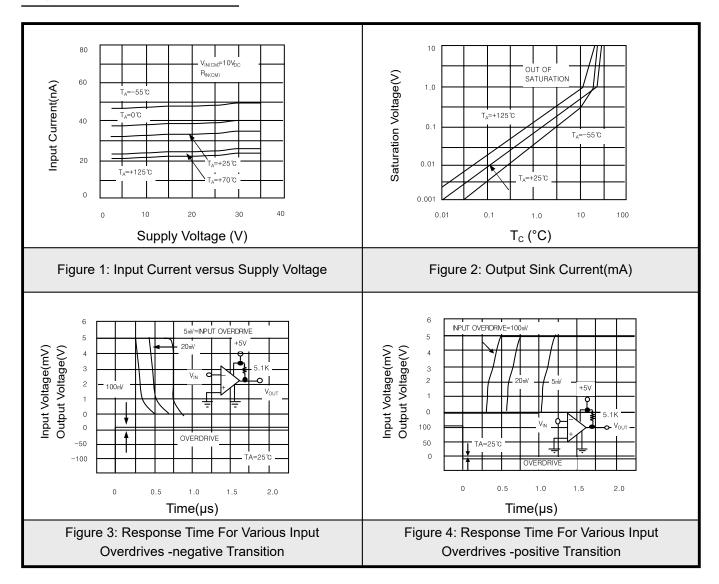
Notes: *1. At output switch point, V_0 =1.4V, R_s =0 Ω with V_{CC} from 5.0V to 30V, and over the full input common mode range (0V to V_{CC} =-1.5 V).

- *2. Due to the PNP transistor inputs, bias current will flow out of the inputs. This current is essentially constant, independent of the output state, therefore, no loading changes will exist on the input lines.
- *3. Input common mode of either input should not be permitted to go more than 0.3V negative of ground or minus supply. The upper limit of common mode range is V_{CC} -1.5 V.
- *4. Response time is specified with a 100 mV step and 5.0 mV of overdrive. With larger magnitudes of overdrive faster response times are obtainable.
- *5. The comparator will exhibit proper output state if one of the inputs becomes greater than V_{CC}, the other input must remain within the common mode range. The low input state must not be less than -0.3V of ground or minus supply.

Low Power Low Offset Voltage Dual Comparators



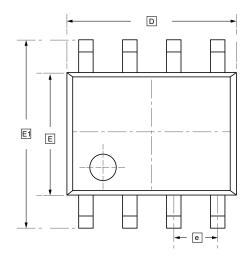
6.Typical Characteristic

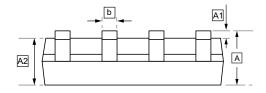


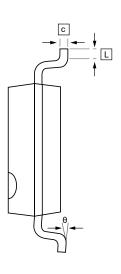
Low Power Low Offset Voltage Dual Comparators



7.SOP-8 Package Outline Dimensions







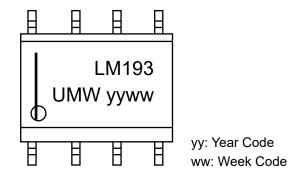
DIMENSIONS (mm are the original dimensions)

Symbol	Α	A 1	A2	b	С	D	Е	E1	е	L	θ
Min	1.350	0.000	1.350	0.330	0.170	4.700	3.800	5.800	1.270	0.400	0°
Max	1.750	0.100	1.550	0.510	0.250	5.100	4.000	6.200	BSC	1.270	8°



Low Power Low Offset Voltage Dual Comparators

8. Ordering information



Order Code	Package	Base QTY	Delivery Mode		
UMW LM193DR	SOP-8	2500	Tape and reel		







Low Power Low Offset Voltage Dual Comparators

9.Disclaimer

UMW reserves the right to make changes to all products, specifications. Customers should obtain the latest version of product documentation and verify the completeness and currency of the information before placing an order.

When applying our products, please do not exceed the maximum rated values, as this may affect the reliability of the entire system. Under certain conditions, any semiconductor product may experience faults or failures. Buyers are responsible for adhering to safety standards and implementing safety measures during system design, prototyping, and manufacturing when using our products to prevent potential failure risks that could lead to personal injury or property damage.

Unless explicitly stated in writing, UMW products are not intended for use in medical, life-saving, or life-sustaining applications, nor for any other applications where product failure could result in personal injury or death. If customers use or sell the product for such applications without explicit authorization, they assume all associated risks.

When reselling, applying, or exporting, please comply with export control laws and regulations of China, the United States, the United Kingdom, the European Union, and other relevant countries, regions, and international organizations.

This document and any actions by UMW do not grant any intellectual property rights, whether express or implied, by estoppel or otherwise. The product names and marks mentioned herein may be trademarks of their respective owners.