

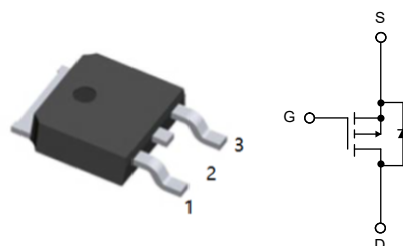
1.Features

- $V_{DS(V)} = -40V$
- $R_{DS(ON)} < 17m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 29m\Omega (V_{GS} = -4.5V)$
- Package with low thermal resistance

2.Pinning information

Pin	Symbol	Description
1	G	GATE
2	D	DRAIN
3	S	SOURCE

TO-252(DPAK)
top view



3.Absolute Maximum Ratings $T_c = 25^\circ C$

Parameter		Symbol	Value	Units
Drain-Source Voltage		V_{DS}	-40	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_c = 25^\circ C^a$	I_D	-50	A
	$T_c = 125^\circ C$		-39	
Continuous Source Current (Diode Conduction) ^a		I_S	-50	
Pulsed Drain Current ^b		I_{DM}	-200	
Single Pulse Avalanche Current	$L = 0.1mH$	I_{AS}	-40	
Single Pulse Avalanche Energy	$L = 0.1mH$	E_{AS}	80	mJ
Maximum Power Dissipation ^b	$T_A = 25^\circ C$	P_D	3	W
	$T_c = 25^\circ C$		136	W
	$T_A = 125^\circ C$		45	W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 175	$^\circ C$



4.Thermal Resistance Rating

Parameter		Symbol	LIMIT	Units
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	50	°C/W
Junction-to-Case (Drain)		R _{thJC}	1.1	°C/W

Notes

- a. Package limited.
- b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- c. When mounted on 1" square PCB (FR4 material).
- d. Parametric verification ongoing.



5. Specifications ($T_C = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS}=0V, I_D=-250\mu A$	-40			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.5		-2.5	V
Gate-Body Leakage	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS}=-40V, V_{GS}=0V$			-1	μA
		$V_{DS}=-40V, V_{GS}=0V, T_J=125^\circ\text{C}$			-50	μA
		$V_{DS}=-40V, V_{GS}=0V, T_J=175^\circ\text{C}$			-150	μA
On-State Drain Current ^a	$I_{D(ON)}$	$V_{DS}\leq -5V, V_{GS}=-10V$	-50			A
Drain-Source On-State Resistance ^a	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-17A$			17	m Ω
		$V_{GS}=-4.5V, I_D=-14A$			29	m Ω
Forward Transconductance ^a	g_{FS}	$V_{DS}=-15V, I_D=-17A$		61		S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=-25V, f=1\text{MHz}$		2872	3950	pF
Output Capacitance	C_{oss}			508	635	pF
Reverse Transfer Capacitance	C_{rss}			352	440	pF
Total Gate Charge ^c	Q_g	$V_{DS}=-30V, V_{GS}=-10V, I_D=-50A$		60	80	nC
Gate-Source Charge ^c	Q_{gs}			5.7	8.6	nC
Gate-Drain Charge ^c	Q_{gd}			14.7	22	nC
Gate Resistance	R_G	$f=1\text{MHz}$	1.5	3	4.5	Ω
Turn-On Delay Time ^c	$t_{D(on)}$	$V_{DD}=-20V, R_L=0.4\Omega$		10	15	ns
Rise Time ^c	t_r			12	18	ns
Turn-Off Delay Time ^c	$t_{D(off)}$			40	60	ns
Fall Time ^c	t_f	$I_D\leq -50A, V_{GEN}=-10V, R_G=1\Omega$		16	24	ns



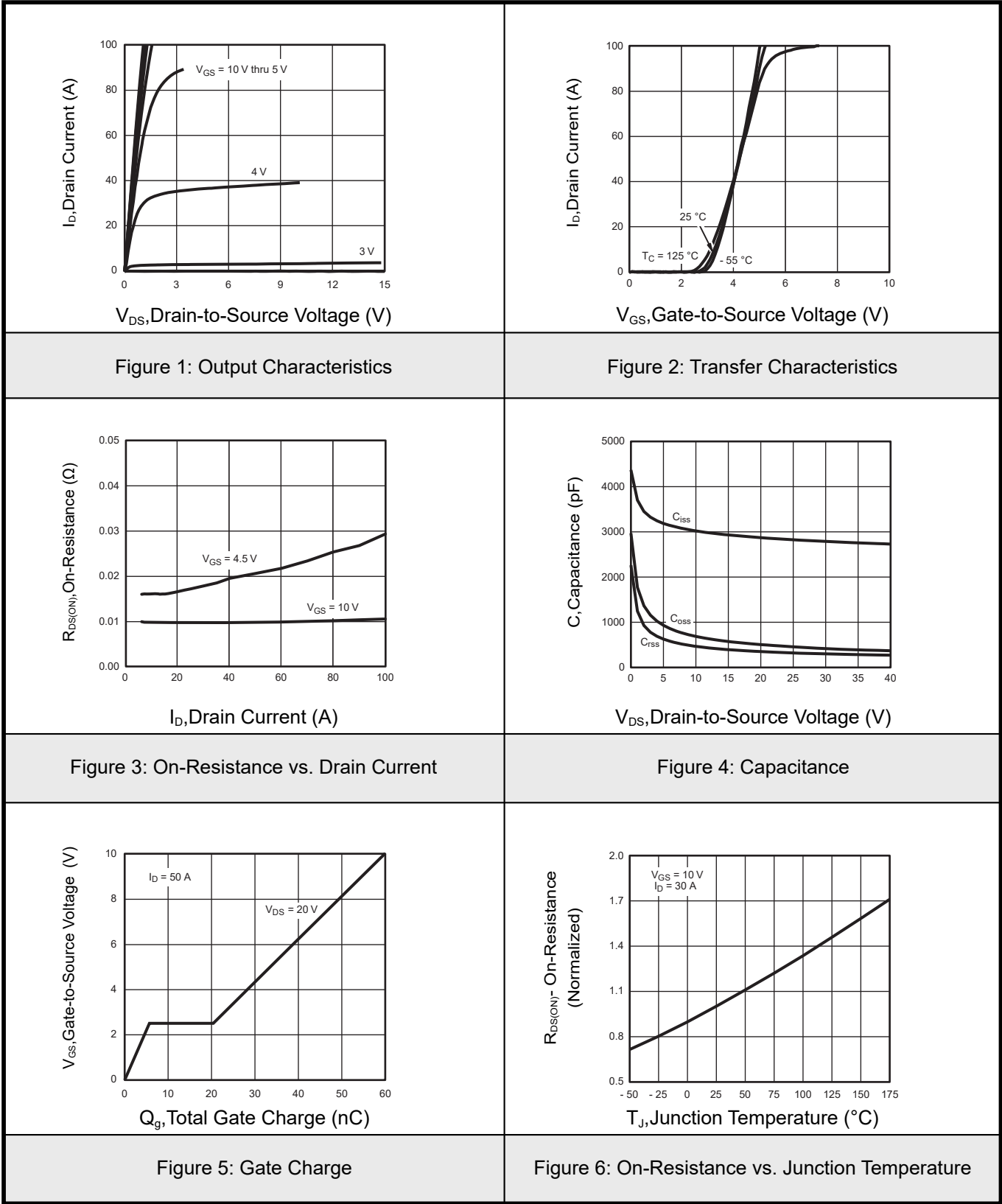
Source-Drain Diode Ratings and Characteristics ^b						
Pulsed Current ^a	I _{SM}				-200	A
Forward Voltage	V _{SD}	I _F =-50A, V _{GS} =0V		-1	-1.5	V

Notes:

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

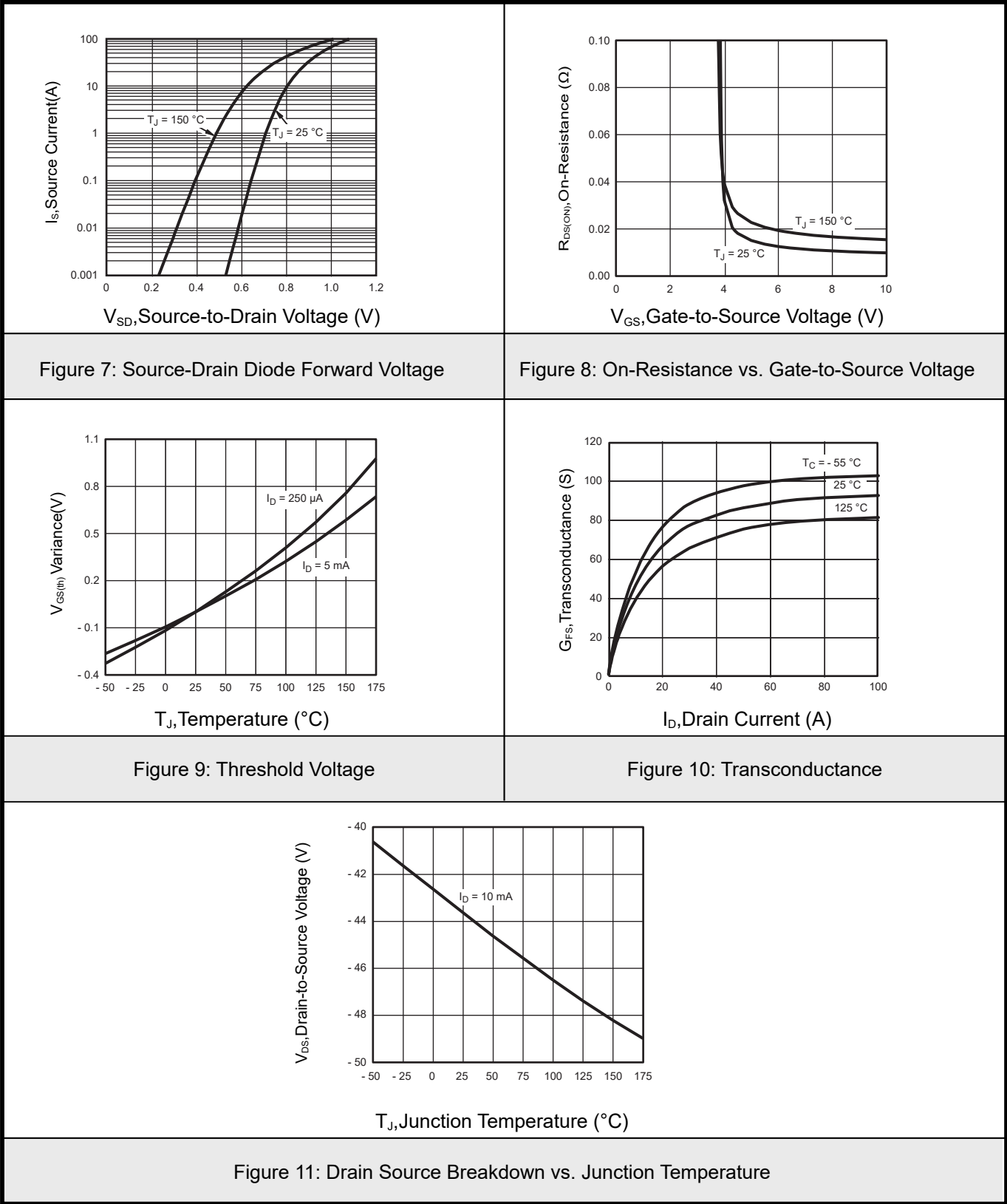


6.1 Typical Characteristics



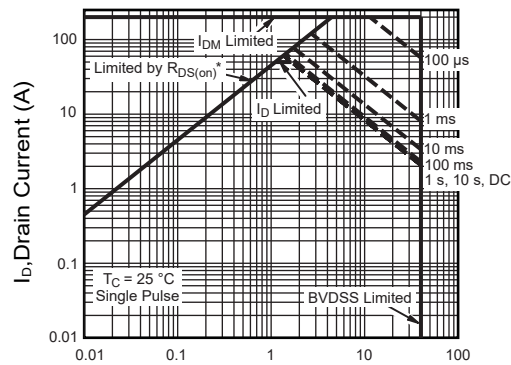


6.2Typical Characteristics





6.3 Typical Characteristics



V_{DS} , Drain-to-Source Voltage (V) * $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Figure 12: Safe Operating Area

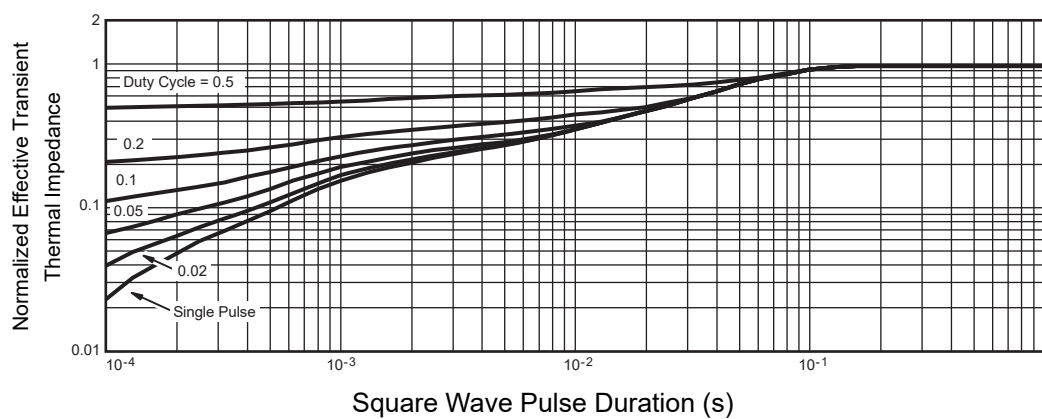


Figure 13: Normalized Thermal Transient Impedance, Junction-to-Case



6.4 Typical Characteristics

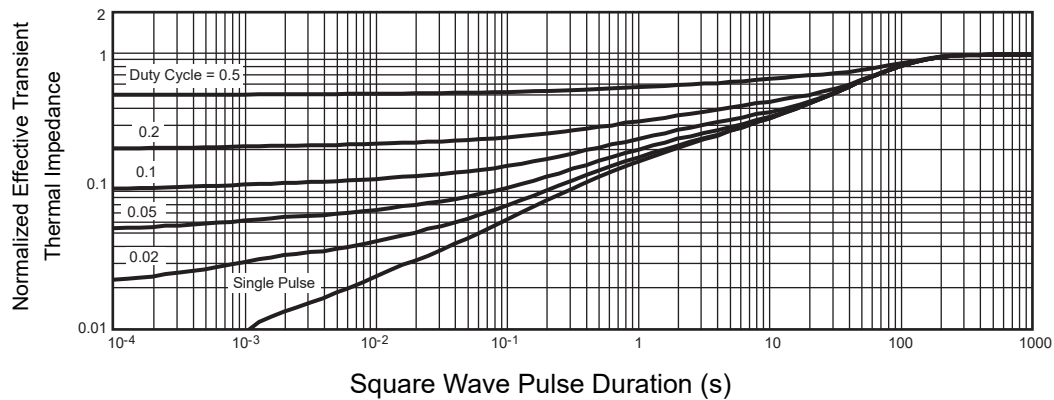


Figure 14: Normalized Thermal Transient Impedance, Junction-to-Ambient

Note:

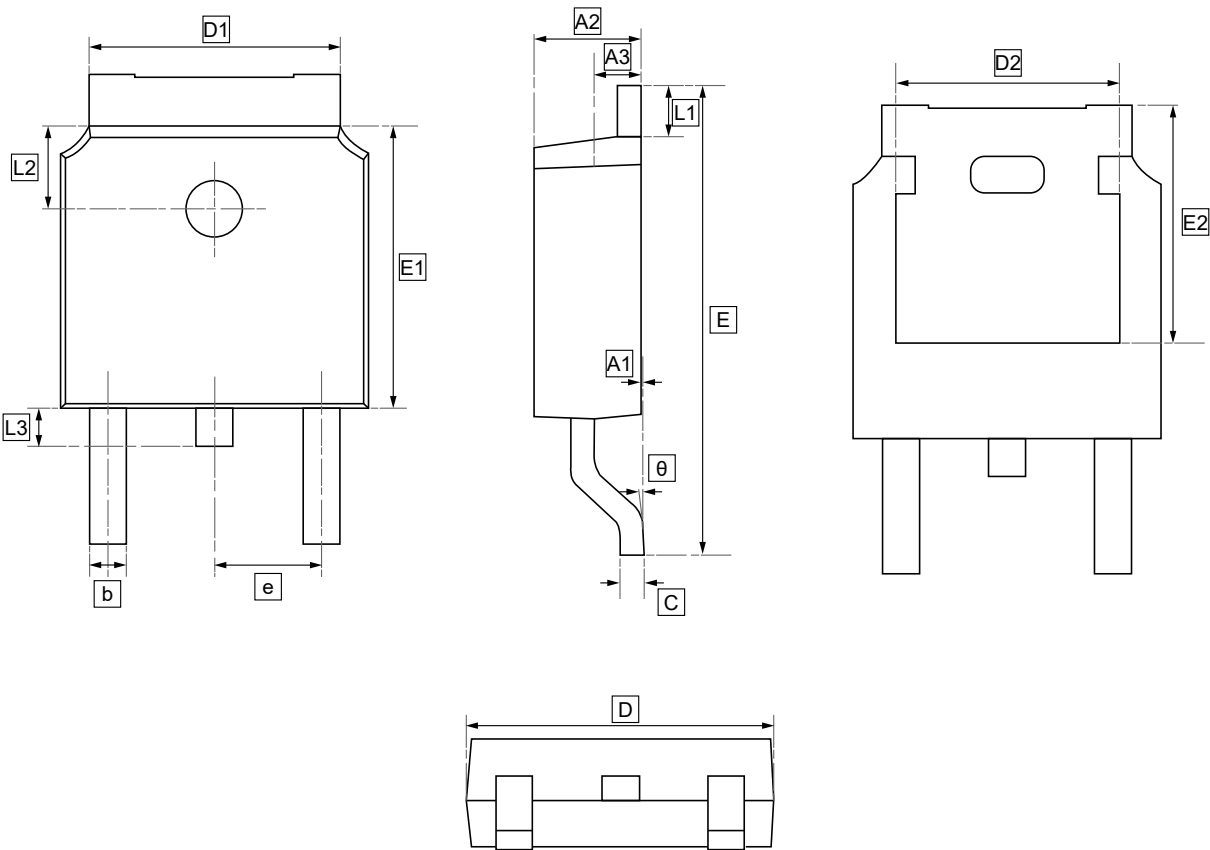
The characteristics shown in the two graphs

- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



7.TO-252 Package Outline Dimensions

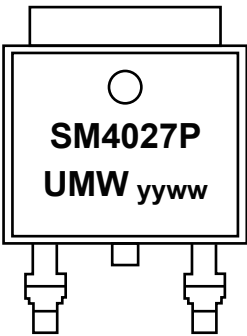


DIMENSIONS (mm are the original dimensions)

Symbol	A1	A2	A3	b	c	D	D1	D2	E	E1	E2	e	L1	L2	L3	θ
Min	0.00	2.18	0.90	0.65	0.46	6.35	4.95	4.32	9.40	5.97	5.21	2.286 BSC	0.89	1.70	0.60	0.00
Max	0.13	2.39	1.10	0.85	0.61	6.73	5.46	4.90	10.41	6.22	5.38		1.27	1.90	1.00	8.00



8.Ordering information



yy: Year Code
ww: Week Code

Order Code	Package	Base QTY	Delivery Mode
UMW SM4027PSU	TO-252	2500	Tape and reel



9.Disclaimer

UMW reserves the right to make changes to all products, specifications. Customers should obtain the latest version of product documentation and verify the completeness and currency of the information before placing an order.

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