

## 1.Description

The PESDNC5D3V3U protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events.

They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one unidirectional line in applications where arrays are not practical.

## 2.Features

- 150W peak pulse power per line ( $t_p=8/20\mu s$ )
- SOD-523 package
- Replacement for MLV(0603)
- Unidirectional configurations
- Response time is typically < 1 ns
- Protect one I/O or power line
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)  $\pm 30KV$ (air),  $\pm 30KV$ (contact); IEC 61000-4-4 (EFT)40A(5/50ns)

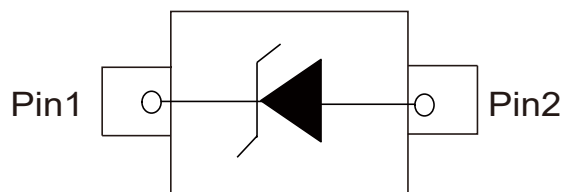
## 3.Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

## 4.Mechanical Characteristics

- Lead finish:100% matte Sn(Tin) Mounting
- position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17  $\mu m$
- Pin flatness: $\leq 3mil$

## 5.Pinning information



**SOD-523**



## 6. Absolute Maximum Ratings $T_A = 25^\circ\text{C}$

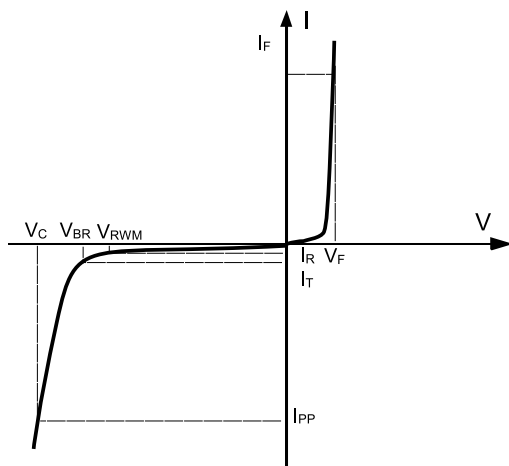
Parameter	Symbol	Value	Units
Peak Pulse Power ( $t_p=8/20\mu\text{s}$ )	$P_{PP}$	150	W
Junction Temperature	$T_J$	-55 to +150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to +150	$^\circ\text{C}$



## 7. Electrical Characteristic ( $T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Reverse Stand-off Voltage	$V_{RWM}$				3.3	V
Reverse Breakdown Voltage	$V_{BR}$	$I_T=1\text{mA}$	5			V
Reverse Leakage Current	$I_R$	$V_{RWM}=3.3\text{V}$ , $T=25^\circ\text{C}$			1	$\mu\text{A}$
Clamping Voltage	$V_C$	$I_{PP}=1\text{A}$ , $t_p=8/20\mu\text{s}$			5.8	V
Clamping Voltage	$V_C$	$I_{PP}=5\text{A}$ , $t_p=8/20\mu\text{s}$			8.8	V
Junction Capacitance	$C_J$	$V_R=0\text{V}$ , $f=1\text{MHz}$		120		pF

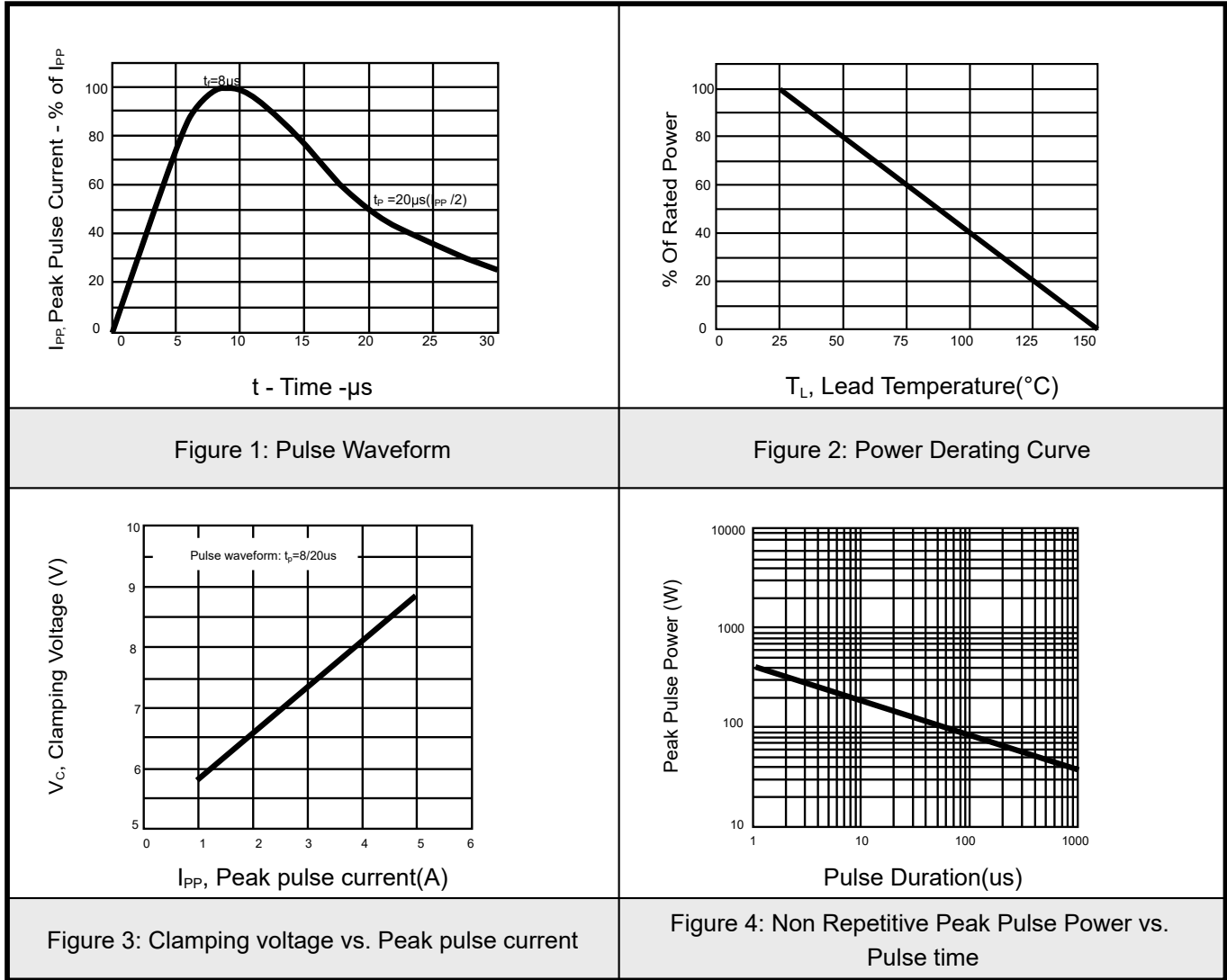
## 8. Electrical Parameters ( $T_A=25^\circ\text{C}$ unless otherwise noted)



Symbol	Parameter
$V_{RWM}$	Peak Reverse Working Voltage
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$P_{PP}$	Peak Pulse Power
$C_J$	Junction Capacitance
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$



9. Typical characteristic





## 10.Solder Reflow Recommendation

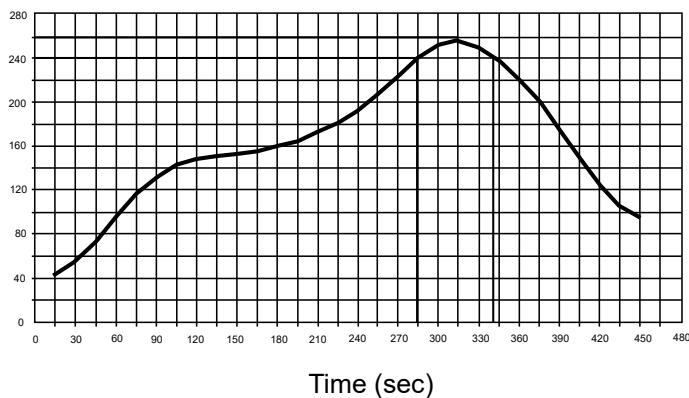


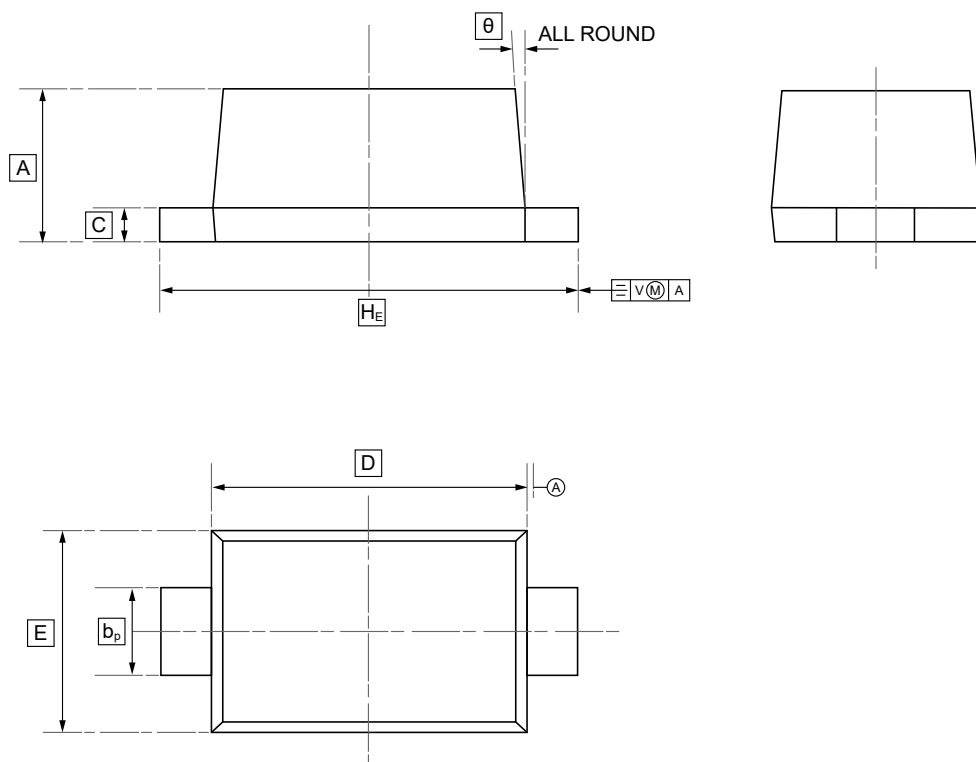
Figure 5: Peak Temp=257°C, Ramp Rate=0.802deg. °C/sec

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.



## 11.SOD-523 Package Outline Dimensions

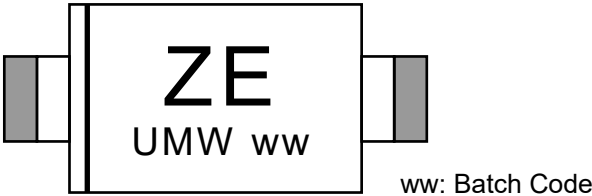


**DIMENSIONS (mm are the original dimensions)**

Symbol	A	b <sub>p</sub>	C	D	E	H <sub>E</sub>	θ
Min	0.58	0.3	0.100	1.15	0.75	1.5	5°
Max	0.68	0.4	0.135	1.25	0.85	1.7	



12.Ordering information



Order Code	Package	Base QTY	Delivery Mode
UMW PESDNC5D3V3U	SOD-523	3000	Tape and reel



## **13.Disclaimer**

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