

Features

- Low RDS(on)@VGS=4.5V
- 3.3V Logic Level Control
- N-Channel SOT23 Package
- · Pb-Free, RoHS Compliant



Applications

- Load Switch
- DC/DC Converter
- Switching Circuits
- Power Management

| V _{(BR)DSS} | R _{DS(ON)} Typ | I _D Max |
|----------------------|-------------------------|--------------------|
| 20V | 21mΩ @ 4.5V | 0.0 |
| | 28mΩ @ 3.3V | 6A |

Absolute Maximum Ratings

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

| Symbol | Parameter | Rating | Unit | | |
|----------------------|--|----------------------|------|------|--|
| Common I | Ratings (TA=25°C Unless Otherwise Noted) | .clilli | | CINI | |
| V _{GS} | Gate-Source Voltage | | ±10 | V | |
| V _{(BR)DSS} | Drain-Source Breakdown Voltage | 20 | V | | |
| TJ | Maximum Junction Temperature | 150 | °C | | |
| T _{STG} | Storage Temperature Range | -50 to 150 | °C | | |
| Mounted o | n Large Heat Sink | 2. | | • | |
| I _{DM} | Pulse Drain Current Tested① | T _A =25°C | 24.8 | А | |
| I _D | Out to the Country of | T _A =25°C | 6 | C | |
| | Continuous Drain Current | T _A =70°C | 4.2 | A | |
| P _D | | T _A =25°C | 1.56 | | |
| | Maximum Power Dissipation $T_A = 70$ | | 0.9 | - W | |
| $R_{	hetaJA}$ | Thermal Resistance Junction-Ambient | 80 | °C/W | | |

| Symbol | Parameter | Condition | Min | Тур | Max | Unit |
|-----------------------|--|--|------|------|--------|------|
| Static Ele | ctrical Characteristics @ T _J = 25°C (unle | ss otherwise stated) | | • | .16 | |
| $V_{(BR)DSS}$ | Drain-Source Breakdown Voltage | Vgs=0V Ip=250µA | 20 | \ | 87 | V |
| | Zero Gate Voltage Drain Current(T _A =25°C) | VDS=16V, VGS=0V | - | \ | 1 | μA |
| DSS | Zero Gate Voltage Drain Current(T _A =125°C) | V _{DS} =16V, V _{GS} =0V | (5) | | 100 | uA |
| I _{GSS} | Gate-Body Leakage Current | Vgs=±10V, Vps=0V | - | | ±100 | nA |
| $V_{GS(TH)}$ | Gate Threshold Voltage | Vps=Vgs, Ip=250μA | 0.45 | 0.6 | 1.0 | V |
| R _{DS(ON)} | Drain-Source On-State Resistance② | Vgs=4.5V, Ip=4A | - | 21 | | mΩ |
| R _{DS(ON)} | Drain-Source On-State Resistance② | Vgs=3.3V, ID=3A | | 28 | | mΩ |
| Dynamic | Electrical Characteristics @ TJ = 25°C (u | nless otherwise stated |) | |) | |
| C _{iss} | Input Capacitance | | | 457 | | pF |
| C _{oss} | Output Capacitance | V _{DS} =10V, V _{GS} =0V, f=1MHz | 115 | 71 | | pF |
| C_{rss} | Reverse Transfer Capacitance | 2. | - | 66 | | pF |
| R_g | Gate Resistance | f=1MHz | | 7.8 | | Ω |
| Q_{g} | Total Gate Charge | Vps=10V | | 6.6 | -11/1° | nC |
| Q_{gs} | Gate Source Charge | ID=4A, | - | 0.4 | 25. | nC |
| Q_{gd} | Gate Drain Charge | Vgs =4.5 V | | 2 | | nC |
| Switching | g Characteristics @ T」= 25°C (unless oth | nerwise stated) | (C) | | | |
| t _{d(on)} | Turn on Delay Time | Ç |) - | 4.1 | | ns |
| t, | Turn on Rise Time | VDD=10V, ID=1A, | | 11.6 | | ns |
| $\mathbf{t}_{d(off)}$ | Turn Off Delay Time | Rg=3.3Ω, Vgs=4.5V | - | 24 | - < | ns |
| t _f | Turn Off Fall Time | VG3-4.5V | - | 7.6 | CHI | ns |
| Source D | rain Diode Characteristics @ TJ = 25°C (| unless otherwise stated | (k | | 2 | _ |
| I _{SD} | Source drain current(Body Diode) | Ta=25℃ |) | 11- | 2 | Α |
| V_{SD} | Forward on voltage② | Tj=25°C, IsD=4A, Vgs=0V | | 0.79 | 1.2 | V |

Notes:

 $[\]ensuremath{\textcircled{1}}$ Pulse width limited by maximum allowable junction temperature

②Pulse test ; Pulse width≤300 μ s, duty cycle≤2%.

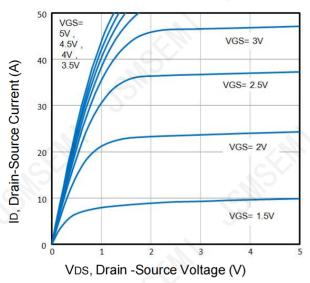


Fig1. Typical Output Characteristics

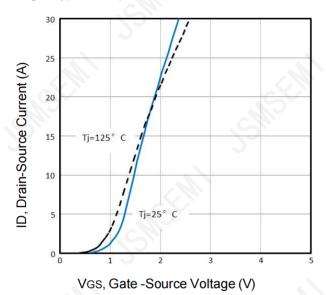


Fig3. Typical Transfer Characteristics

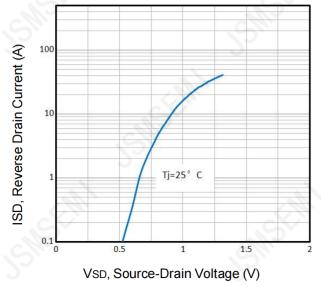


Fig5. Typical Source-Drain Diode Forward Voltage

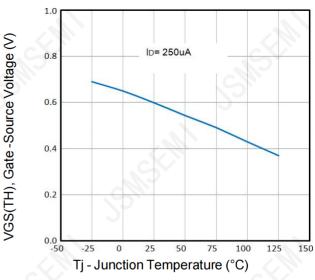


Fig2. VGS(TH) Voltage Vs. Temperature

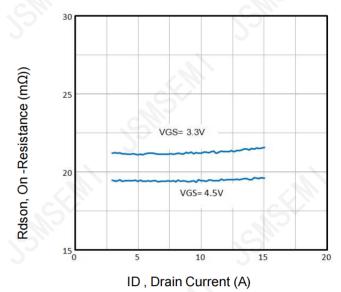


Fig4. On-Resistance vs. Drain Current and Gate

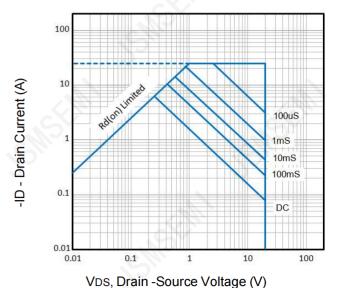


Fig6. Maximum Safe Operating Area

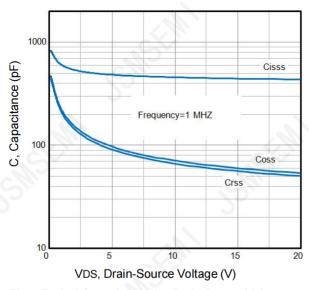


Fig7. Typical Capacitance Vs. Drain-Source Voltage

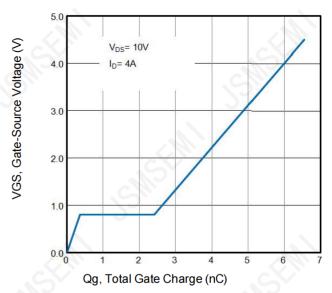


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

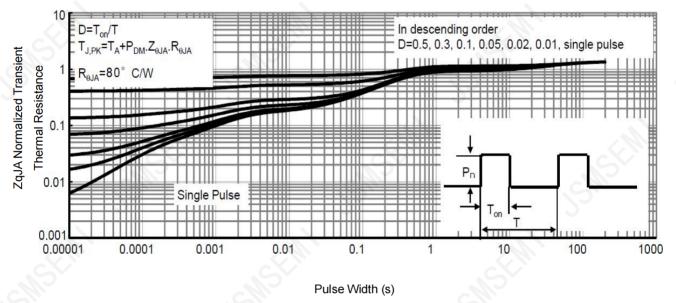


Fig9. Normalized Maximum Transient Thermal Impedance

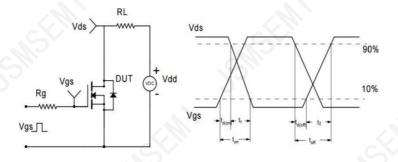
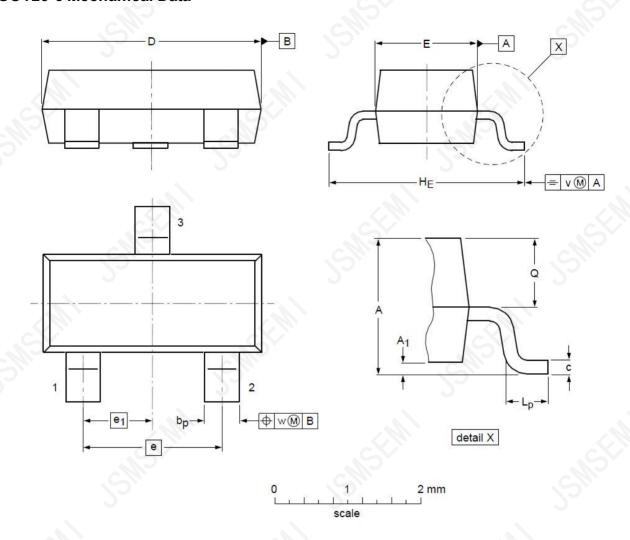


Fig10. Switching Time Test Circuit and waveforms



SOT23-3 Mechanical Data



DIMENSIONS (unit : mm)

| Symbol | Min | Тур | Max | Symbol | Min | Тур | Max |
|-----------------------|------|------|------|----------------|------|------|------|
| Α | 0.90 | 1.01 | 1.15 | A ₁ | 0.01 | 0.05 | 0.10 |
| b _p | 0.30 | 0.42 | 0.50 | С | 0.08 | 0.13 | 0.15 |
| D | 2.80 | 2.92 | 3.00 | E | 1.20 | 1.33 | 1.40 |
| е | D - | 1.90 | | e ₁ | | 0.95 | 19 |
| HE | 2.25 | 2.40 | 2.55 | Lp | 0.30 | 0.42 | 0.50 |
| Q | 0.45 | 0.49 | 0.55 | v | | 0.20 | |
| w | - | 0.10 | 7 | | | | |



Revision History

| Rev. | Change | Date |
|------|-----------------|-----------|
| V1.0 | Initial version | 2/23/2024 |
| | | |

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