

1.Description

The AO4485 uses advanced trench technology to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use as a DC-DC converter application.

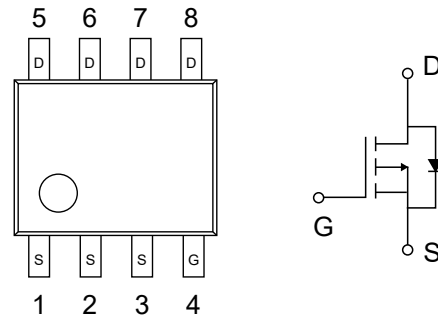
2.Features

- $V_{DS(V)} = -40V$
- $I_D = -10A$
- $R_{DS(ON)} < 15m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 20m\Omega (V_{GS} = -4.5V)$

3.Pinning information

Pin	Symbol	Description
4	G	GATE
1,2,3	S	SOURCE
5,6,7,8	D	DRAIN

SOP-8



4.Absolute Maximum Ratings $T_A = 25^\circ C$

Parameter		Symbol	10 Sec	Steady State	Units
Drain-Source Voltage		V _{DS}	-40		V
Gate-Source Voltage		V _{GS}	±20		
Continuous Drain Current ^A	T _A =25°C	I _D	-12	-10	A
	T _A =70°C		-9	-8	
Pulsed Drain Current ^B		I _{DM}	-120		
Avalanche Current ^G		I _{AR}	-28		
Repetitive avalanche energy L=0.3mH ^G		E _{AR}	118		mJ
Power Dissipation ^A	T _A =25°C	P _D	3.1	1.7	W
	T _A =70°C		2	1.1	
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 150		°C



5.Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	31	40	°C/W
Maximum Junction-to-Ambient ^A	Steady-State		59	75	°C/W
Maximum Junction-to-Lead ^C	Steady-State	R _{θJL}	16	24	°C/W



6. Electrical Characteristic ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

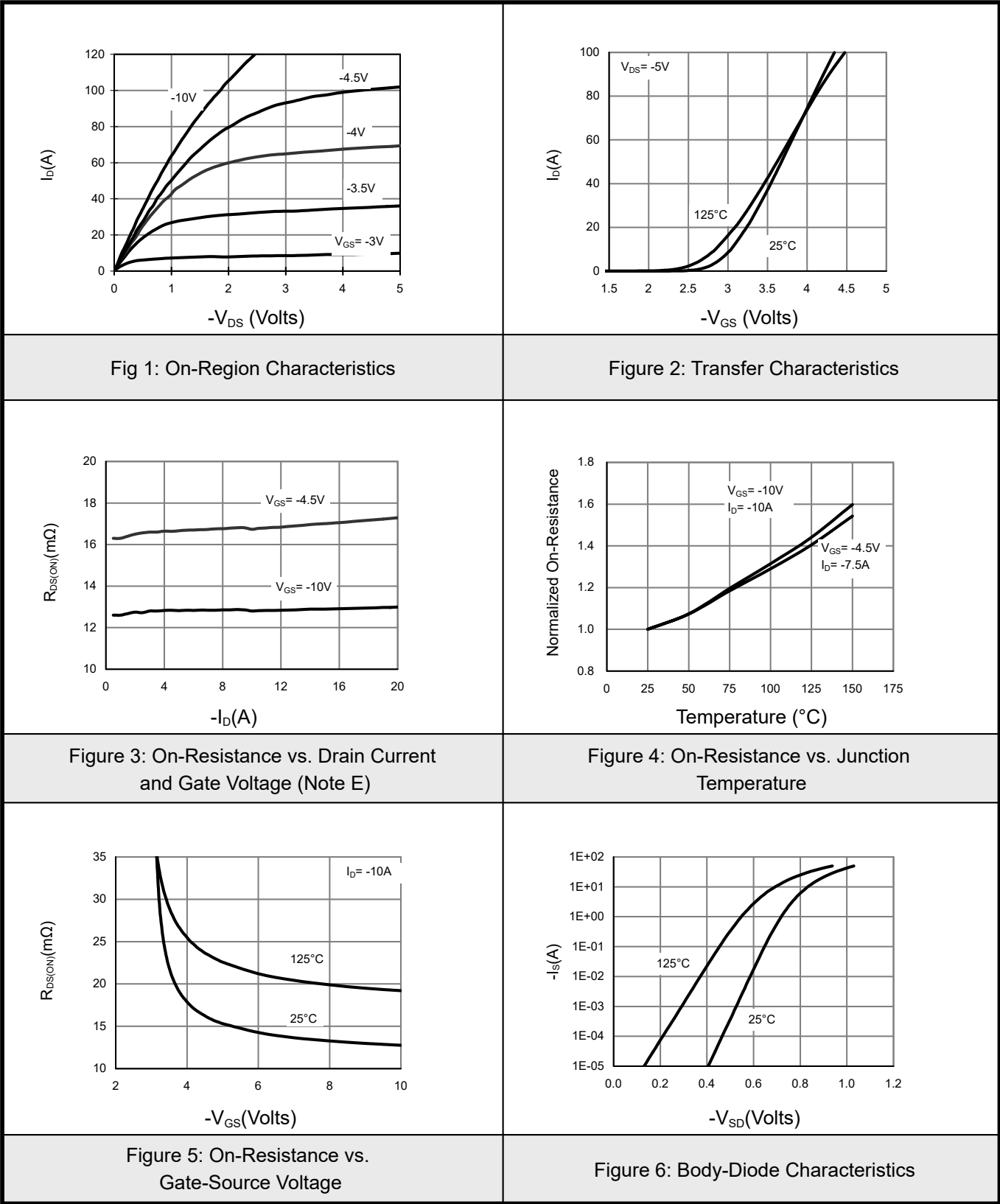
Parameter	Symbol	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=-250\mu\text{A}$, $V_{GS}=0\text{V}$	-40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-40\text{V}$, $V_{GS}=0\text{V}$			-1	μA
		$T_J=55^{\circ}\text{C}$			-5	
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	-1	-1.5	-2.5	V
On state drain current	$I_{D(ON)}$	$V_{GS}=-10\text{V}$, $V_{DS}=-5\text{V}$	-120			A
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10\text{V}$, $I_D=-10\text{A}$		12.5	15	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-8\text{A}$		16	20	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{DS}=-5\text{V}$, $I_D=-10\text{A}$		25		S
Diode Forward Voltage	V_{SD}	$I_S=-1\text{A}$, $V_{GS}=0\text{V}$		-0.7	-1	V
Maximum Body-Diode Continuous Current	I_S				-3	A
DYNAMIC PARAMETERS						
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}$, $V_{DS}=-20\text{V}$, $f=1\text{MHz}$		2500	3000	pF
Output Capacitance	C_{oss}			260		pF
Reverse Transfer Capacitance	C_{rss}			180		pF
Gate resistance	R_g	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $f=1\text{MHz}$	2.5	4	6	Ω
SWITCHING PARAMETERS						
Total Gate Charge	$Q_g(10\text{V})$	$V_{GS}=-10\text{V}$, $V_{DS}=-20\text{V}$ $I_D=-10\text{A}$		42	55	nC
Total Gate Charge	$Q_g(4.5\text{V})$			18.6		nC
Gate Source Charge	Q_{gs}			7		nC
Gate Drain Charge	Q_{gd}			8.6		nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS}=-10\text{V}$, $V_{DS}=-20\text{V}$ $R_L=2\Omega$, $R_{GEN}=3\Omega$		9.4		ns
Turn-On Rise Time	t_r			20		ns
Turn-Off DelayTime	$t_{D(off)}$			55		ns
Turn-Off Fall Time	t_f			30		ns
Body Diode Reverse Recovery Time	t_{rr}	$I_F=-10\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		38	49	ns
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F=-10\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		47		nC



- A: The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design.
- B: Repetitive rating, pulse width limited by junction temperature.
- C. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.
- D. The static characteristics in Figures 1 to 6 are obtained using $t \leq 300\mu\text{s}$ pulses, duty cycle 0.5% max.
- E. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The SOA curve provides a single pulse rating.
- F. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.
- G. E_{AR} and I_{AR} ratings are based on low frequency and duty cycles to keep $T_J=25^{\circ}\text{C}$.



7.1 Typical characteristic





7.2 Typical characteristic

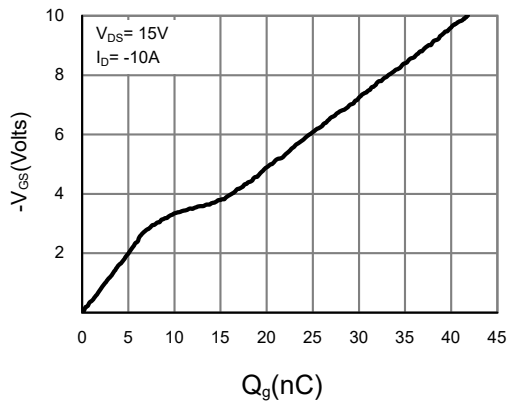


Figure 7: Gate-Charge Characteristics

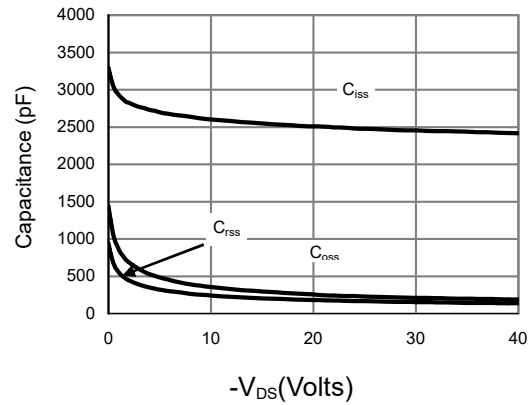


Figure 8: Capacitance Characteristics

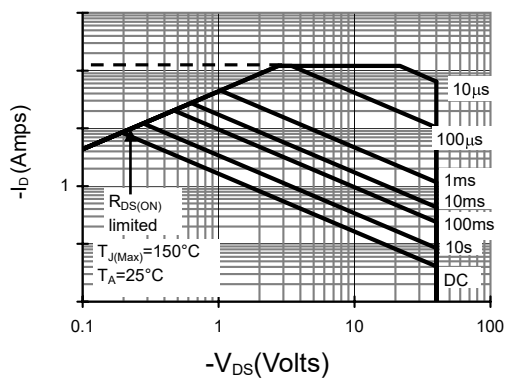


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

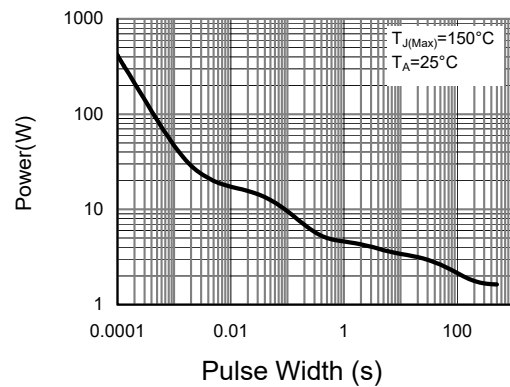


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

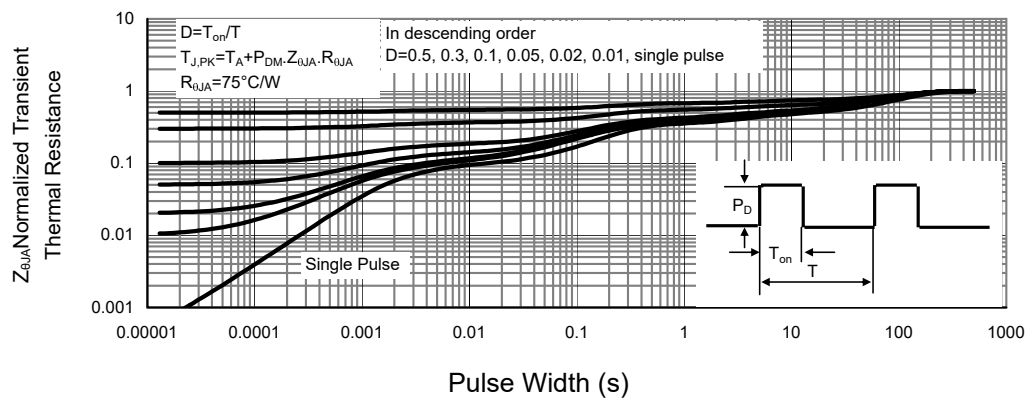
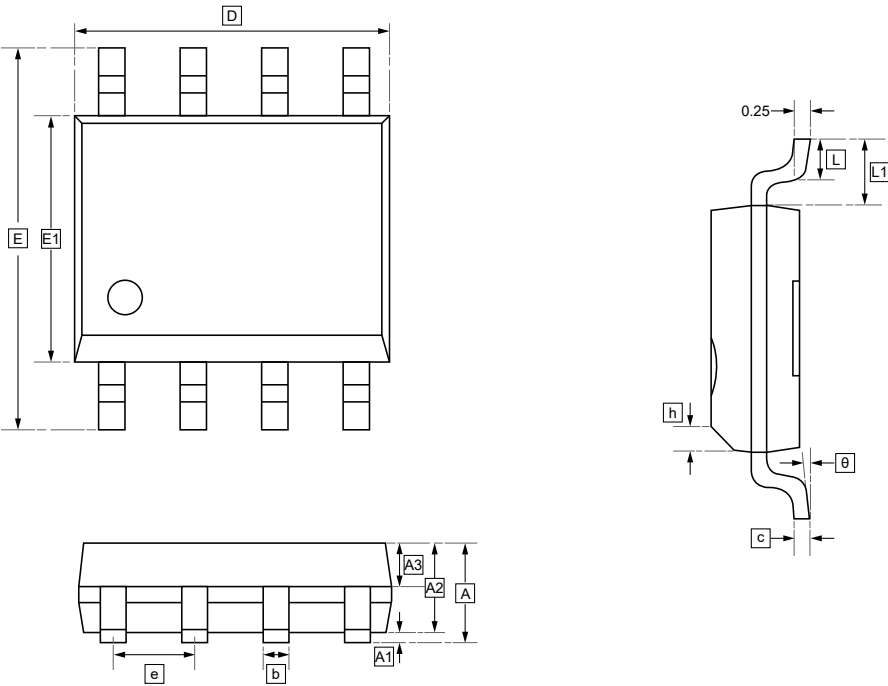


Figure 11: Normalized Maximum Transient Thermal Impedance (Note E)



8.SOP-8 Package Outline Dimensions



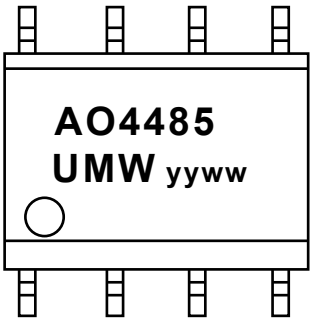
DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	A3	b	c	D	E	E1	e	h	L
Min	-	0.05	1.30	0.60	0.39	0.20	4.80	5.80	3.80	1.24	0.30	0.50
Max	1.75	0.20	1.50	0.70	0.47	0.24	5.00	6.20	4.00	1.30	0.50	0.80

Symbol	L1	θ
Min	1.00	0°
Max	1.10	8°



9.Ordering information



yy: Year Code
ww: Week Code

Order Code	Package	Base QTY	Delivery Mode
UMW AO4485	SOP-8	3000	Tape and reel



10.Disclaimer

UMW reserves the right to make changes to all products, specifications. Customers should obtain the latest version of product documentation and verify the completeness and currency of the information before placing an order.

When applying our products, please do not exceed the maximum rated values, as this may affect the reliability of the entire system. Under certain conditions, any semiconductor product may experience faults or failures. Buyers are responsible for adhering to safety standards and implementing safety measures during system design, prototyping, and manufacturing when using our products to prevent potential failure risks that could lead to personal injury or property damage.

Unless explicitly stated in writing, UMW products are not intended for use in medical, life-saving, or life-sustaining applications, nor for any other applications where product failure could result in personal injury or death. If customers use or sell the product for such applications without explicit authorization, they assume all associated risks.

When reselling, applying, or exporting, please comply with export control laws and regulations of China, the United States, the United Kingdom, the European Union, and other relevant countries, regions, and international organizations.

This document and any actions by UMW do not grant any intellectual property rights, whether express or implied, by estoppel or otherwise. The product names and marks mentioned herein may be trademarks of their respective owners.