

1.Description

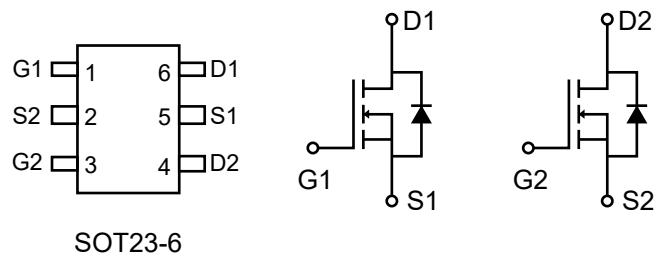
The AO6802 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

2.Product Summary

- $V_{DS(V)}=30V$
- $R_{DS(ON)}<40m\Omega(V_{GS}=10V)$
- $R_{DS(ON)}<55m\Omega(V_{GS}=4.5V)$

3.Pinning information

Pin	Symbol	Description
1,3	G1, G2	GATE
5,2	S1, S2	SOURCE
6,4	D1, D2	DRAIN



4.Absolute Maximum Ratings $T_A = 25^\circ C$

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_A=25^\circ C$	I_D	3.5	A
	$T_A=70^\circ C$		3	
Pulsed Drain Current ^C		I_{DM}	20	
Power Dissipation ^B	$T_A=25^\circ C$	P_D	1.15	W
	$T_A=70^\circ C$		0.73	W
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$



5.Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	t ≤ 10s	R _{θJA}	78	110	°C/W
Maximum Junction-to-Ambient ^{A,D}	Steady-State		150	150	°C/W
Maximum Junction-to-Lead	Steady-State	R _{θJL}	64	80	°C/W



6. Electrical Characteristic ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

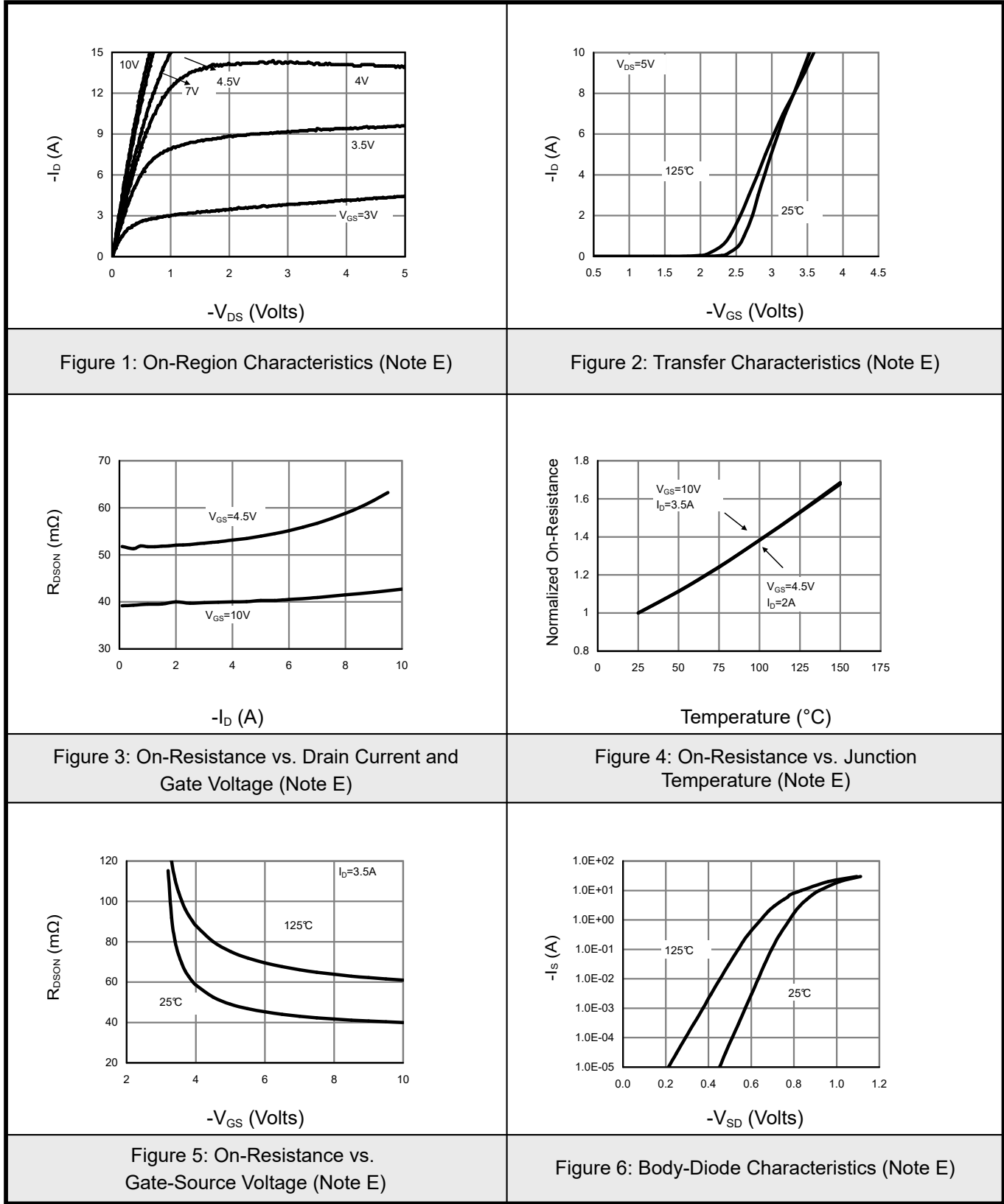
Parameter	Symbol	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$			1	μA
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1.5	2	2.5	V
On state drain current	$I_{D(ON)}$	$V_{GS}=10\text{V}$, $V_{DS}=5\text{V}$	20			A
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=3.5\text{A}$		30	40	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=2\text{A}$		40	55	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{GS}=5\text{V}$, $I_D=3.5\text{A}$		12		S
Diode Forward Voltage	V_{SD}	$I_S=1\text{A}$, $V_{GS}=0\text{V}$		0.79	1	V
Maximum Body-Diode Continuous Current	I_S				1.5	A
DYNAMIC PARAMETERS						
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}$, $V_{DS}=15\text{V}$, $f=1\text{MHz}$		170	210	pF
Output Capacitance	C_{oss}			35		pF
Reverse Transfer Capacitance	C_{rss}			23		pF
Gate resistance	R_g	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $f=1\text{MHz}$	1.7	3.5	5.3	Ω
SWITCHING PARAMETERS						
Total Gate Charge	$Q_g(10\text{V})$	$V_{GS}=10\text{V}$, $V_{DS}=15\text{V}$ $I_D=3.5\text{A}$		4.05	5	nC
Total Gate Charge	$Q_g(4.5\text{V})$			2	3	nC
Gate Source Charge	Q_{gs}			0.55		nC
Gate Drain Charge	Q_{gd}			1		nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS}=10\text{V}$, $V_{DS}=15\text{V}$ $R_L=4.2\Omega$, $R_{GEN}=3\Omega$		4.5		ns
Turn-On Rise Time	t_r			1.5		ns
Turn-Off DelayTime	$t_{D(off)}$			18.5		ns
Turn-Off Fall Time	t_f			15.5		ns
Body Diode Reverse Recovery Time	t_{rr}	$I_F=3.5\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		7.5	10	ns
Body Diode Reverse Recovery Charge	Q_{rr}	$I_F=3.5\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$		2.5		nC



- A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design.
- B. The power dissipation P_D is based on $T_{J(\text{MAX})}=150^{\circ}\text{C}$, using $\leq 10\text{s}$ junction-to-ambient thermal resistance.
- C. Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})}=150^{\circ}\text{C}$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^{\circ}\text{C}$.
- D. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.
- E. The static characteristics in Figures 1 to 6 are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% max.
- F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of $T_{J(\text{MAX})}=150^{\circ}\text{C}$. The SOA curve provides a single pulse rating.



7.1 Typical characteristic





7.2 Typical characteristic

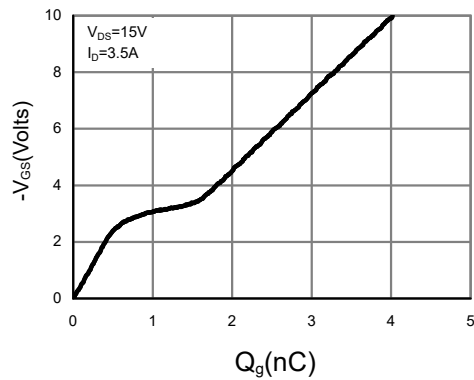


Figure 7: Gate-Charge Characteristics

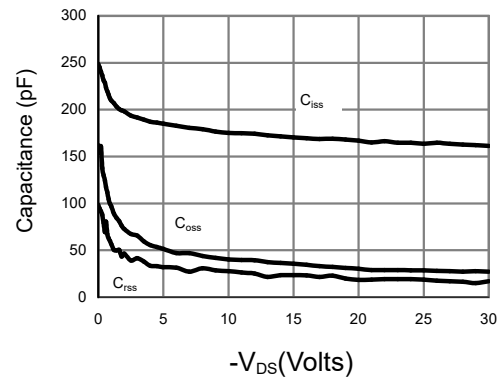


Figure 8: Capacitance Characteristics

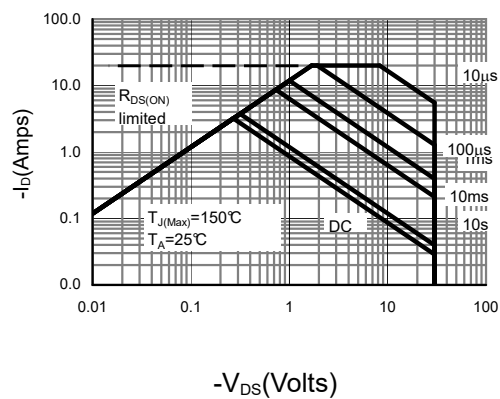


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

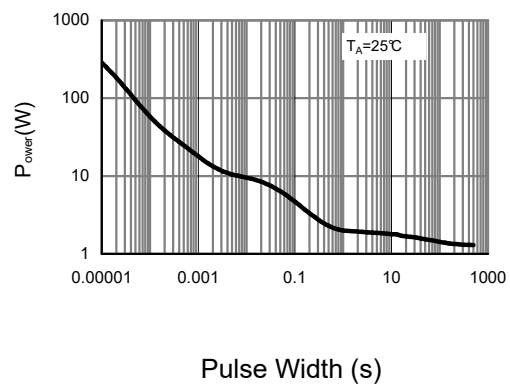


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

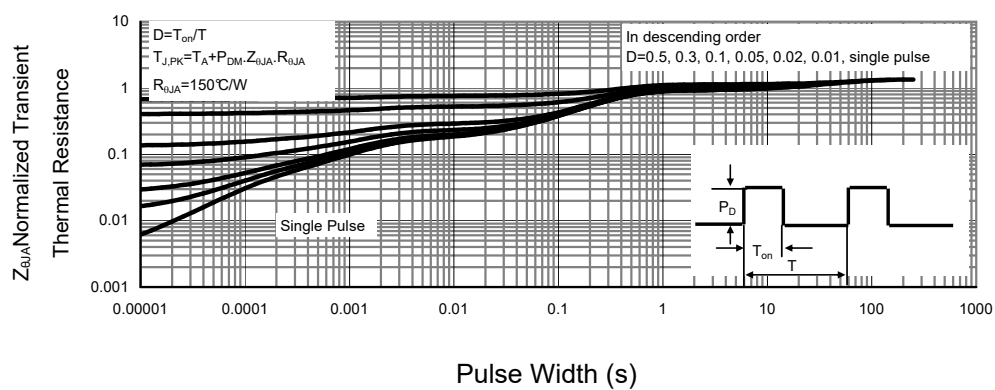
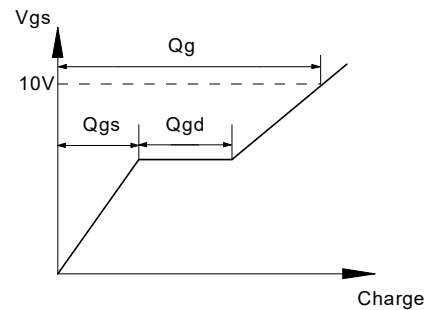
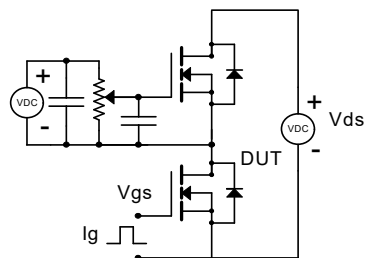


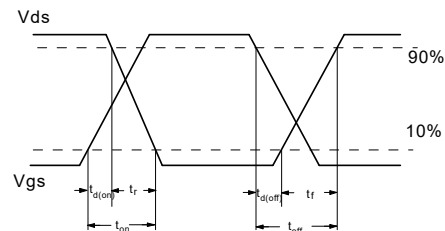
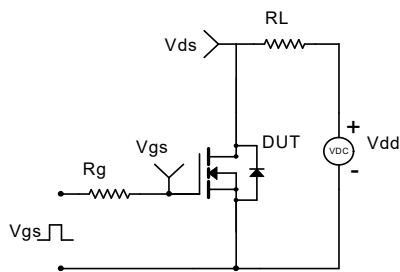
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)



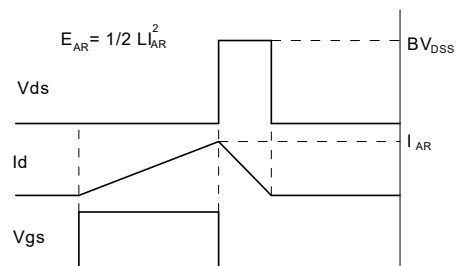
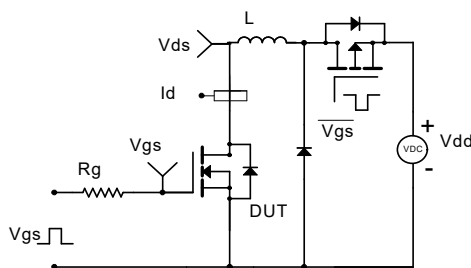
7.3 Typical characteristic



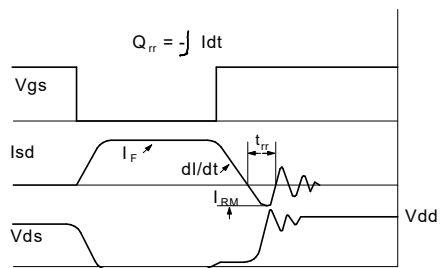
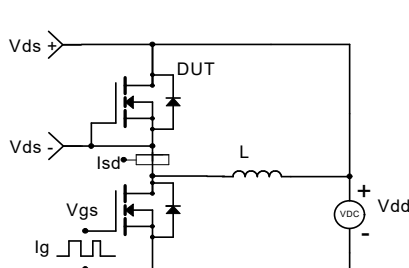
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



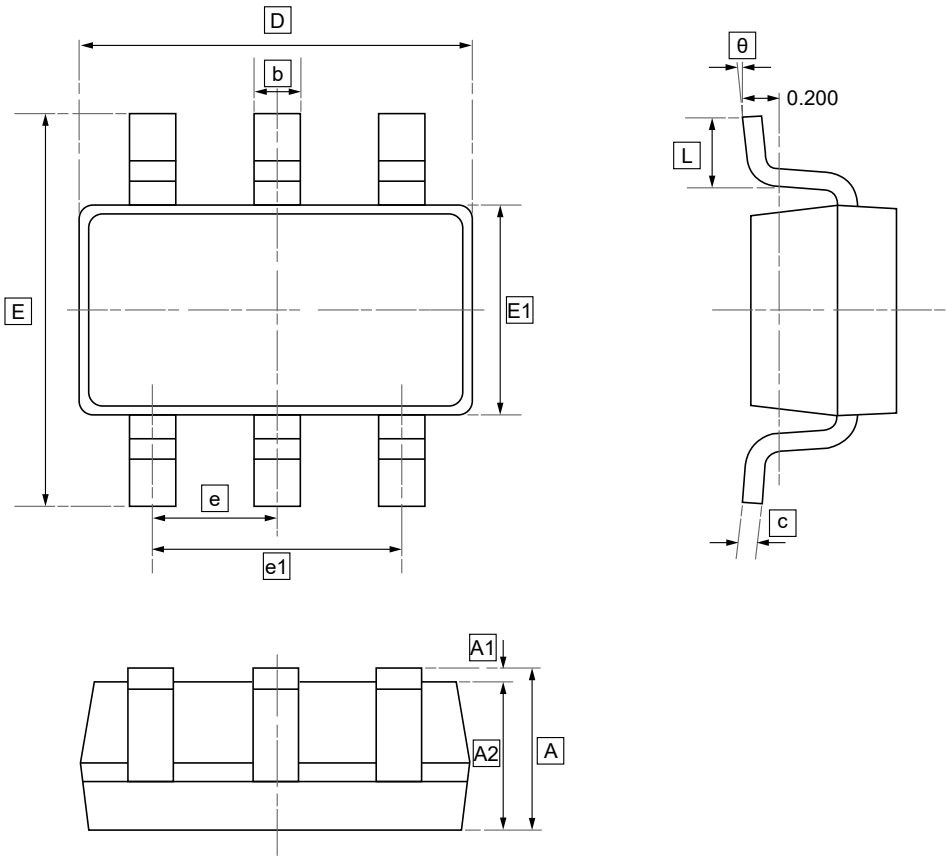
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



8.SOT23-6 Package Outline Dimensions

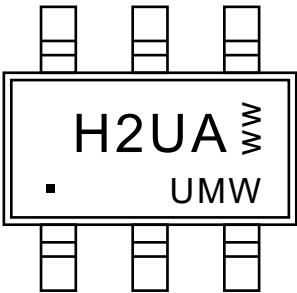


DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	b	c	D	E1	E	e	e1	L	θ
Min	1.050	0.000	1.050	0.300	0.100	2.820	1.500	2.650	0.950	1.800	0.300	0°
Max	1.250	0.100	1.150	0.500	0.200	3.020	1.700	2.950	BSC	2.000	0.600	8°



9.Ordering information



ww: Week Code

Order Code	Package	Base QTY	Delivery Mode
UMW AO6802	SOT23-6	3000	Tape and reel



10.Disclaimer

UMW reserves the right to make changes to all products, specifications. Customers should obtain the latest version of product documentation and verify the completeness and currency of the information before placing an order.

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