UMW IRLZ34N

55V N-Channel MOSFET

1.Description

This benefit, combined with the fast switching speed and ruggedized device design that Power MOSFETs are well knownfor, provides the designerwith an extremely efficient device for use in a wide variety of applications.

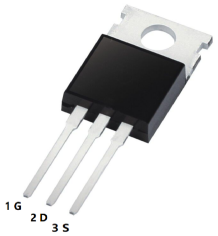
2.Features

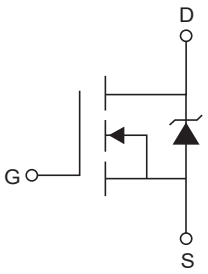
- $V_{DS\ (V)}$=55V
- I_D=30A
- $R_{DS(ON)}$<35mΩ(V_{GS}=10V)

2.Pinning information

Pin	Symbol	Description
1	G	GATE
2	D	DRAIN
3	S	SOURCE

TO-220





3.Absolute Maximum Ratings

Parameter		Symbol	Rating	Units
Continuous Drain Current, V_{GS}=10V	T_C=25°C	I_D	30	A
Continuous Drain Current, V_{GS}=10V	T_C=100°C		21	A
Pulsed Drain Current ①		I_{DM}	110	A
Power Dissipation	T_C=25°C	P_D	68	W
Linear Derating Factor			0.45	W/°C
Gate-to-Source Voltage		V_{GS}	±16	V
Single Pulse Avalanche Energy②		E_{AS}	110	mJ
Avalanche Current①		I_{AR}	16	A
Repetitive Avalanche Energy①		E_{AR}	6.8	mJ
Peak Diode Recovery dv/dt ③		dv/dt	5	V/ns
Storage Temperature Range		T_J, T_{STG}	-55 to 175	°C
Soldering Temperature, for 10 seconds			300 (1.6mm from case)	°C
Mounting torque,6-32 or M3 Screw			10 lbf.in (1.1N.m)	

4.Thermal resistance rating

Parameter	Symbol	Typ	Max	Units
Junction-to-Case	$R_{\theta JC}$		2.2	°C/W
CaSe-to-Sink, Flat, GreaSed Surface	$R_{\theta CS}$	0.5		°C/W
Junction-to-Ambient	$R_{\theta JA}$		62	°C/W

5.Electrical Characteristics @ T_J= 25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=250μA, V_{GS}=0V	55			V
Breakdown Voltage Temp. Coefficient	$\Delta V_{(BR)DSS}/T_J$	I_D=1mA,Reference to 25°C		0.065		V/°C
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=16A ④			35	mΩ
		V_{GS}=5V, I_D=16A ④			46	
		V_{GS}=4V, I_D=14A ④			60	
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA	1		2	V
Forward Transconductance	g_{FS}	V_{DS}=25V, I_D=16A	11			S
Drain-to-Source Leakage Current	I_{DSS}	V_{DS}=55V, V_{GS}=0V			25	μA
		V_{DS}=44V,V_{GS}=0V,T_J=150°C			250	
Gate-to-Source Forward Leakage	I_{GSS}	V_{GS}=16V			100	nA
Gate-to-Source Reverse Leakage		V_{GS}=-16V			-100	
Total Gate Charge	Q_g	I_D=16A			25	nC
Gate-to-Source Charge	Q_{gs}	V_{DS}=44V, V_{GS}=5V			5.2	
Gate-to-Drain ("Miller") Charge	Q_{gd}	See Fig. 6 and 13 ④			14	
Turn-On Delay Time	$t_{D(on)}$	V_{DD}=28V, I_D=16A		8.9		ns
Rise Time	t_r	R_G=6.5Ω, V_{GS}=5V		100		ns
Turn-Off Delay Time	$t_{D(off)}$	R_D=1.8Ω,See Fig. 10 ④		21		ns
Fall Time	t_f			29		ns
Internal Drain inductance	L_D	Between lead, 6mm (0.25in.) from package and center of die contact		4.5		nH
Internal Source inductance	L_S			7.5		
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=25V f = 1.0MHz, See Fig. 5		880		pF
Output Capacitance	C_{oss}			220		pF
Reverse Transfer Capacitance	C_{rss}			94		pF

Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	MOSFET symbol showing the integral reverse p-n junction diode.			30	A
Pulsed Source Current (Body Diode) ①	I_{SM}				110	A
Diode Forward Voltage	V_{SD}	T_J=25°C, I_S=16A, V_{GS}=0V ④			1.3	V
Reverse Recovery Time	t_{rr}	T_J=25°C, I_F=16A dI/dt=100A/μs ④		76	110	ns
Reverse Recovery Charge	Q_{rr}			190	290	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L_S+L_D)				

Notes:

① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)

② V_{DD}=25V, starting T_J=25°C, L=610μH, R_G=25Ω, I_{AS}=16A. (See Figure 12)

③ I_{SD} ≤16A, di/dt ≤270A/μs, V_{DD} ≤ $V_{(BR)DSS}$,T_J ≤ 175°C

④ Pulse width ≤ 300μs; duty cycle ≤ 2%

6.1Typical Characterisitics

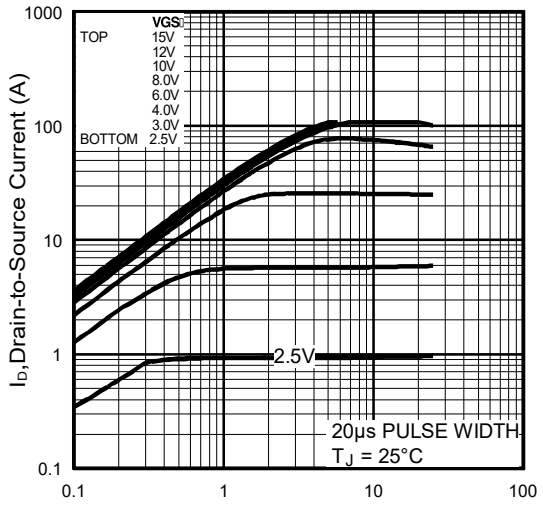


Figure 1: Typical Output Characteristics

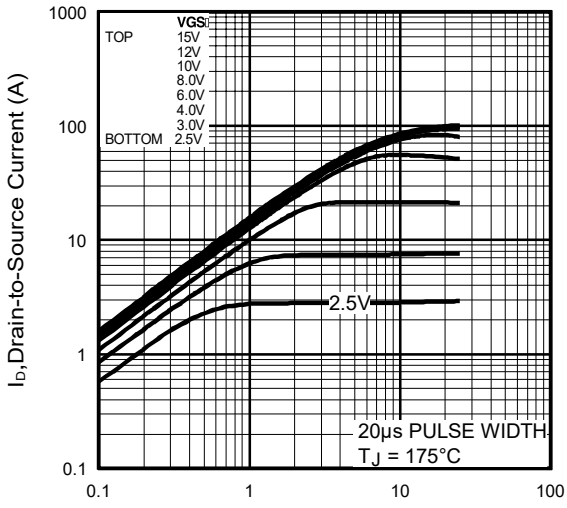


Figure 2: Typical Output Characteristics

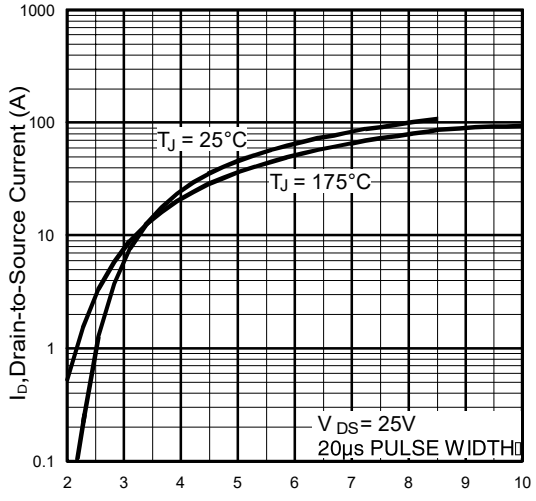


Figure 3: Typical Transfer Characteristics

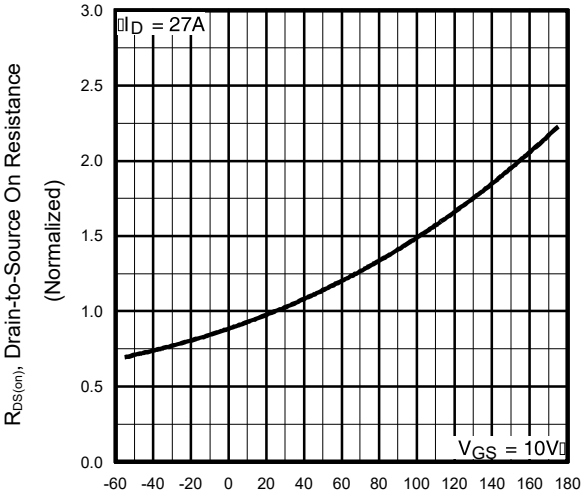
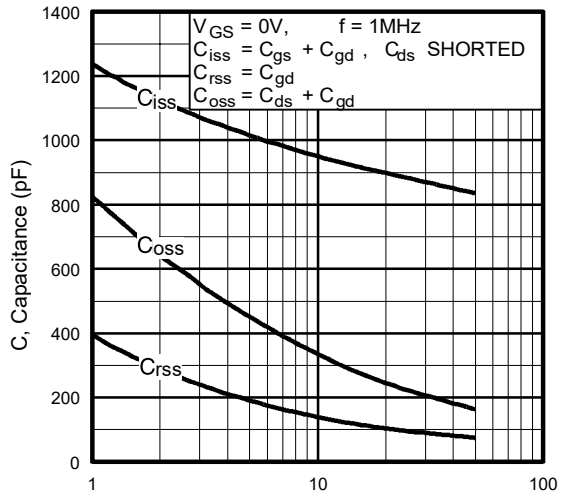


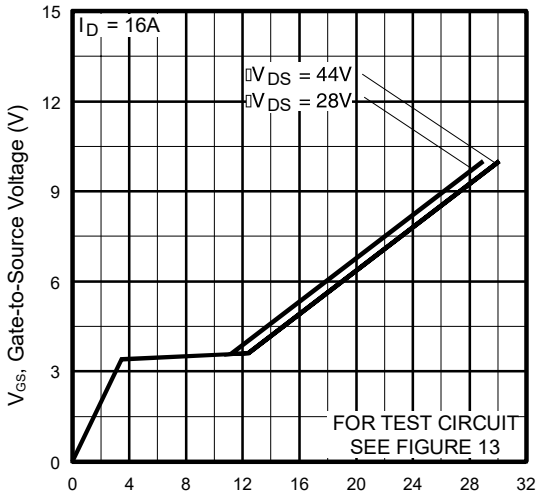
Figure 4: Nomalized On-ResistanceVs. Temperature

6.2Typical Characterisitics



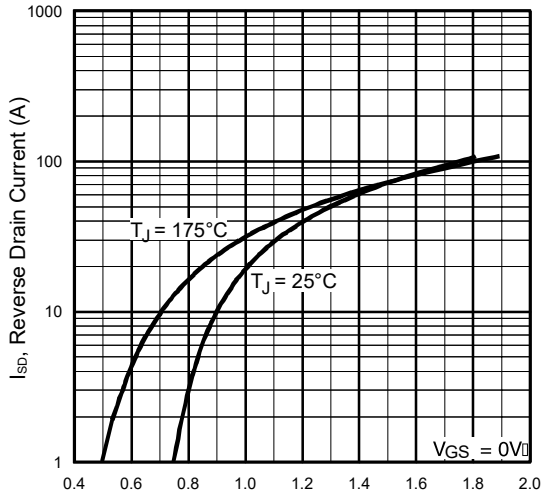
V_{DS} ,Drain-to-Source Voltage (V)

Figure 5: Typical Capacitance Vs. Drain-to-Source Voltage



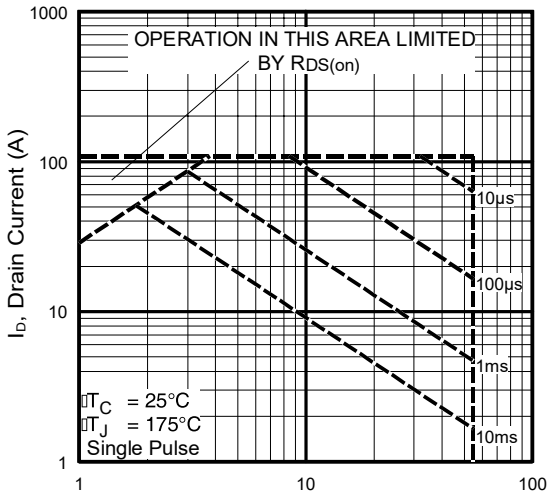
Q_G, Total Gate Charge (nC)

Figure 6: Typical Gate Charge Vs. Gate-to-Source Voltage



V_{SD}, Source-to-Drain Voltage (V)

Figure 7: Typical Source-Drain Diode Forward Voltage



V_{DS}, Drain-to-Source Voltage (V)

Figure 8: Maximum Safe Operating Area

6.3Typical Characterisitics

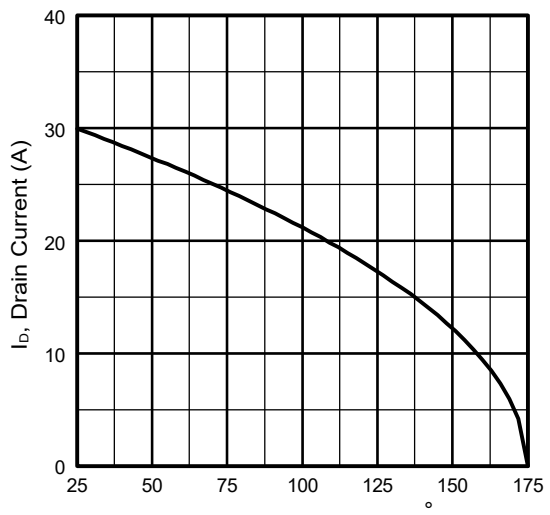


Figure 9: Maximum Drain Current Vs. Case Temperature

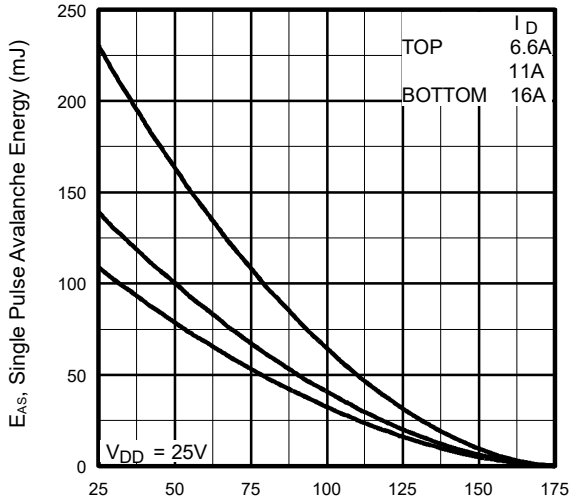


Figure 10: Maximum Avalanche Energy Vs. Drain Current

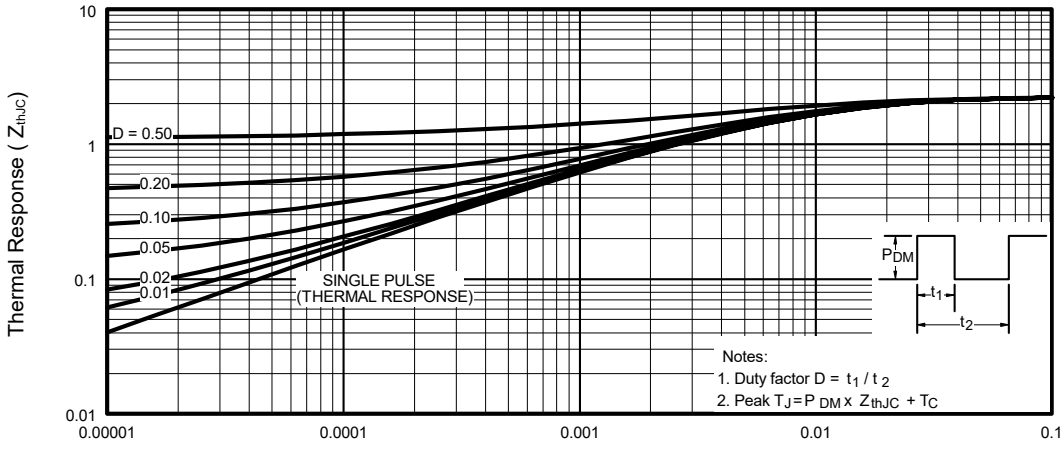


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

6.4Typical Characterisitics

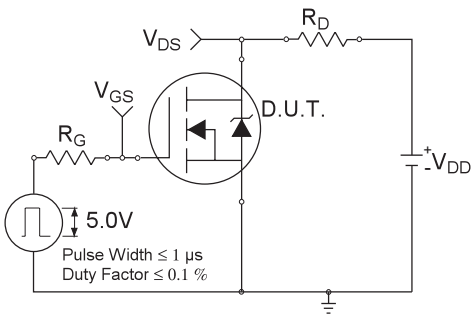


Figure 12a: Switching Time Test Circuit

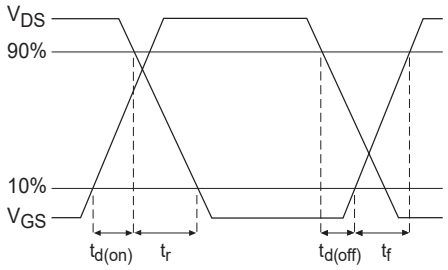


Figure 12b: Switching Time waveforms

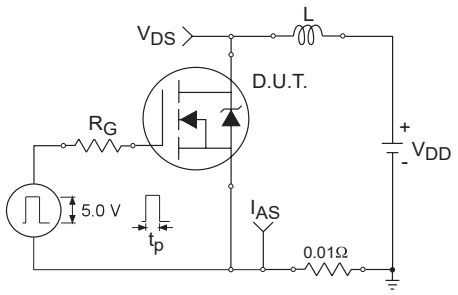


Figure 13a: Unclamped Inductive Test Circuit

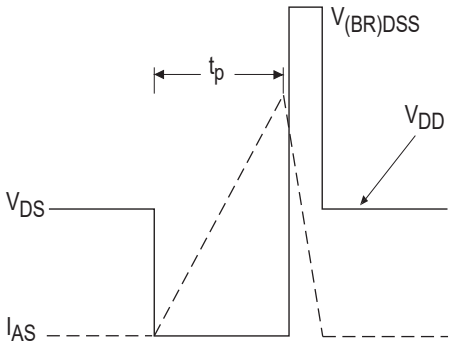


Figure 13b: Unclamped Inductive waveforms

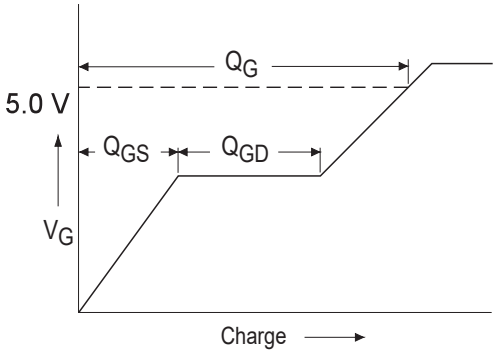


Figure 14a: Basic Gate Charge waveform

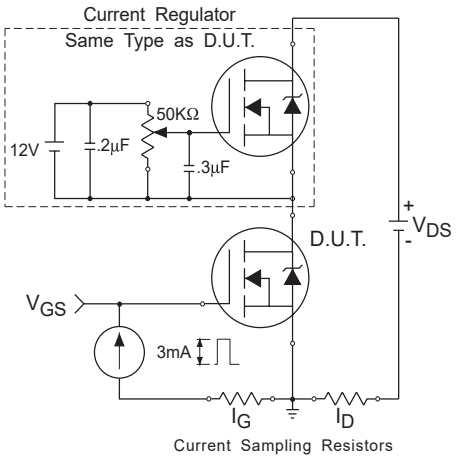
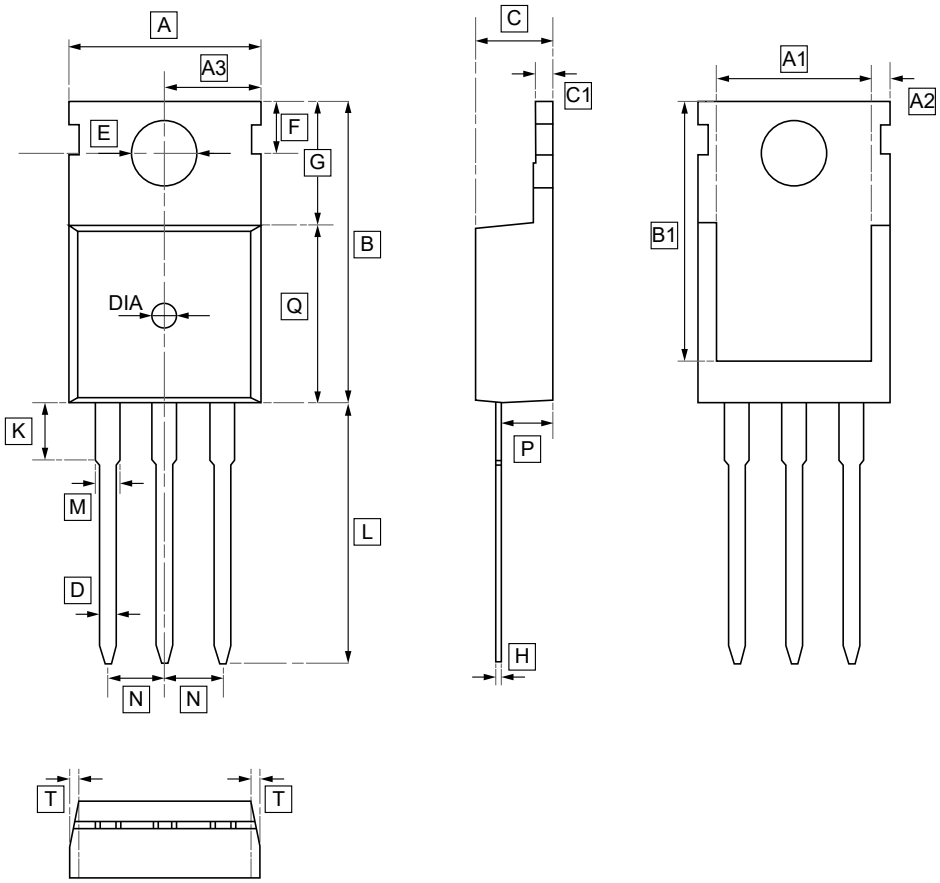


Figure 14b: Gate Charge Test Circuit

7.TO-220 Package Outline Dimensions

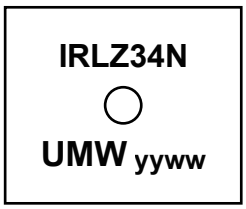


DIMENSIONS (mm are the original dimensions)

Symbol	A	A1	A2	A3	B	B1	C	C1	D	E	F	G
Min	9.7	8.44	1.05	4.8	15.4	12.9	4.28	1.1	0.6	3.4	2.65	5.2
Max	10.3	8.84	1.25	5.2	16.2	13.5	4.68	1.5	1.0	3.8	3.25	5.8

Symbol	H	K	L	L1	M	N	P	Q	T	DIA
Min	0.4	2.9	12.8	2.7	1.15	2.49	2.1	8.7	W:0.35	⊙1.5
Max	0.6	3.3	13.6	3.3	1.35	2.59	2.7	9.3		(deep 0.2)

8.Ordering information



yy: Year Code
ww: Week Code

Order Code	Package	Base QTY	Delivery Mode
UMW IRLZ34N	TO-220	1000	Tube and box

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