

Description

TSS Series are designed to protect baseband equipment such as modems, line cards, CPE and DSL equipments from damaging overvoltage transients. The series provide a robust peak surge current capability which enables equipments to comply with global regulatory standards.

Features

- Low voltage overshoot
- Low on-state voltage
- Does not degrade in capability after multiple surge events within limit.
- Low capacitance
- Fails short circuit when surged in excess of ratings
- Pb-free E3 means 2nd level interconnect is Pb-free and the terminal finish material is tin(Sn) (IPC/JEDEC J-STD609A.01)

Mechanical Characteristics

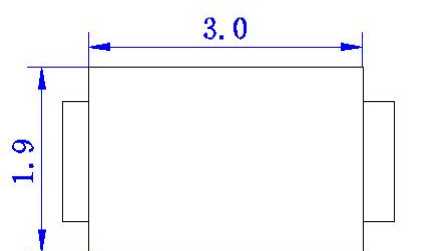
Package: SMF/SOD-123FL

- Case Material: "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Polarity: Color band denotes cathode except bi-directional models
- Weight: 0.017g
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

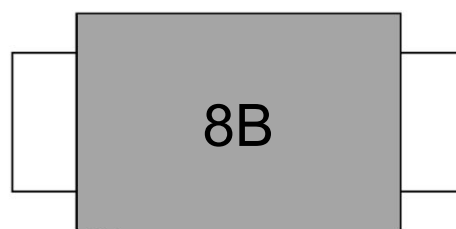
- Audio/Video line
- Network and telecom
- Data lines and security systems
- Serial ports

Dimensions & Symbol (Unit: mm Max)



Bi-directional

Marking Information



8B: P0080FB Marking code

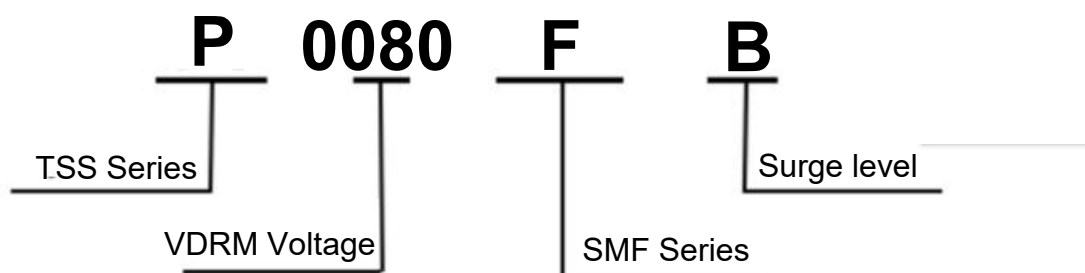
Electrical Characteristics (T=25°C)

Part Number	I _{DRM} @V _{DRM}		V _s @I _s		V _T @ I _T		I _H	C _o
	μA	V	V	mA	V	A	mA	pF
	max	min	max	max	max	max	min	max
P0080FB	5	6	25	800	4	2.2	40	50

Notes:

- All measurements are made at an ambient temperature of 25°C. I_{PP} applies to -40°C through +85°C temperature range.
- Off-state capacitance (C_o) is measured at 1 MHz with a 2 V bias and is typical val

Part number code



Absolute Maximum Ratings(T=25°C, RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
Operating junction temperature range	T _j	-40 to +125	°C
Storage temperature range	T _{stg}	-65 to +150	°C
Repetitive peak pulse Voltage(10/700uS)	V _{PP}	4000	V

Typical Characteristics

FIG1: V-I cure characteristics

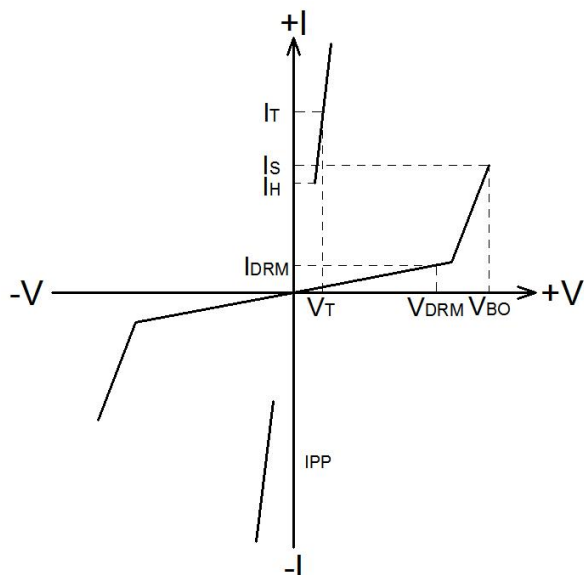


FIG2: Pulse Waveform

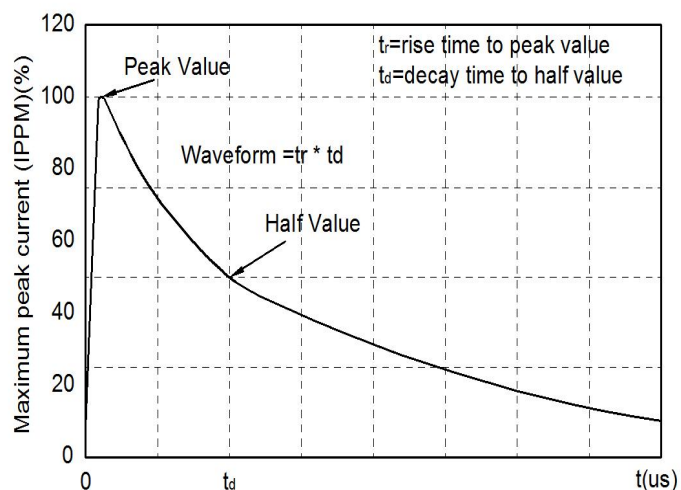


FIG3: Normalized V_s change vs.junction temperature

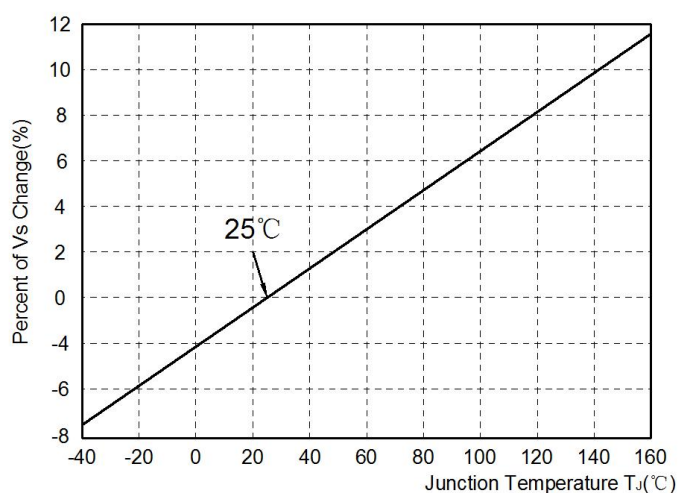
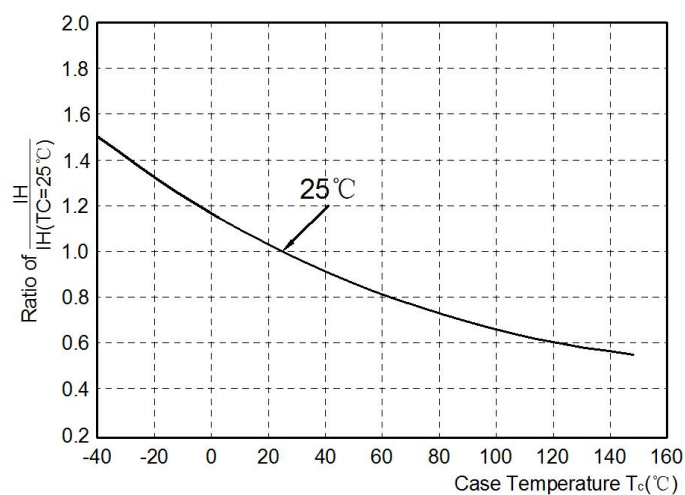
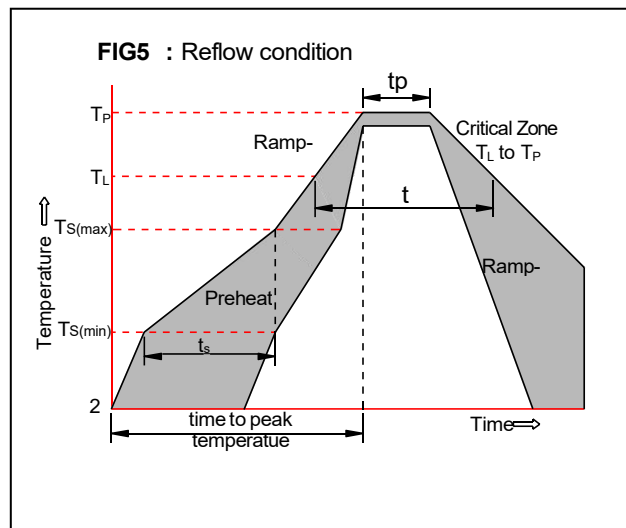


FIG4: Normalized DC holding current vs.case temperature

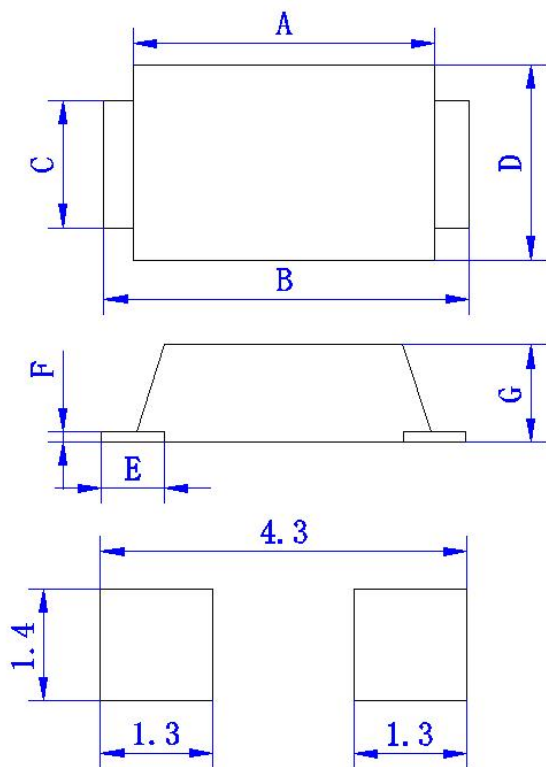


Soldering parameters

Reflow Condition		Pb-Free assembly (see as below)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150°C
	-Temperature Max($T_{s(max)}$)	+200°C
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3°C/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature(T_L)(Liquid us)	+217°C
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_P)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_p)		30 secs. Max
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp (T_P)		8 min. Max
Do not exceed		+260°C

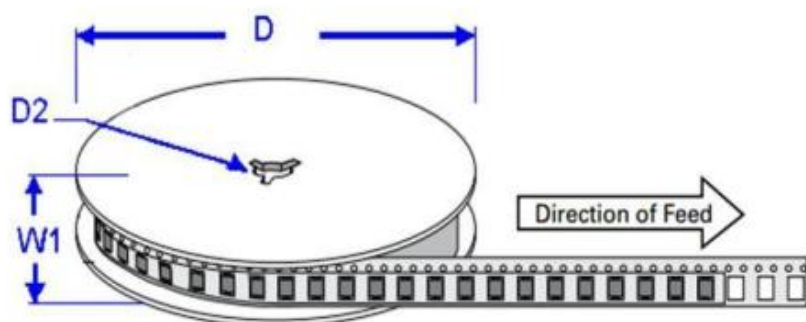
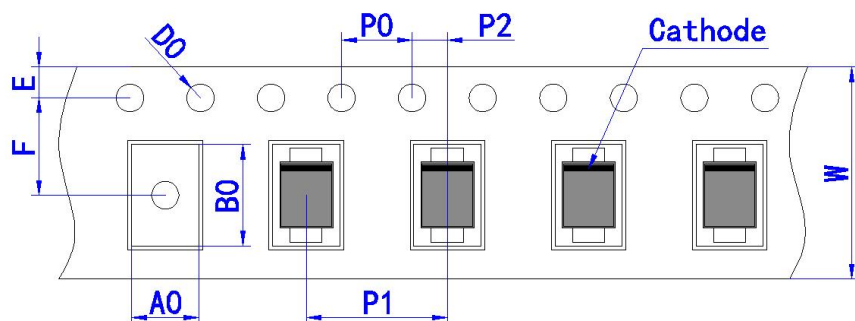


Package mechanical data



Ref.(mm)	Millimeters	
	Min.	Max.
A	2.5	3.0
B	3.4	4.0
C	0.7	1.1
D	1.5	1.9
E	0.45	0.95
F	0.05	0.26
G	0.9	1.1

Tape & reel specification - SOD-123FL



Ref.	Millimeters
A0	2.15±0.20
B0	3.95±0.20
C	178.00
D0	1.55±0.10
E	1.75±0.20
E1	13.50±1.00
F	3.50±0.10
P0	4.00±0.20
P1	4.00±0.20
P2	2.00±0.20
W	8.00±0.30
W1	9.00±4.00