

T410-800SW 4A TRIAC

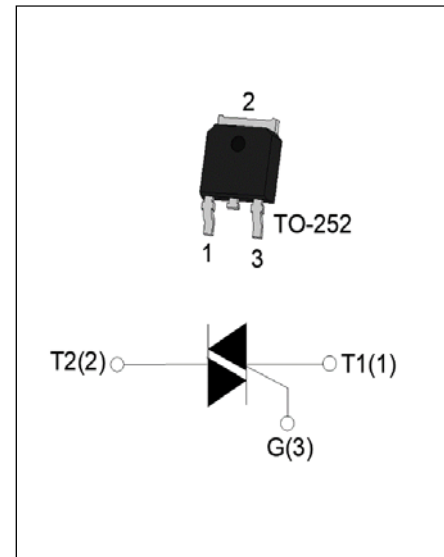
Rev.A.2.0

DESCRIPTION

The T410-800SW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Compared to traditional triacs, T410-800SW provides a very high switching capability up to junction temperatures of 150°C. It can be driven directly through the MCU I/O port. Package TO-252 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM}/V_{RRM}	800	V
$I_{GT\ I/II/III}$	10/10/10	μA



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-150	°C
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	800	V
RMS on-state current ($T_c \leq 128^\circ C$)	$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (full cycle , $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	40	A
Non repetitive surge peak on-state current (full cycle , $t_p=16.6ms$, $T_j=25^\circ C$)		44	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	8	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100Hz$, $T_j=150^\circ C$)	di/dt	50	$A/\mu s$
Peak gate current ($t_p=20\mu s$, $T_j=150^\circ C$)	I_{GM}	4	A
Average gate power dissipation ($T_j=150^\circ C$)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25^\circ C$; non-repetitive, off-state)	V_{pp}	3	kV

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ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	I - II -III	MAX.	10	mA
V_{GT}		I - II -III	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=150^{\circ}\text{C}$ $R_L=3.3\text{K}\Omega$	I - II -III	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I -III	MAX.	20	mA
		II		35	
I_H	$I_T=100\text{mA}$		MAX.	20	mA
dV/dt	$V_D=540\text{V}$ Gate Open $T_j=150^{\circ}\text{C}$		MIN.	150	V/ μs
$(dI/dt)_c$	$(dV/dt)_c=20\text{V}/\mu\text{s}$, $T_j=150^{\circ}\text{C}$		MIN.	1	A/ms
t_{on}	$I_G=20\text{mA}$ $I_A=200\text{mA}$ $I_R=20\text{mA}$ $T_j=25^{\circ}\text{C}$		TYP.	2.5	μs
t_{off}				25	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5.5\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.4	V
V_{TO}	Threshold voltage	$T_j=150^{\circ}\text{C}$	0.6	V
R_D	Dynamic resistance	$T_j=150^{\circ}\text{C}$	129	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=150^{\circ}\text{C}$	1	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (DC)	4.3	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (DC)	120	$^{\circ}\text{C}/\text{W}$

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FIG.1: Maximum power dissipation versus RMS on-state current

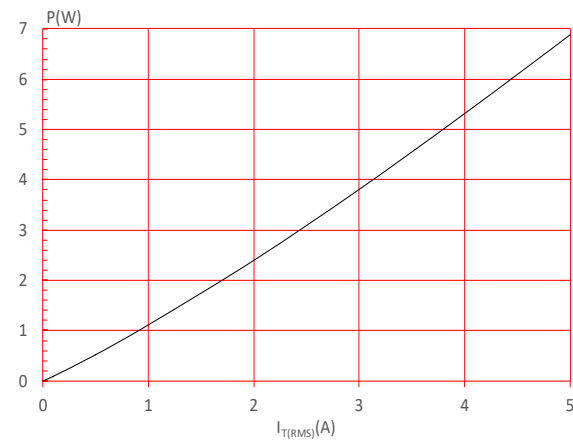


FIG.2: RMS on-state current versus case temperature

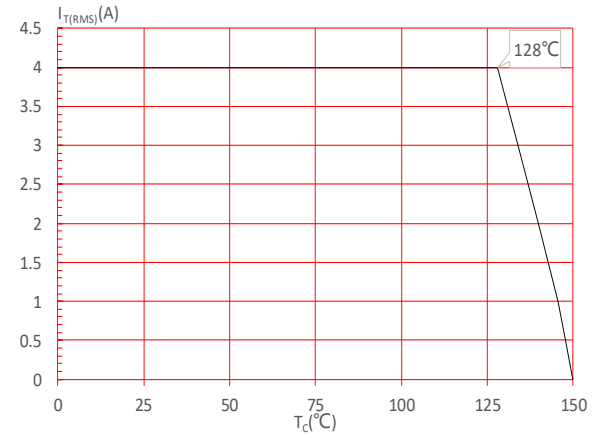


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35μm)(full cycle)

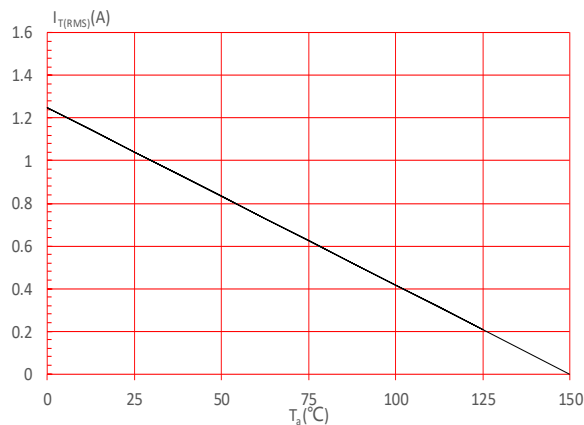


FIG.4: Surge peak on-state current versus number of cycles

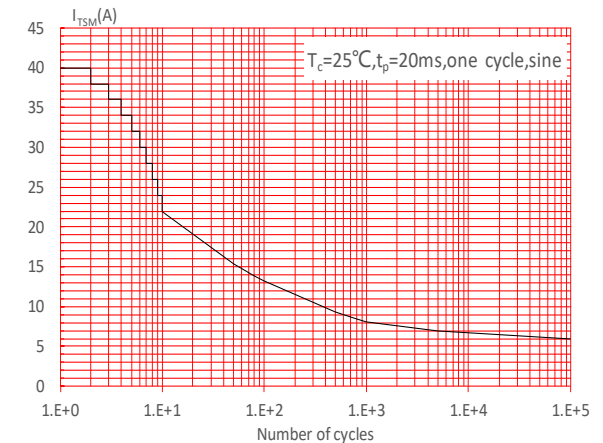


FIG.5: On-state characteristics

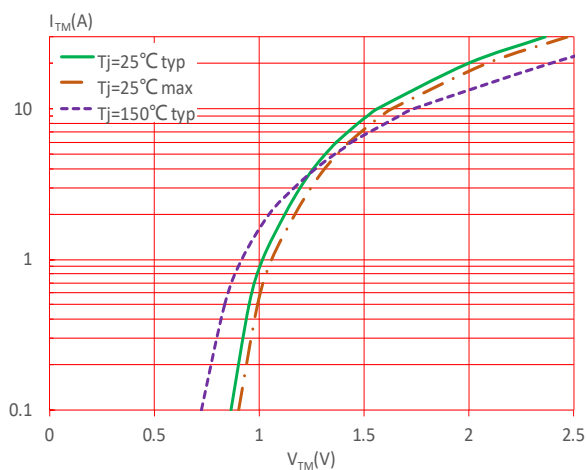
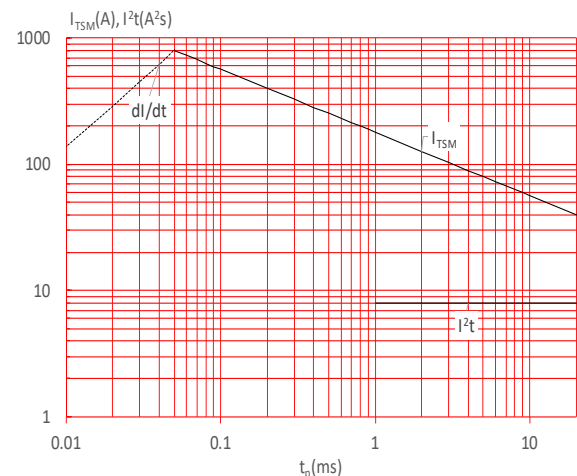


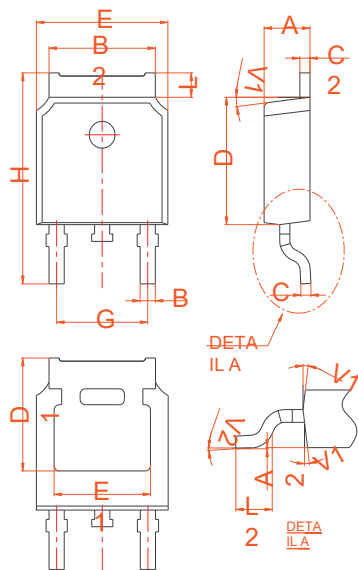
FIG.6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t ($di/dt < 50A/\mu s$)



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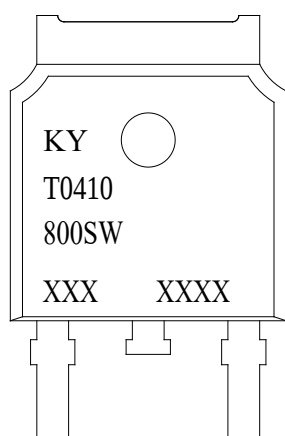
PACKAGE MECHANICAL DATA



TO-252

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.03		0.23	0.001		0.009
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
C	0.45		0.55	0.018		0.022
C2	2.70		2.90	0.106		0.114
D	6.00		6.20	0.236		0.244
E	6.40		6.70	0.252		0.264
G	4.40		4.70	0.173		0.185
H	9.35		10.6	0.368		0.417
L1	1.30		1.70	0.051		0.067
L2	1.37		1.50	0.054		0.059
L3		0.8			0.031	
L4		0.8			0.031	
V1		4°			4°	
V2	0°		8°	0°		8°

MARKING



XXX XXXX

Product
Code

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PACKAGE	OUTLINE	Base qty (PCS)	INNER BOX (PCS)	TAPE & REEL (PCS)
TO-252	TAPING	2,500	25,000	13 inch

Document Revision History

Date	Revision	Changes
Jun.22, 2025	A.2.0	Last updated

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