### 1A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

### **FEATURES:**

Glass Passivated Chip Junction

Reverse Voltage - 100 to 1000 V

Forward Current - 1 A

High Surge Current Capability

**Designed for Surface Mount Application** 

### **MECHANICAL DATA**

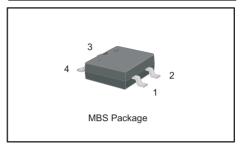
· Case: MBS

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 100mg / 0.0035oz

### **PINNING**

PIN	DESCRIPTION	
1	Input Pin ( ~ )	
2	Input Pin ( ~ )	
3	Output Anode ( + )	
4	4 Output Cathode ( - )	



### Maximum Ratings and Electrical characteristics

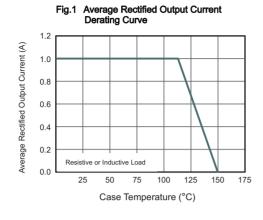
Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	MB1S-10	MB2S-10	MB4S-10	MB6S-10	MB8S-10	MB10S-10	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	100	200	400	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	100	200	400	600	800	1000	V
Average Rectified Output Current @ Fig.1	Io	1.0						Α
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	I <sub>FSM</sub>	35						А
Peak Forward Surge Current 1.0 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	I <sub>FSM</sub>	70						А
I²t Rating for fusing(3ms≤t≤8.3ms)	l <sup>2</sup> t	5.1						A <sup>2</sup> S
Maximum Forward Voltage at 1.0 A	V <sub>F</sub>	1.1						V
Maximum DC Reverse Current @T <sub>A</sub> =25 °C at Rated DC Blocking Voltage @T <sub>A</sub> =125 °C	I <sub>R</sub>	5 100					μA	
Typical Junction Capacitance ( Note1 )	C <sub>j</sub>	7						pF
Typical Thermal Resistance ( Note2 )	$R_{ ext{ iny BJA}} \ R_{ ext{ iny BJC}} \ R_{ ext{ iny BJL}}$	45 15 25						°C/W
Operating and Storage Temperature Range	$T_j$ , $T_{stg}$	-55 ~ <b>+</b> 150					°C	

Note: 1. Measured at 1MHz and applied reverse voltage of 4 V D.C.

2. Mounted on glass epoxy PC board 4X1.5" X 1.5" (3.81 X 3.81 cm) copper pad.





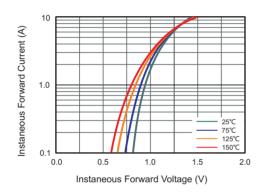


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

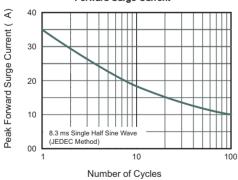


Fig.2 Typical Instaneous Reverse Characteristics

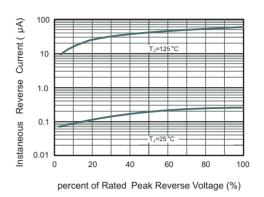
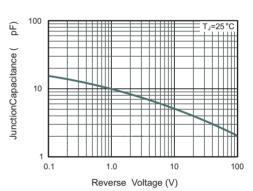


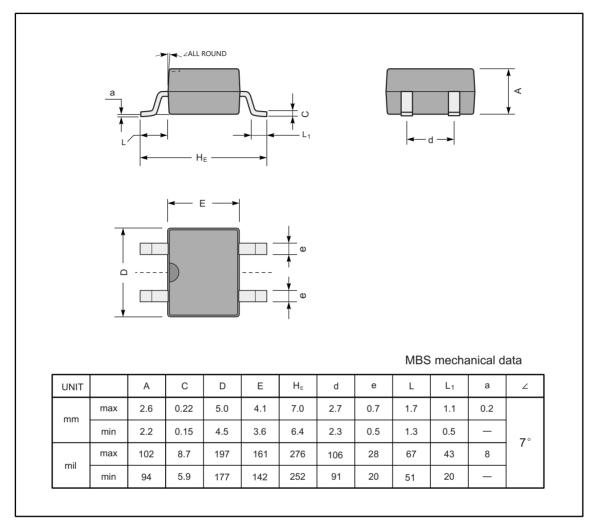
Fig.4 Typical Junction Capacitance



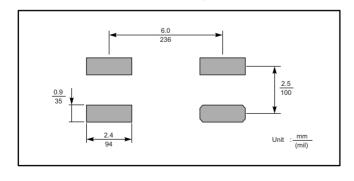
# PACKAGE OUTLINE

# Plastic surface mounted package; 4 leads

## **MBS**



# The recommended mounting pad size



## Marking

Type number	Marking code				
MB1S-10	10S1				
MB2S-10	10S2				
MB4S-10	10S4				
MB6S-10	10S6				
MB8S-10	10S8				
MB10S-10	10S10				

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