



Description

The DMN3009LFVQ-13 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 30V$ $I_D = 90A$

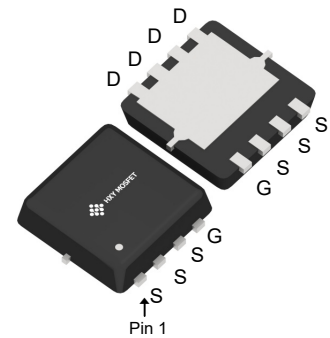
$R_{DS(ON)} < 4.6\ m\Omega$ @ $V_{GS}=10V$

Application

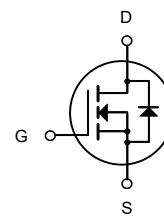
Battery protection

Load switch

Uninterruptible power supply



DFN3X3-8L



N-Channel MOSFET

Package Marking and Ordering Information

| Product ID | Pack | Brand | Qty(PCS) |
|----------------|-----------|------------|----------|
| DMN3009LFVQ-13 | DFN3X3-8L | HXY MOSFET | 5000 |

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|----------------------|---|------------|--------------|
| V_{DS} | Drain-Source Voltage | 30 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| $I_D@T_C=25^\circ C$ | Continuous Drain Current, V_{GS} @ 10V ¹ | 90 | A |
| $I_D@T_C=75^\circ C$ | Continuous Drain Current, V_{GS} @ 10V ¹ | 45 | A |
| I_{DM} | Pulsed Drain Current ² | 290 | A |
| E_{AS} | Single Pulse Avalanche Energy ³ | 196 | mJ |
| I_{AS} | Avalanche Current | 36 | A |
| $P_D@T_C=25^\circ C$ | Total Power Dissipation ⁴ | 46 | W |
| T_{STG} | Storage Temperature Range | -55 to 175 | $^\circ C$ |
| T_J | Operating Junction Temperature Range | -55 to 175 | $^\circ C$ |
| $R_{\theta JA}$ | Thermal Resistance Junction-ambient ¹ | 62 | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case ¹ | 1.72 | $^\circ C/W$ |



Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|------------------------------|--|--|------|------|-----------|-----------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS}=0V$, $I_D=250\mu A$ | 30 | --- | --- | V |
| $\Delta BV_{DSS}/\Delta T_J$ | BV_{DSS} Temperature Coefficient | Reference to 25°C , $I_D=1\text{mA}$ | --- | --- | --- | $V/^{\circ}\text{C}$ |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance ² | $V_{GS}=10V$, $I_D=30A$ | --- | 3.5 | 4.6 | $m\Omega$ |
| | | $V_{GS}=4.5V$, $I_D=15A$ | --- | 7.8 | 10 | |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}=V_{DS}$, $I_D=250\mu A$ | 1.2 | 1.6 | 2.5 | V |
| $\Delta V_{GS(th)}$ | $V_{GS(th)}$ Temperature Coefficient | | --- | --- | --- | $mV/^{\circ}\text{C}$ |
| I_{DSS} | Drain-Source Leakage Current | $V_{DS}=30V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$ | --- | --- | 1 | μA |
| | | $V_{DS}=30V$, $V_{GS}=0V$, $T_J=100^{\circ}\text{C}$ | --- | --- | 100 | |
| I_{GSS} | Gate-Source Leakage Current | $V_{GS}=\pm 20V$, $V_{DS}=0V$ | --- | --- | ± 100 | nA |
| g_{fs} | Forward Transconductance | $V_{DS}=10V$, $I_D=30A$ | --- | 80 | --- | S |
| R_g | Gate Resistance | $V_{DS}=0V$, $V_{GS}=0V$, $f=1\text{MHz}$ | --- | 2 | --- | Ω |
| Q_g | Total Gate Charge | $V_{DS}=15V$, $V_{GS}=4.5V$, $I_D=30A$ | --- | 20 | --- | nC |
| Q_{gs} | Gate-Source Charge | | --- | 5 | --- | |
| Q_{gd} | Gate-Drain Charge | | --- | 7.2 | --- | |
| $T_{d(on)}$ | Turn-On Delay Time | $V_{GS}=10V$, $V_{DD}=15V$, $R_G=3\Omega$, $I_D=30A$ | --- | 9 | --- | ns |
| T_r | Rise Time | | --- | 16 | --- | |
| $T_{d(off)}$ | Turn-Off Delay Time | | --- | 43 | --- | |
| T_f | Fall Time | | --- | 12 | --- | |
| C_{iss} | Input Capacitance | $V_{DS}=15V$, $V_{GS}=0V$, $f=1\text{MHz}$ | --- | 2088 | --- | pF |
| C_{oss} | Output Capacitance | | --- | 277 | --- | |
| C_{rss} | Reverse Transfer Capacitance | | --- | 209 | --- | |

Diode Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|----------|--|---|------|------|------|------|
| I_S | Continuous Source Current ^{1,5} | $V_G=V_D=0V$, Force Current | --- | --- | 90 | A |
| V_{SD} | Diode Forward Voltage ² | $V_{GS}=0V$, $I_S=1A$, $T_J=25^{\circ}\text{C}$ | --- | --- | 1.2 | V |

Note :

F The data is tested by surface mounted on a 1/4 inch²⁴ FR-4 board with 20Z copper.

G The data is tested by pulsed pulse width $\leq 300\mu s$ duty cycle $\leq 2\%$

H The EAS data shows Max. rating. The test condition is $V_{RMS} \gg V_{DD}=24V$, $V_{GS}=10V$, $L=0.1mH$, $I_{AS}=36A$.

I The power dissipation is limited by 50°C junction temperature

í The data is theoretically the same as $I_{D(on)}$ and $I_{D(on)max}$. In real applications, it should be limited by total power dissipation.



Typical Characteristics

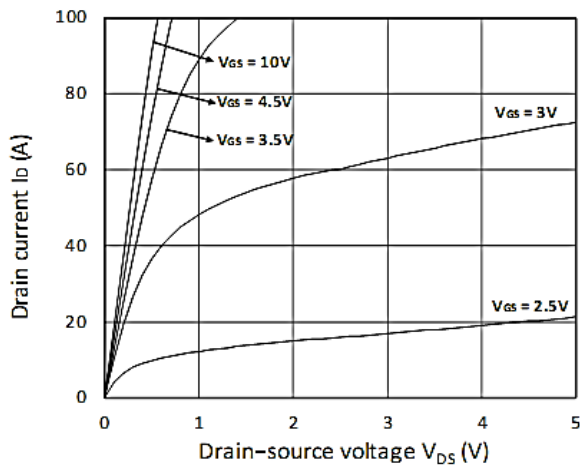


Figure 1. Output Characteristics

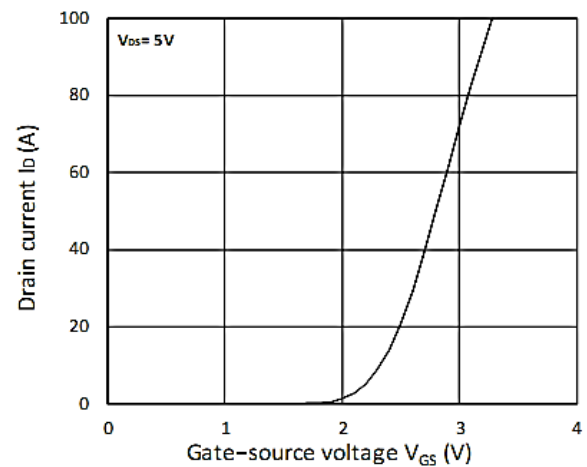


Figure 2. Transfer Characteristics

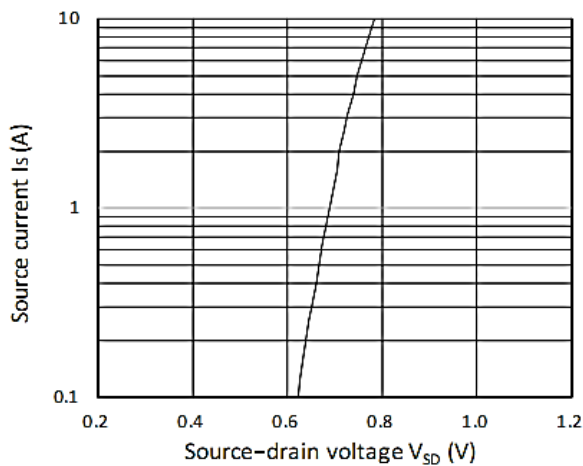


Figure 3. Forward Characteristics of Reverse

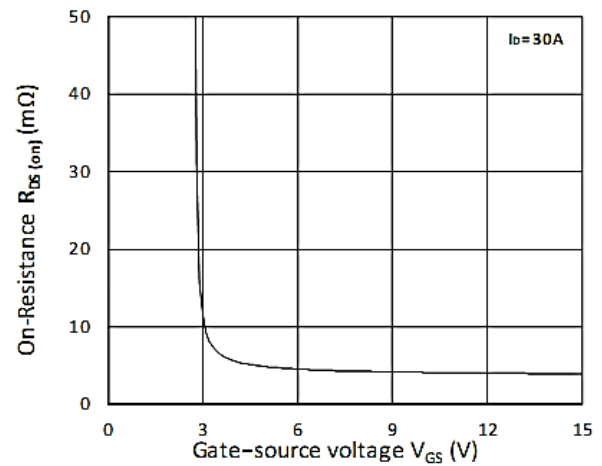


Figure 4. R_DS(ON) vs. V_GS

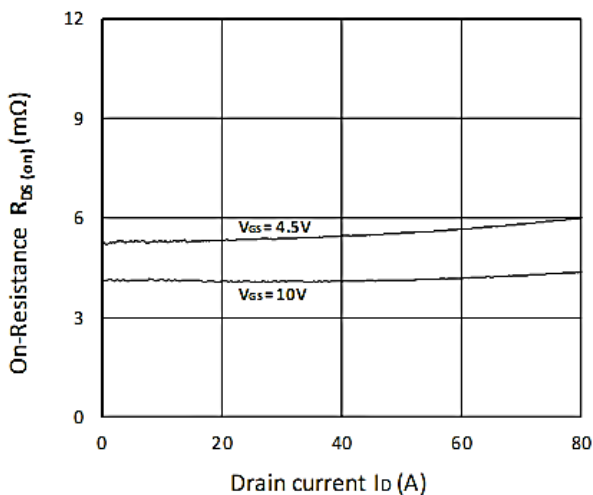


Figure 5. R_DS(ON) vs. I_D

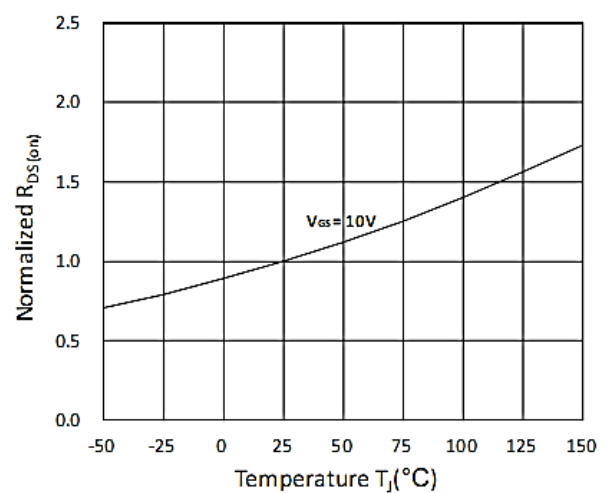


Figure 6. Normalized R_DS(on) vs. Temperature

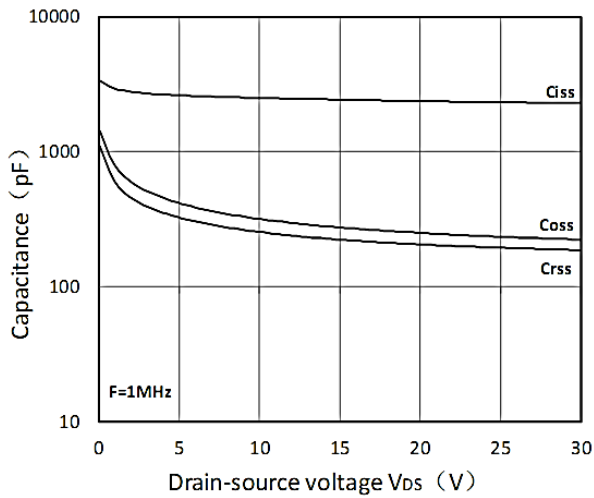


Figure 7. Capacitance Characteristics

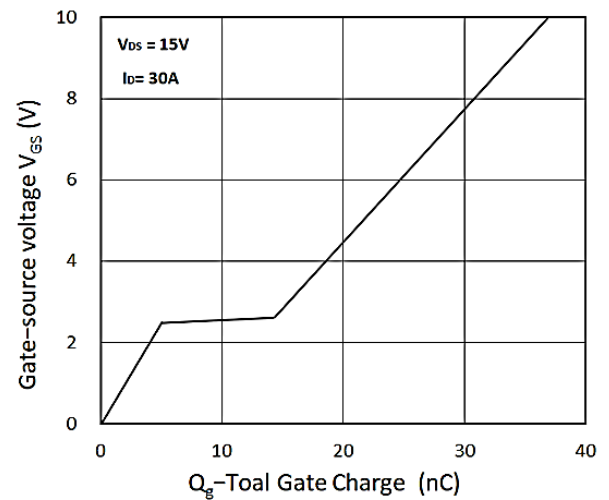


Figure 8. Gate Charge Characteristics

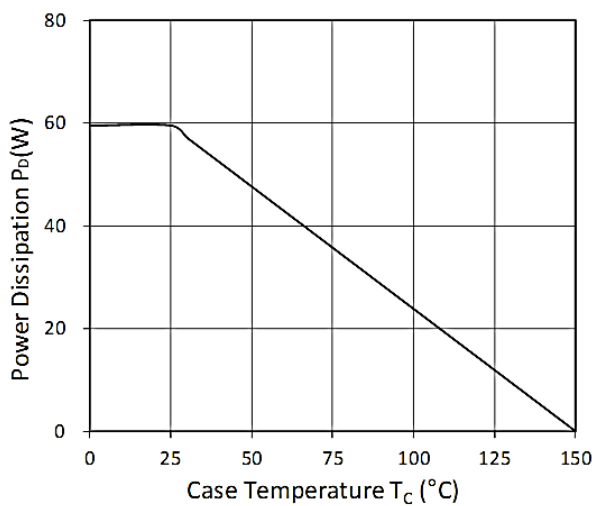


Figure 9. Power Dissipation

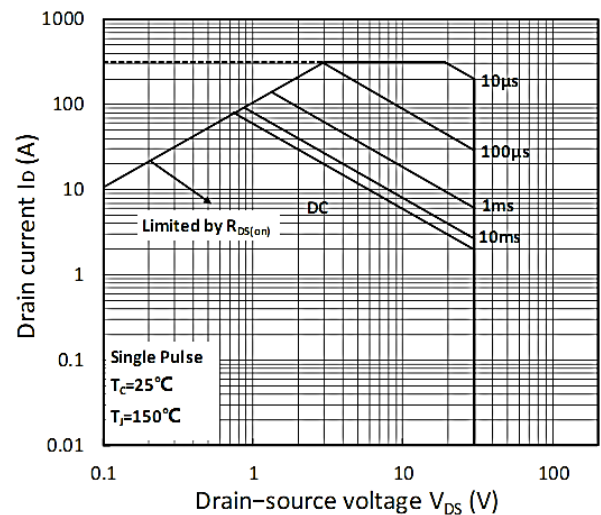


Figure 10. Safe Operating Area

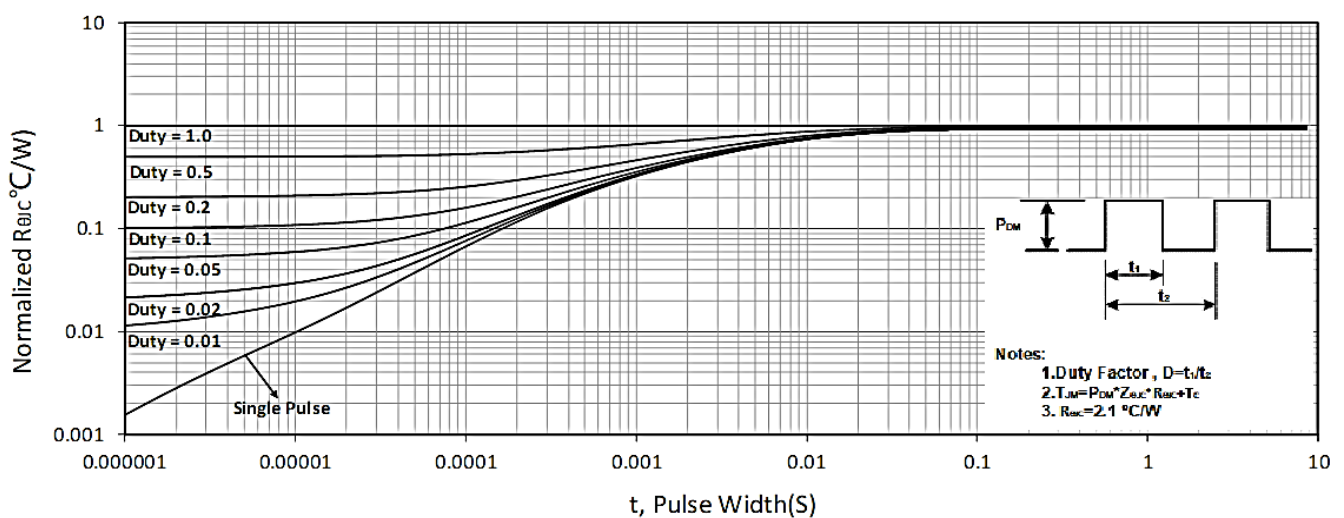
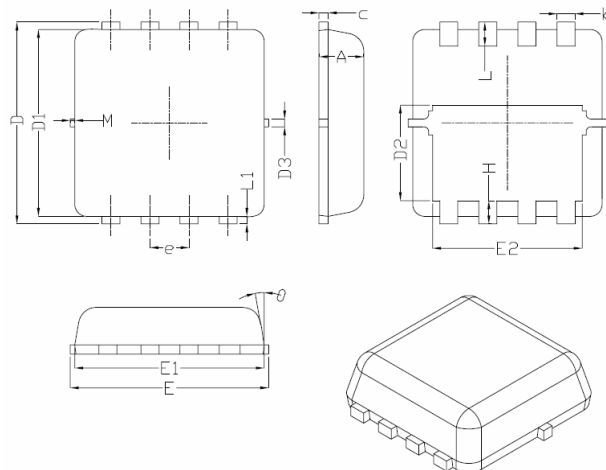


Figure 11. Normalized Maximum Transient Thermal Impedance



DFN3X3-8L Package Information



| Symbol | Dimensions In Millimeters | | |
|--------|---------------------------|------|------|
| | Min. | Nom. | Max. |
| A | 0.70 | 0.75 | 0.80 |
| b | 0.25 | 0.30 | 0.35 |
| c | 0.10 | 0.15 | 0.25 |
| D | 3.25 | 3.35 | 3.45 |
| D1 | 3.00 | 3.10 | 3.20 |
| D2 | 1.48 | 1.58 | 1.68 |
| D3 | - | 0.13 | - |
| E | 3.20 | 3.30 | 3.40 |
| E1 | 3.00 | 3.15 | 3.20 |
| E2 | 2.39 | 2.49 | 2.59 |
| e | 0.65BSC | | |
| H | 0.30 | 0.39 | 0.50 |
| L | 0.30 | 0.40 | 0.50 |
| L1 | - | 0.13 | - |
| M | * | * | 0.15 |
| θ | | 10° | 12° |



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