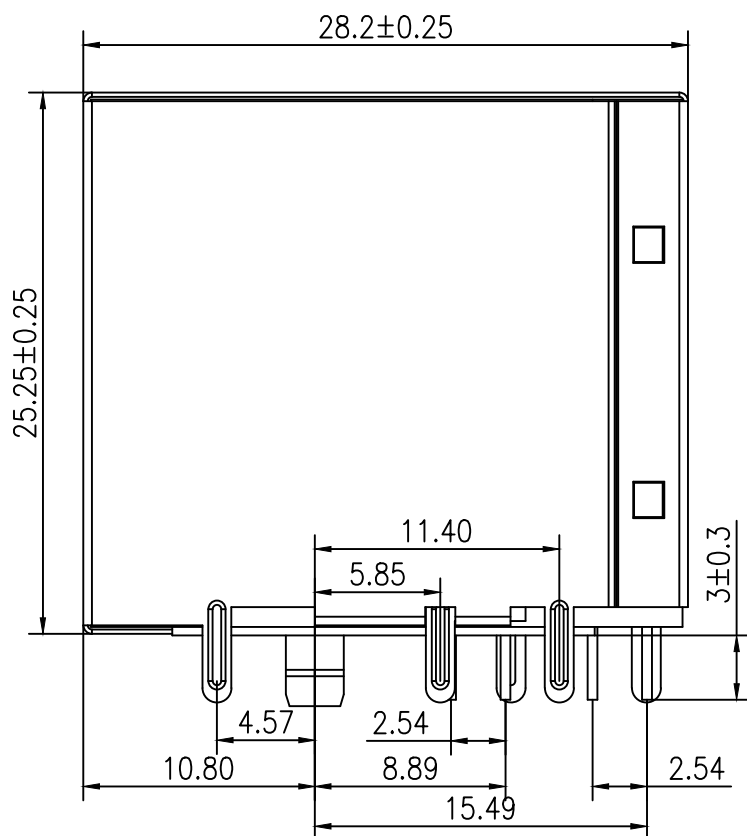
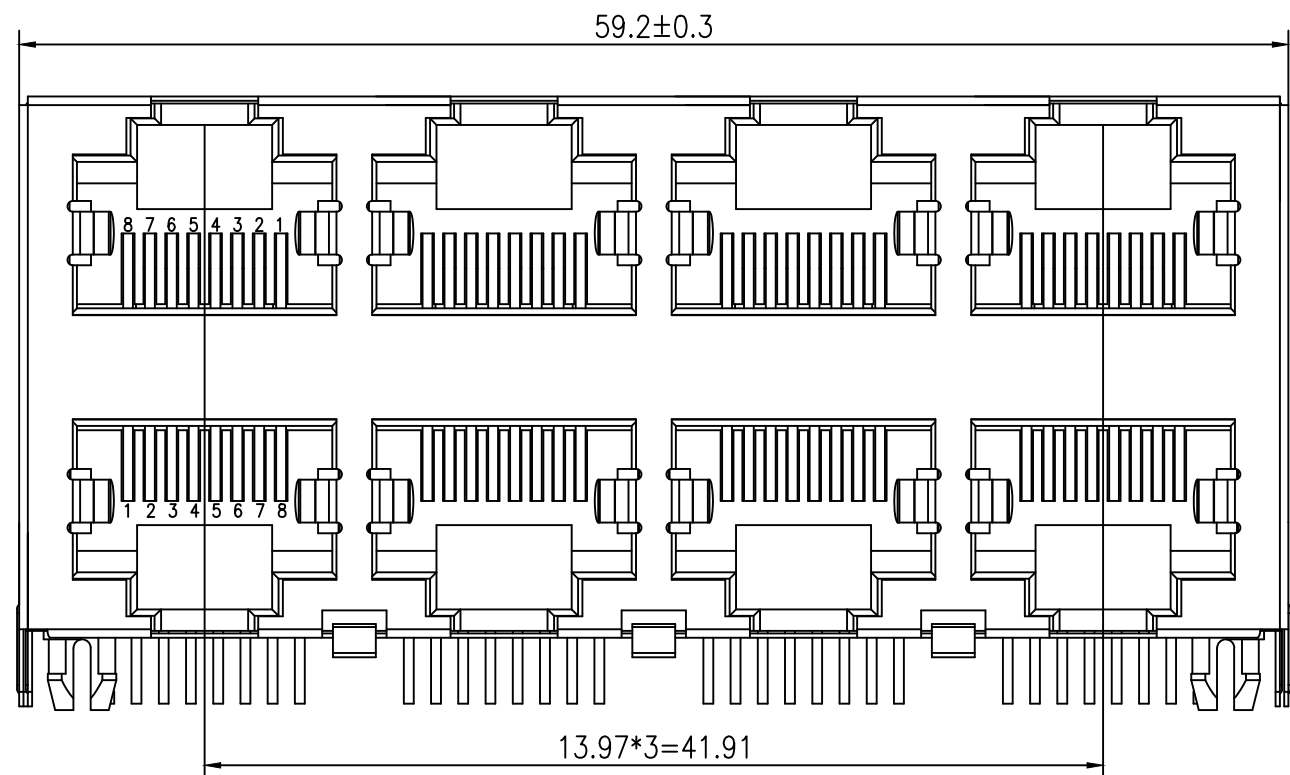


Mechanical:

REV.	ECN NO.	DESCRIPTION	DATE	APPD
A	REL		2009/12/11	



NOTES:

ELECTRICAL:

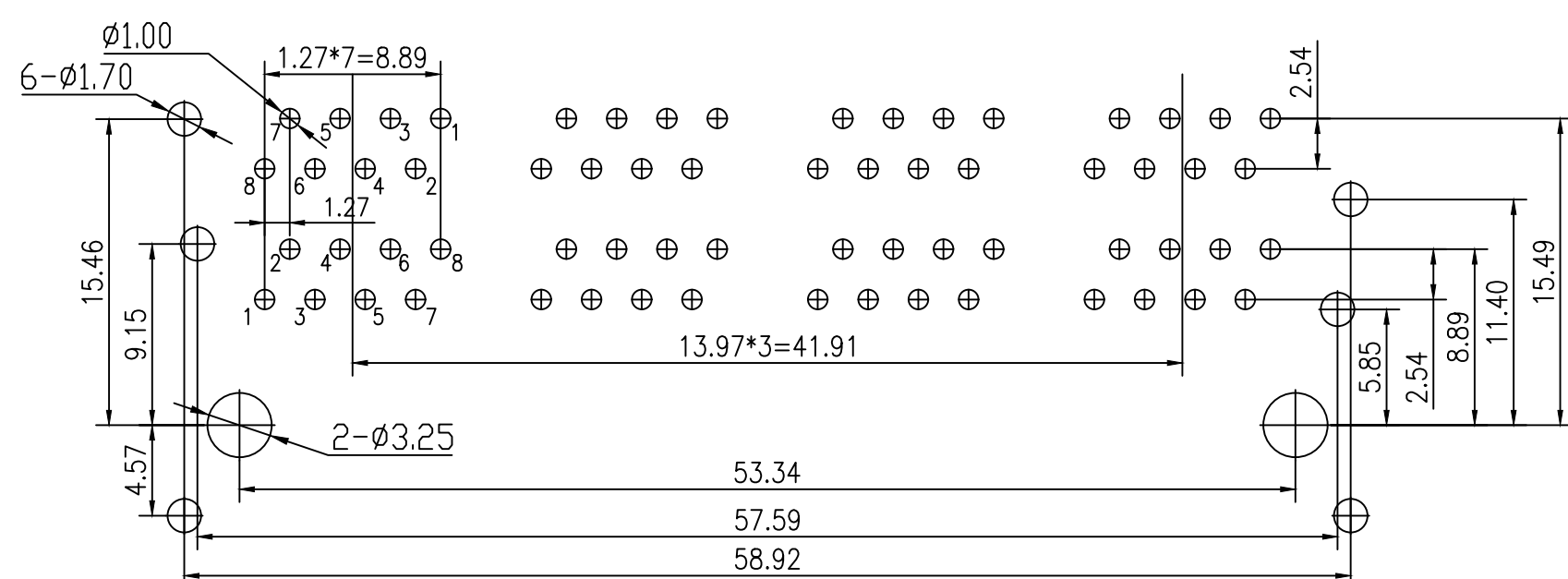
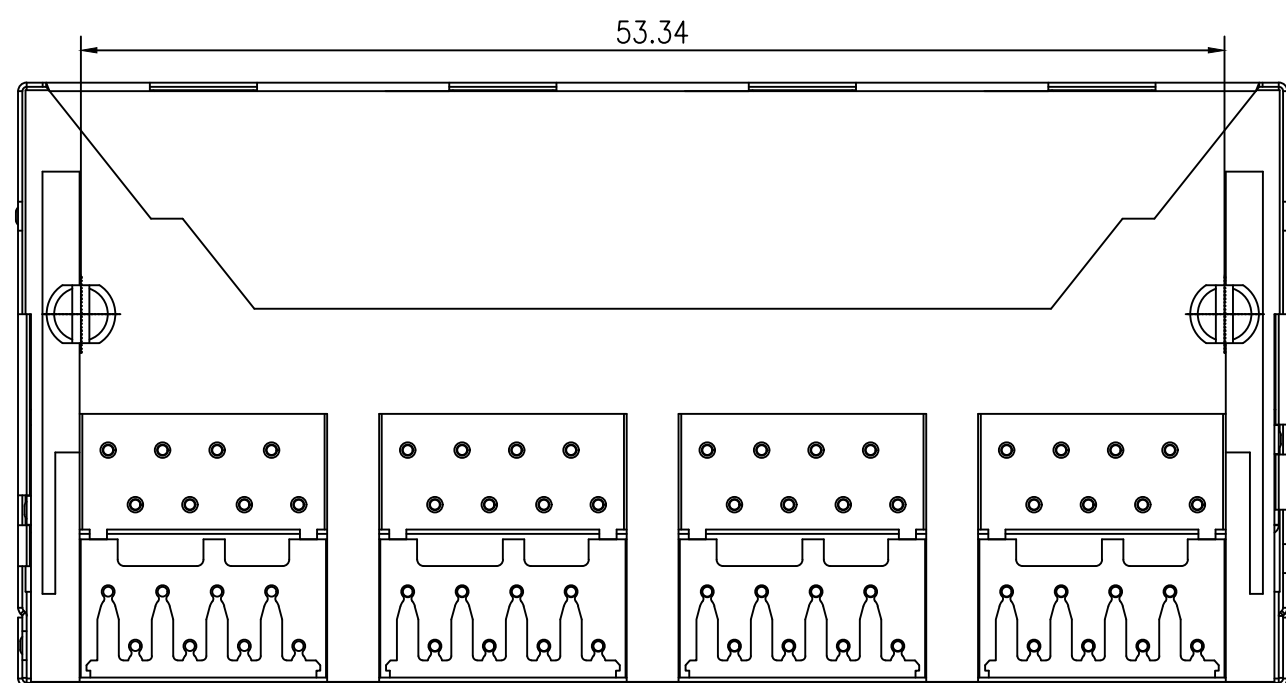
1. VOLTAGE RATING: 125V AC.
2. CURRENT RATING: 1.5A.
3. CONTACT RESISTANCE: 30 mΩ MAX.
4. INSULATION RESISTANCE: 500 MΩ MIN @ 500V DC.
5. DIELECTRIC STRENGTH: 1000V AC 50Hz or 60Hz, 1MIN.

MECHANICAL:

1. INSERT MATERIAL: PBT UL94V-0.
2. CONTACT MATERIAL: $\varnothing 0.46\text{mm}$ C51000 (QSn4-0.3) PHOSPHOR BRONZE GOLD PLATING OVER NICKEL.
3. SHIELD: 0.20mm THICKNESS H65Y BRASS NICKEL PLATED.
4. MATING/UNMATING FORCE: 2.2KG.F MAX.
5. RETENTION STRENGTH: 3.0KG.F MIN BETWEEN JACK AND PLUG.
6. OPERATING LIFE: 750 CYCLES MIN.

ENVIRONMENTAL:

1. STORAGE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$ RELATIVE HUMIDITY<70%.
2. OPERATION: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$.
3. Wave solder tip temperature and time: 250°C , 5 Sec.
4. UL Certification: File Number E484635.

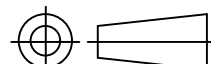


PC Board Layout



LPJE308-XDNL

Gold Plating	↓ "X"
Gold Flash	0 ☒
1.5u"	1 ☐
3u"	2 ☐
6u"	3 ☐
15u"	4 ☐
30u"	5 ☐
50u"	6 ☐

X:X	±0.30	APPD:	TOM	LINK-PP INT'L TECHNOLOGY CO., LIMITED			
X:XX	±0.25	CHKD:	JAMES				
X:XXX	±0.05	DR:	LEO	TITLE: SIDE ENTRY PCB JACK WITH SHIELD & 8P8C-2×4			
ANGLES	±1°	UNIT:	mm	PART NO. : LPJE308-ODNL			
	SCALE:	2/1	SHEET:	1/1	REV:	A	DWG NO. : LP09121131