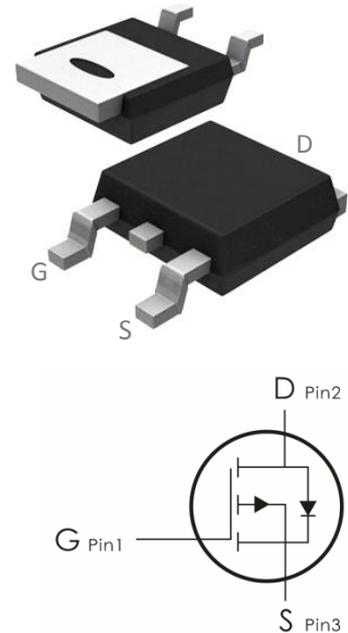


Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=-40V, I_D=-35A, R_{DS(ON)}<16m\ \Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DD016PG	D016P	TO- 252	2500 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	-35	A
	Continuous Drain Current- $T_C=100^\circ\text{C}$	-25	
I_{DM}	Pulsed Drain Current ¹	-144	
P_D	Power Dissipation $T_C=25^\circ\text{C}$	60	mJ
	Power Dissipation $T_C=100^\circ\text{C}$	30	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case ¹	2.5	$^\circ\text{C}/\text{W}$

Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\ \mu\text{A}$	-40	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=-40V$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\ \mu\text{A}$	-1.1	-1.7	-2.5	V
$R_{DS(on)}$	Drain-Source On Resistance	$V_{GS}=-10V, I_D=-20A$	---	12	16	$m\ \Omega$
		$V_{GS}=-4.5V, I_D=-15A$	---	17	22	
G_{FS}	Forward Transconductance	$V_{DS}=-5V, I_D=-5A$	15	---	---	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-20V, V_{GS}=0V, f=1\text{MHz}$	---	2050	---	pF
C_{oss}	Output Capacitance		---	260	---	
C_{rss}	Reverse Transfer Capacitance		---	150	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DS}=-20V, R_L=1.6\ \Omega$ $R_{GEN}=3\ \Omega, V_{GS}=-10V$	---	10	---	ns
t_r	Rise Time		---	24	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	40	---	ns
t_f	Fall Time		---	9	---	ns
Q_g	Total Gate Charge	$V_{GS}=-10V, V_{DS}=-20V,$ $I_D=-8A$	---	45	---	nC
Q_{gs}	Gate-Source Charge		---	6	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	11	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage	$V_{GS}=0V, I_S=-10A,$	---	---	-1.2	V
I_{SD}	Source-Drain Current(Body Diode)	$V_D=V_G=0V$	---	---	-35	A

Notes:

1.Repetitive Rating: Pulse width limited by maximum junction temperature

Typical Characteristics: ($T_c=25^{\circ}\text{C}$ unless otherwise noted)

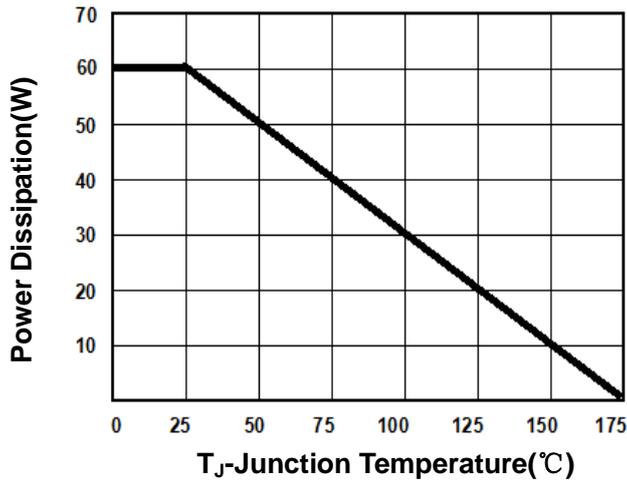


Figure1. Power Dissipation

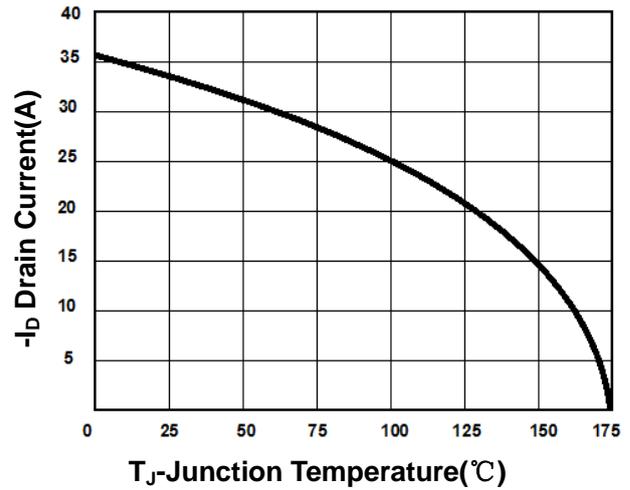


Figure2. Drain Current

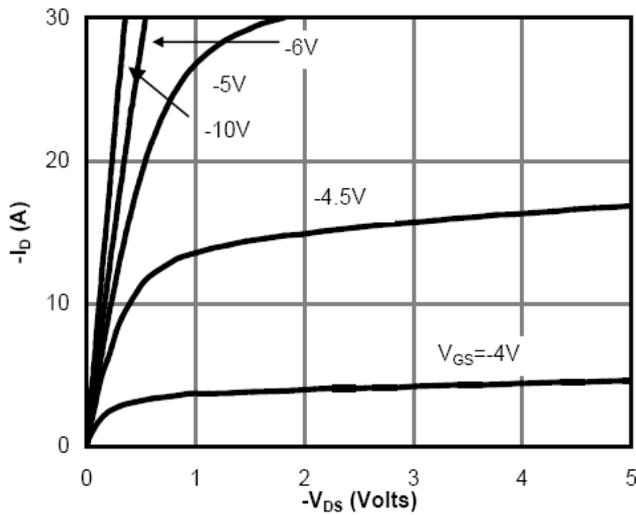


Figure3. Output Characteristics

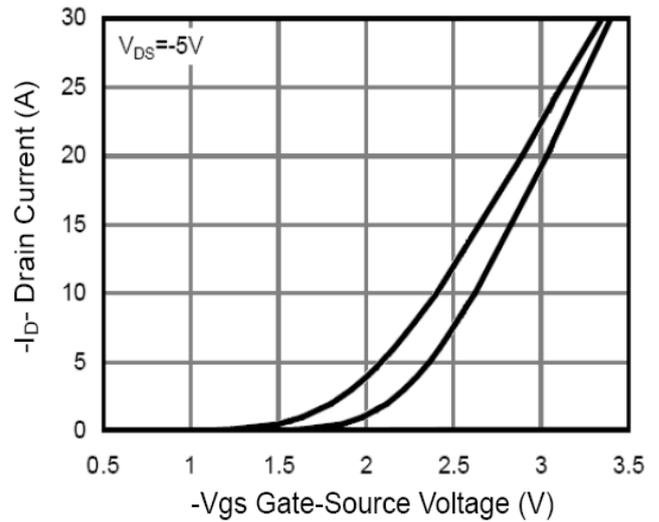


Figure4. Transfer Characteristics

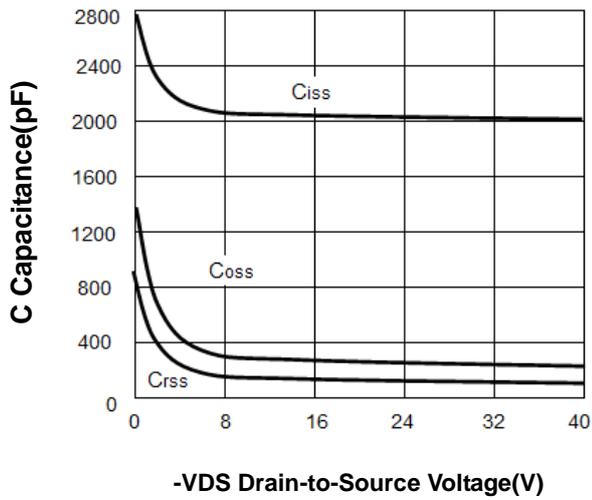


Figure5. Capacitance

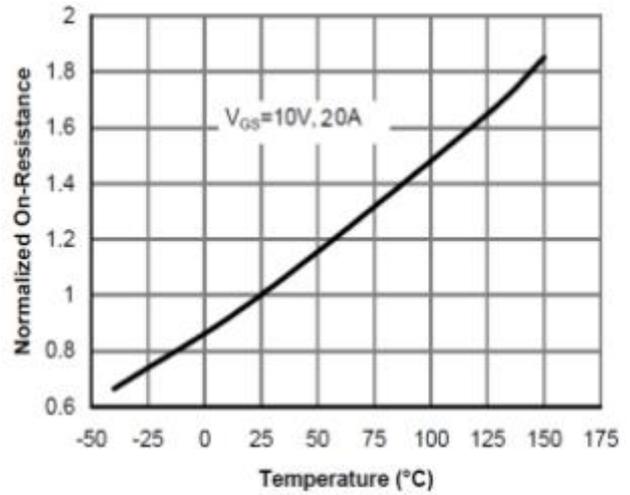


Figure6. $R_{DS(ON)}$ vs Junction Temperature

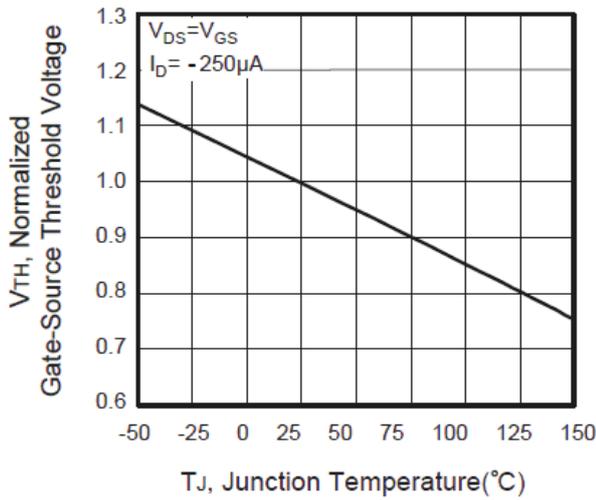


Figure7. $V_{GS(th)}$ vs Junction Temperature

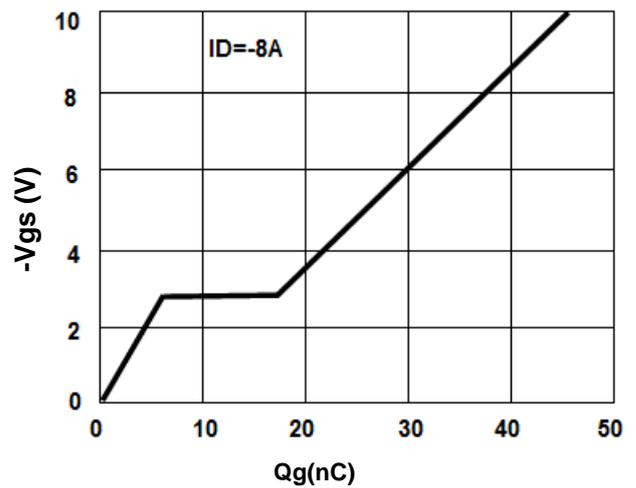


Figure8. Gate Charge Waveforms

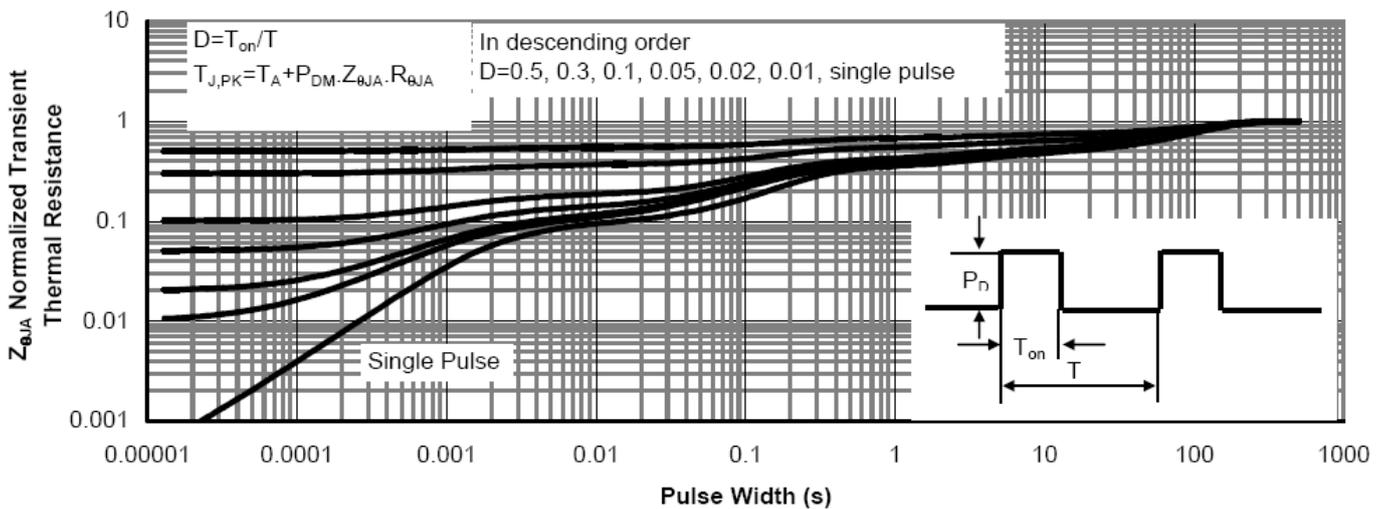
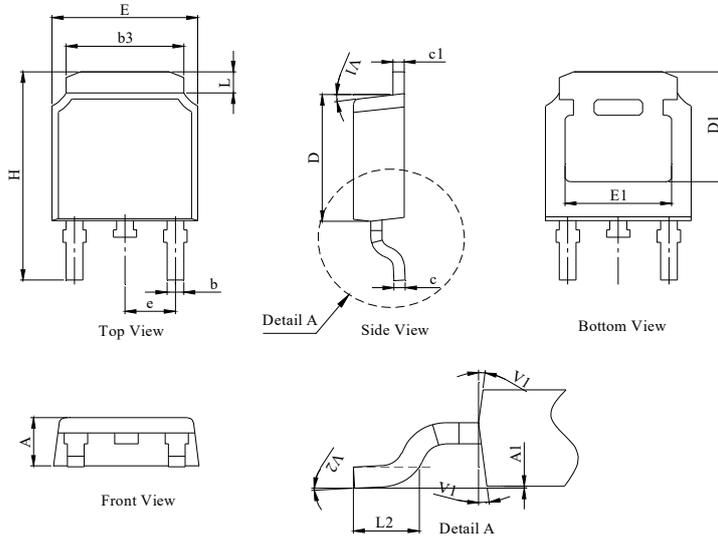


Figure9. Normalized Maximum Transient Thermal Impedance

TO-252 Package Information

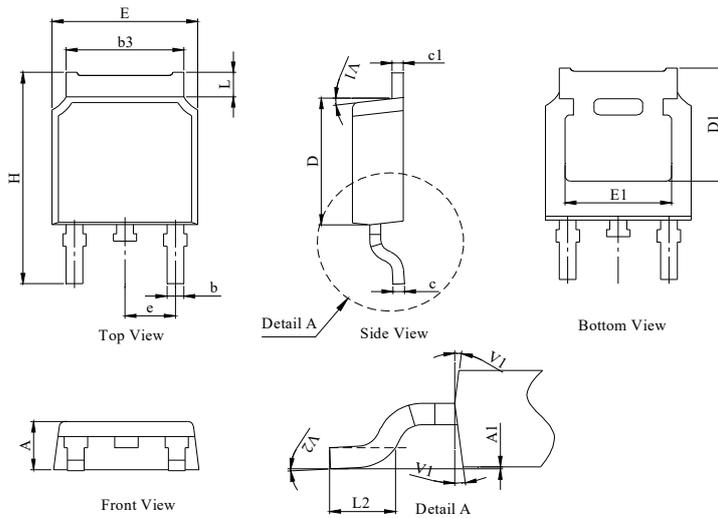
Package Outline Type-A

UNIT: mm



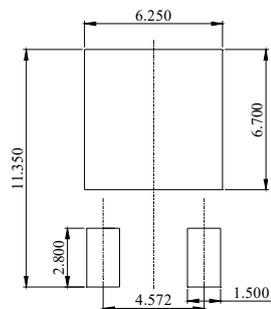
DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.18	2.30	2.39
A1	0	--	0.13
b	0.64	0.76	0.89
c	0.40	0.50	0.61
c1	0.46	0.50	0.58
D	5.97	6.10	6.23
D1	5.05	--	--
E	6.35	6.60	6.73
E1	4.32	--	--
b3	5.21	5.38	5.55
e	2.29 BSC		
H	9.40	10.00	10.40
L	0.89	--	1.27
L2	1.40	--	1.78
V1	7° REF		
V2	0°	--	6°

Package Outline Type-B



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.10	2.30	2.40
A1	0	--	0.13
b	0.66	0.76	0.86
b3	5.21	5.38	5.55
c	0.40	0.50	0.60
c1	0.44	0.50	0.58
D	5.90	6.10	6.30
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.29 BSC		
H	9.50	10.00	10.70
L	1.09	--	1.21
L2	1.35	--	1.65
V1	7° REF		
V2	0°	--	6°

Recommended Soldering Footprint



Marking Information:

①. Doingter LOGO

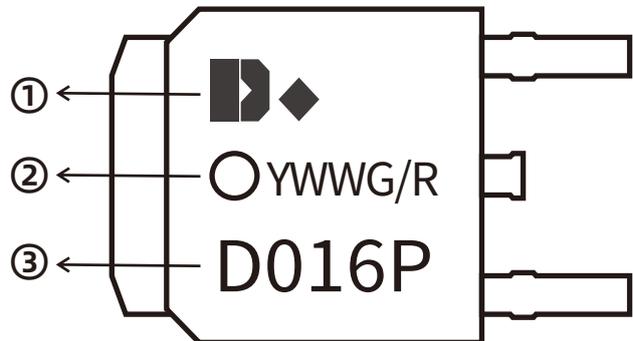
②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)

③. Part NO.

**Previous Version**

Version	Date	Subjects (major changes since last revision)
1.0	2024-07-10	Release of final version

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