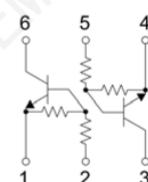
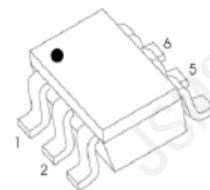


FEATURES

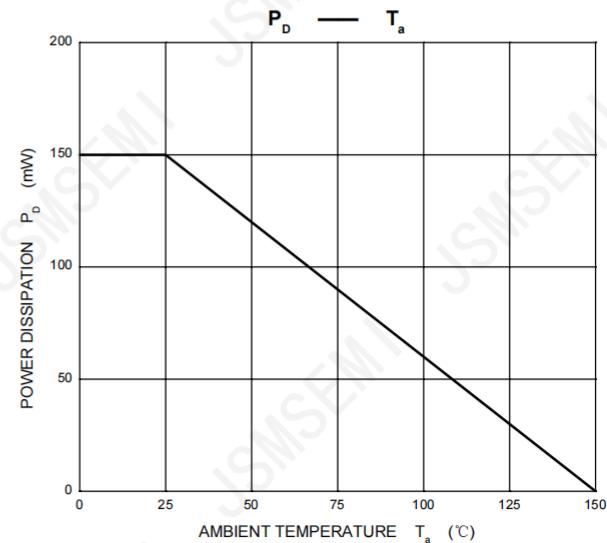
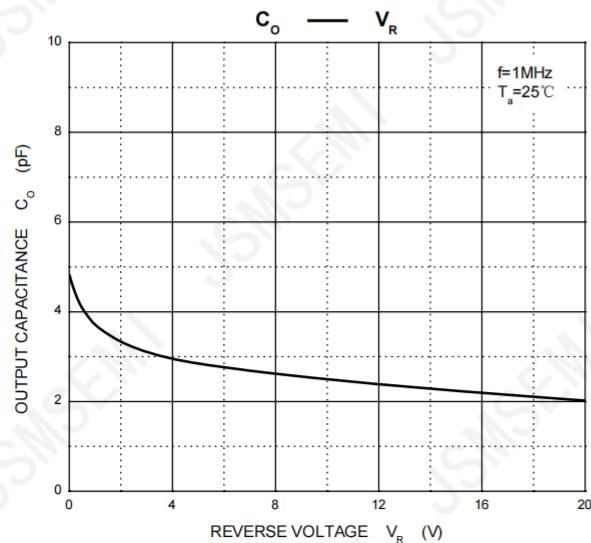
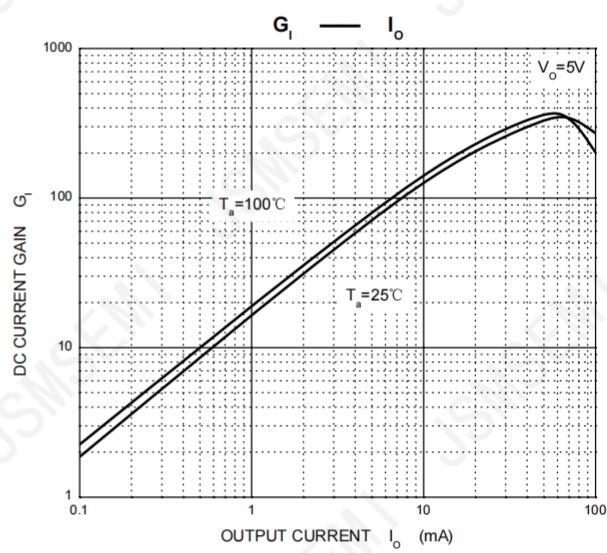
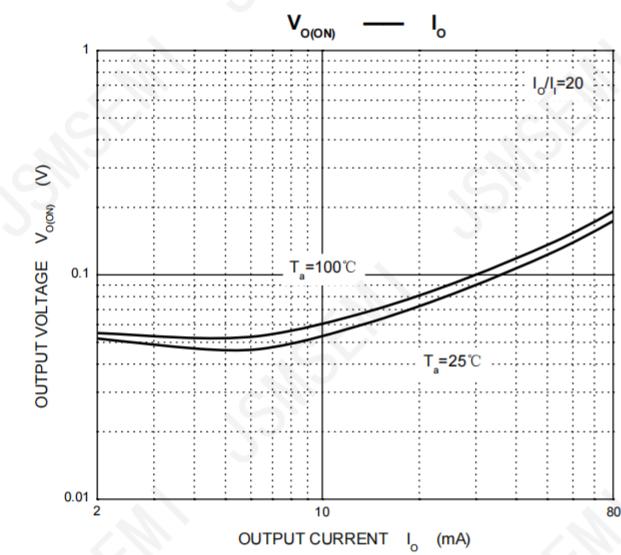
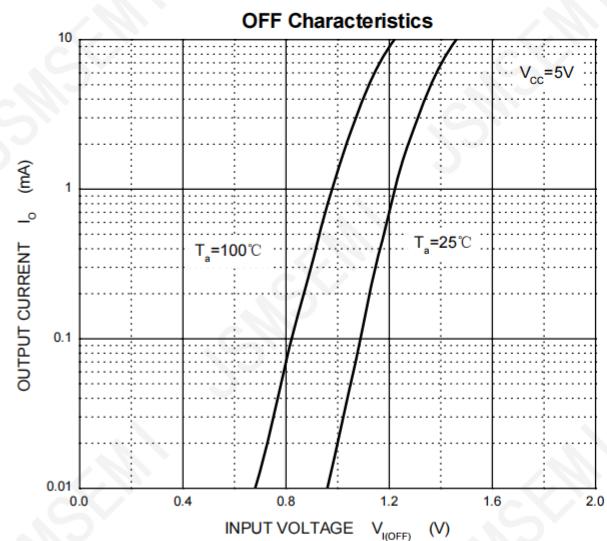
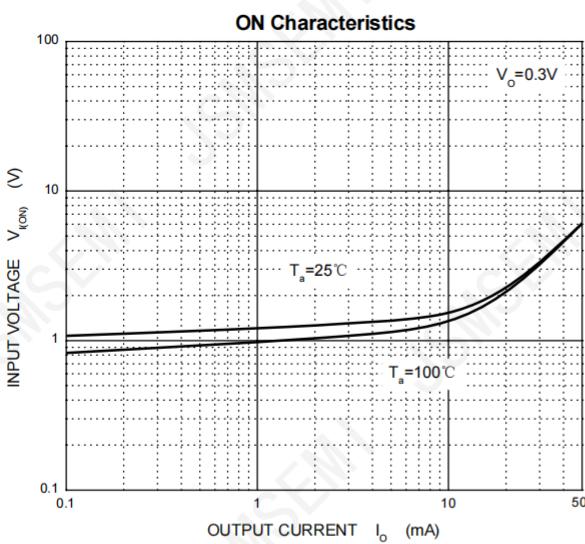
- Two PUMH11,115-JSM chip in a package
- Mounting possible with SOT-363 automatic mounting machines
- Transistor elements are independent, eliminating interference
- Mounting cost and area be cut in half

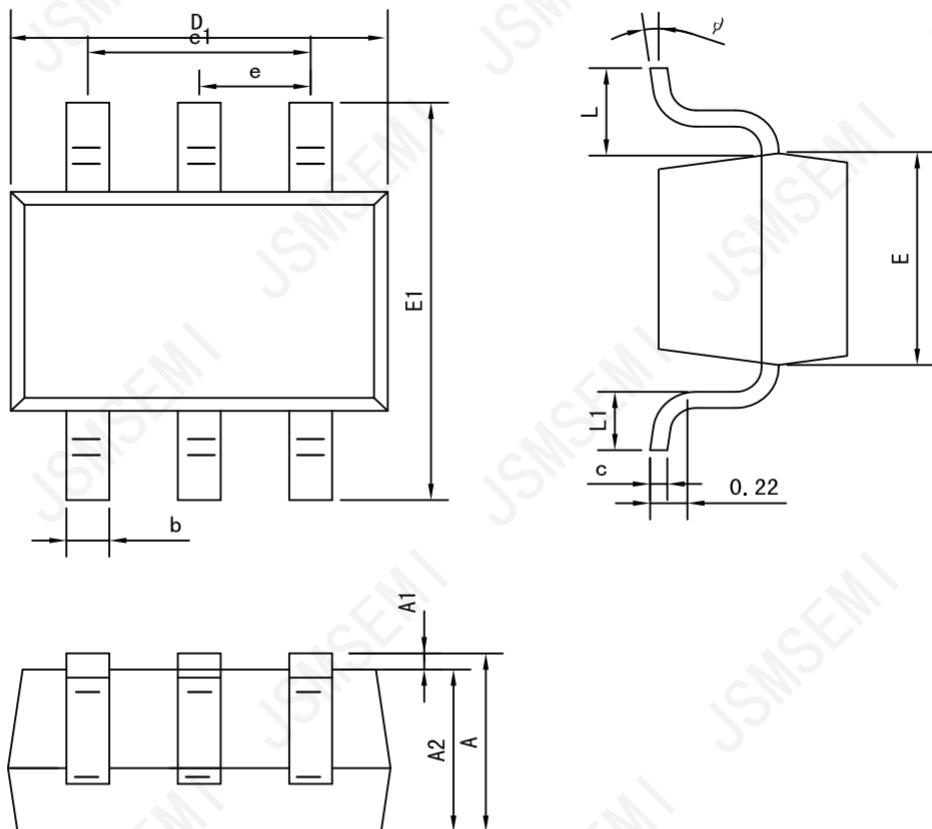

SOT-363
Absolute maximum ratings(Ta=25°C)

Parameter	Symbol	Limits		Unit
Supply voltage	V _{CC}	50		V
Input voltage	V _{IN}	-10~40		V
Output current	I _O	50		mA
	I _{C(MAX)}	100		
Power dissipation	P _d	150		mW
Junction temperature	T _j	150		°C
Storage temperature	T _{stg}	-55~150		°C

Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Input voltage	V _{I(off)}	0.5			V	V _{CC} =5V, I _O =100μA
	V _{I(on)}			3		V _O =0.3V, I _O =10mA
Output voltage	V _{O(on)}		0.1	0.3	V	I _O /I _I =10mA/0.5mA
Input current	I _I			0.88	mA	V _I =5V
Output current	I _{O(off)}			0.5	μA	V _{CC} =50V, V _I =0
DC current gain	G _I	30				V _O =5V, I _O =5mA
Input resistance	R _I	7	10	13	kΩ	
Resistance ratio	R ₂ /R ₁	0.8	1	1.2		
Transition frequency	f _T		250		MHz	V _{CE} =10V, I _E =5mA, f=100MHz



SOT-363 Package outline dimensions


Symbol	Dimension in Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.150	0.350
c	0.080	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.450
e	0.650 TYP	
e1	1.200	1.400
L	0.525 REF	
L1	0.260	0.460
θ	0°	8°

Revision History

Rev.	Change	Date
V1.0	Initial version	2/23/2024

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