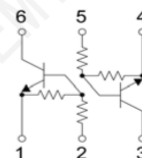
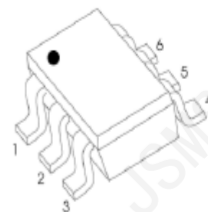


FEATURES

- Two PUMH10-JSM chips in a package
- Mounting possible with SOT-363 automatic mounting machines
- Transistor elements are independent, eliminating interference
- Mounting cost and area be cut in half



SOT-363

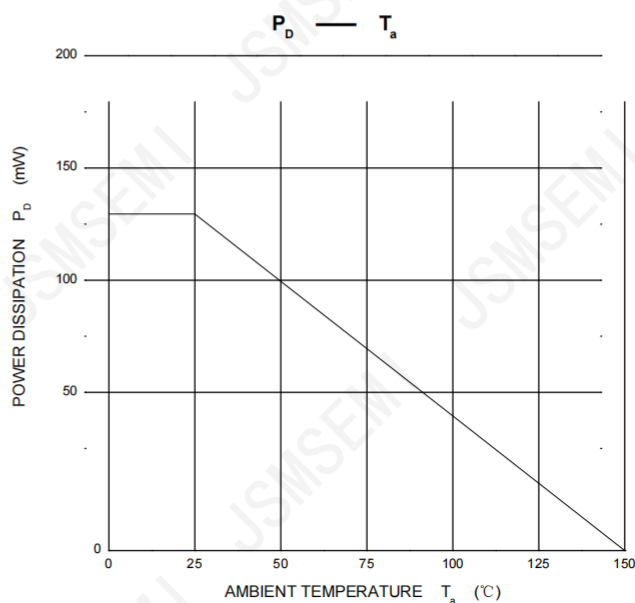
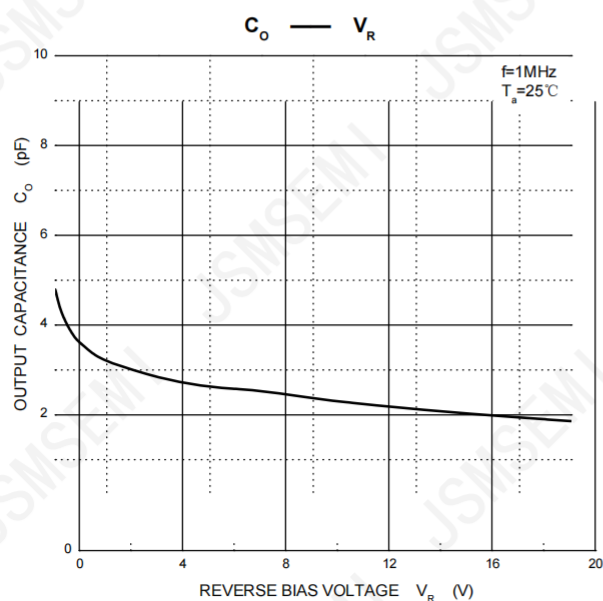
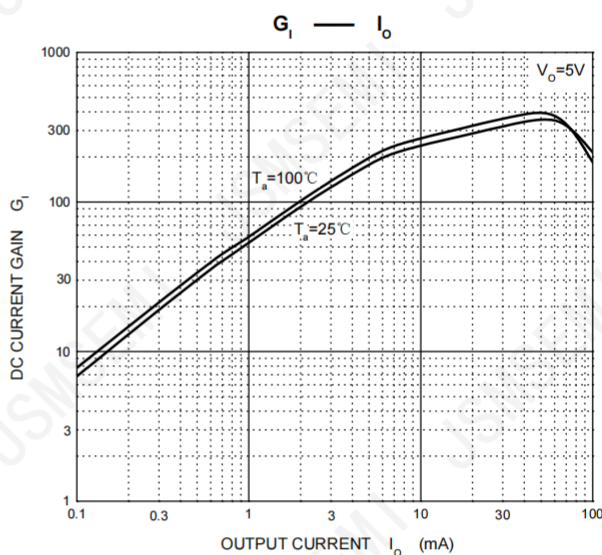
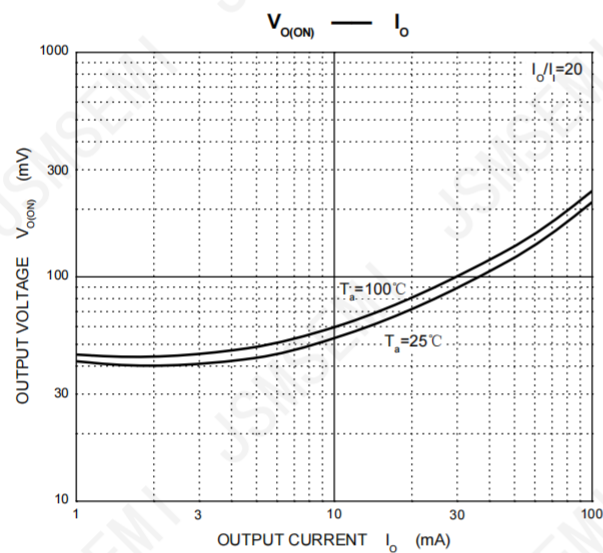
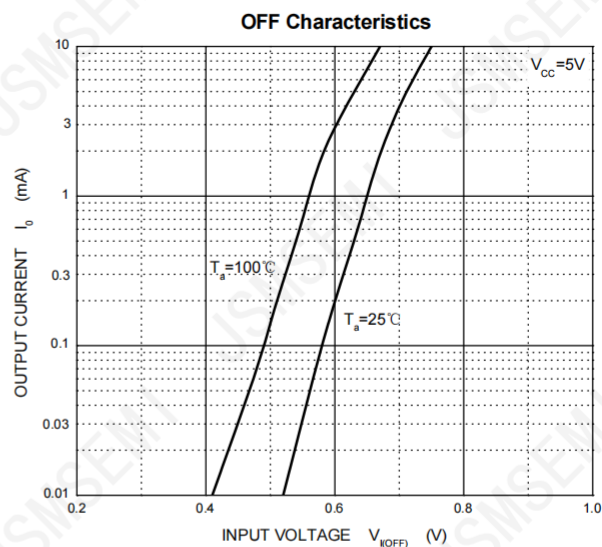
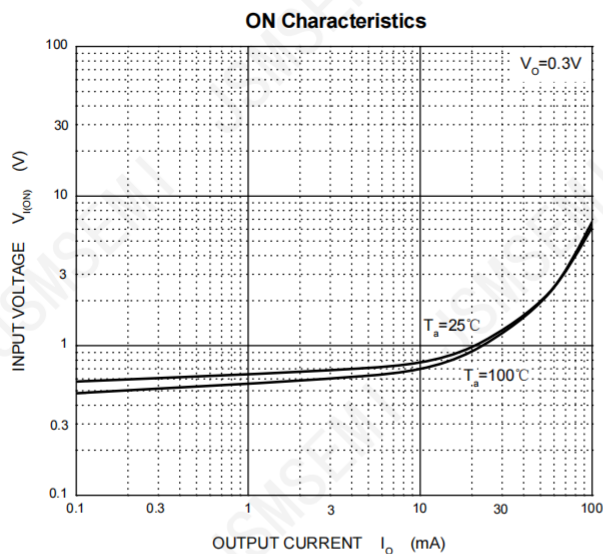
Absolute maximum ratings(Ta=25°C)

Parameter	Symbol		nit
Supply voltage	V_{CC}	50	V
Input voltage	V_{IN}	-5~12	V
Output current	I_O	100	mA
	$I_{C(MAX)}$	100	
Power dissipation	P_d	150	mW
Operation Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	°C

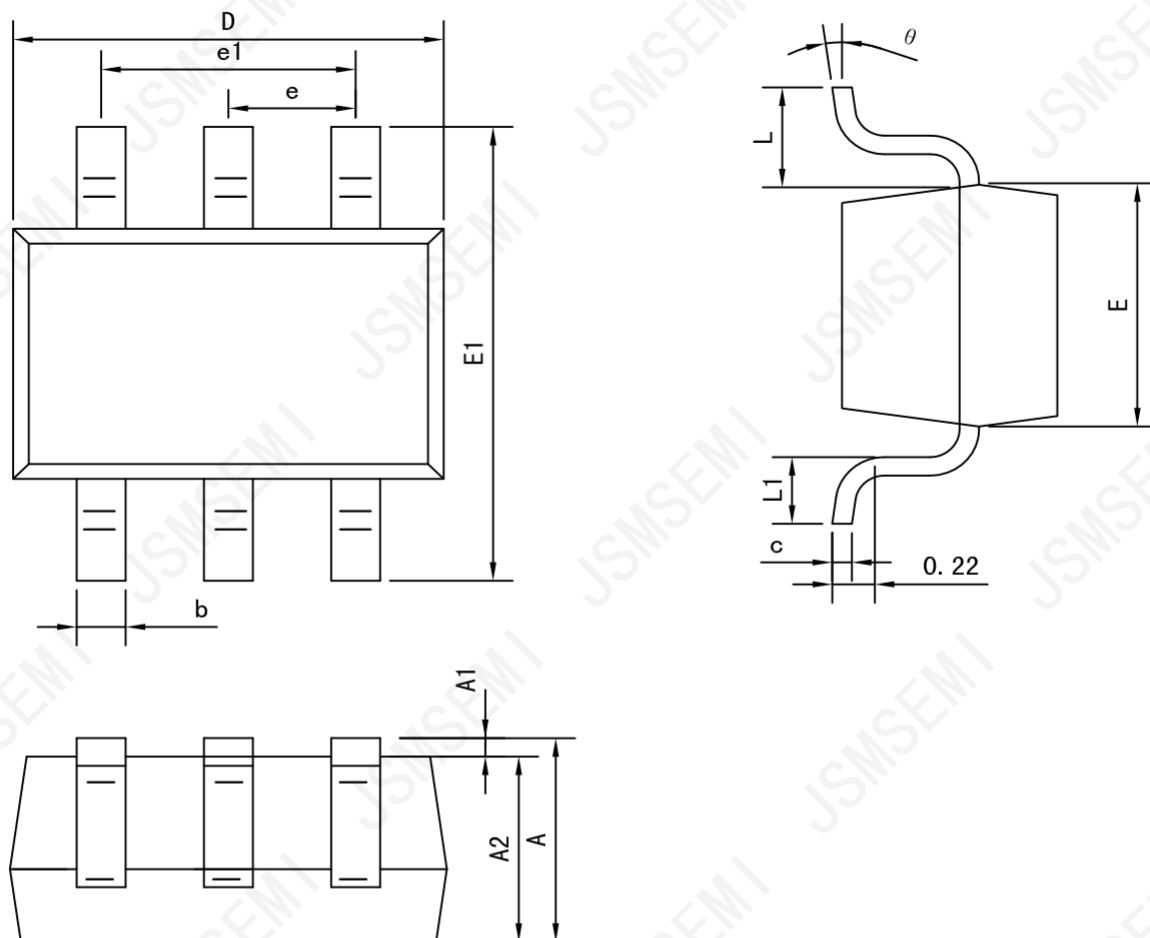
Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	0.5			V	$V_{CC}=5V, I_O=100\mu A$
	$V_{I(on)}$			1.1		$V_O=0.3V, I_O=5mA$
Output voltage	$V_{O(on)}$		0.1	0.3	V	$I_O/I_I=5mA/0.25mA$
Input current	I_I			3.6	mA	$V_I=5V$
Output current	$I_{O(off)}$			0.5	μA	$V_{CC}=50V, V_I=0$
DC current gain	G_I	80				$V_O=5V, I_O=10mA$
Input resistance	R_1	1.54	2.2	2.86	K Ω	-
Resistance ratio	R_2/R_1	17	21	26		-
Transition frequency	f_T		250		MHz	$V_{CE}=10V, I_E=5mA, f=100MHz$

Typical Characteristics



SOT-363 Package outline dimensions



Symbol	Dimension in Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.150	0.350
c	0.080	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.450
e	0.650 TYP	
e1	1.200	1.400
L	0.525 REF	
L1	0.260	0.460
θ	0°	8°

Revision History

Rev.	Change	Date
V1.0	Initial version	2/23/2024

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