

Product Summary

- V_{DS} -200V
- I_D -10A
- $R_{DS(ON)}$ (at $V_{GS}=10V$) <420m Ω
- 100% EAS Tested
- 100% ∇V_{DS} Tested

General Description

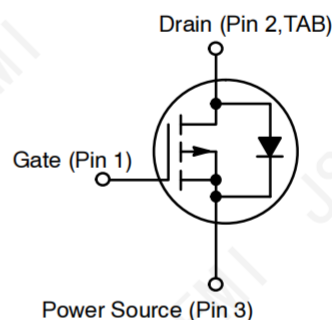
- Vertical Double-diffused MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power switching application
- Uninterruptible power supply
- DC-DC convertor
- Motor drivers



TO-220



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
		TO-220	
Drain-Source Voltage ($V_{GS} = 0V$)	V_{DSS}	-200	V
Continuous Drain Current	I_D	-10	A
Pulsed Drain Current (note1)	I_{DM}	-40	A
Gate-Source Voltage	V_{GSS}	± 20	V
Single Pulse Avalanche Energy (note2)	E_{AS}	165	mJ
Avalanche Current (note1)	I_{AS}	-11	A
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	78	mJ
Linear Derating Factor		0.6	W/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Unit
		TO-220	
Thermal Resistance, Junction-to-Case	R_{thJC}	1.6	K/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	50	

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	-200	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -200V, V_{GS} = 0V, T_J = 25^{\circ}C$	--	--	5	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20V$	--	--	± 120	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-2	--	-4	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -6.6A$	--	0.40	0.42	Ω
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = -25V, f = 1.0MHz$	--	1200	--	pF
Output Capacitance	C_{oss}		--	370	--	
Reverse Transfer Capacitance	C_{rss}		--	81	--	
Total Gate Charge	Q_g	$V_{DD} = -100V, I_D = -13.5A, V_{GS} = -10V$	--	52	68	nC
Gate-Source Charge	Q_{gs}		--	9	--	
Gate-Drain Charge	Q_{gd}		--	25	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -160V, I_D = -13.5A, R_G = 25\Omega$	--	28	56	ns
Turn-on Rise Time	t_r		--	74	148	
Turn-off Delay Time	$t_{d(off)}$		--	260	520	
Turn-off Fall Time	t_f		--	120	240	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^{\circ}C$	--	--	-10	A
Pulsed Diode Forward Current	I_{SM}		--	--	-40	
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C, I_{SD} = -11A, V_{GS} = 0V$	--	--	-5	V
Reverse Recovery Time	t_{rr}	$V_{GS} = 0V, I_S = -11A, di_F/dt = 100A/\mu s$	--	250	300	ns
Reverse Recovery Charge	Q_{rr}		--	2.9	3.6	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = -11A, V_{DD} = 25V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

Ordering Information

Ordernumber	Package	Marking	Operation Temperature Range	MSL Grade	Ship, Quantity	Green
IRF9630PBF	TO-220	IRF9630	-55 to 155°C	1	TURE,1000	Rohs

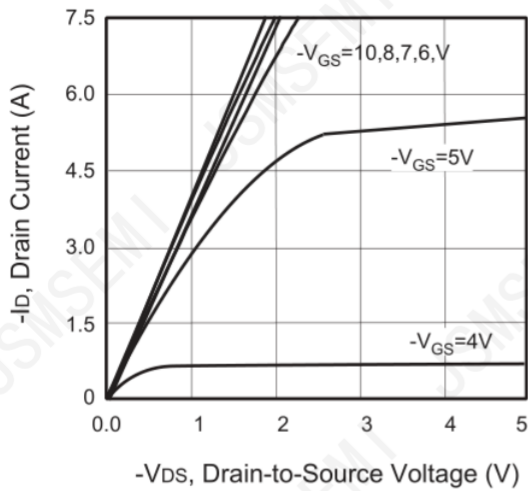


Figure 1. Output Characteristics

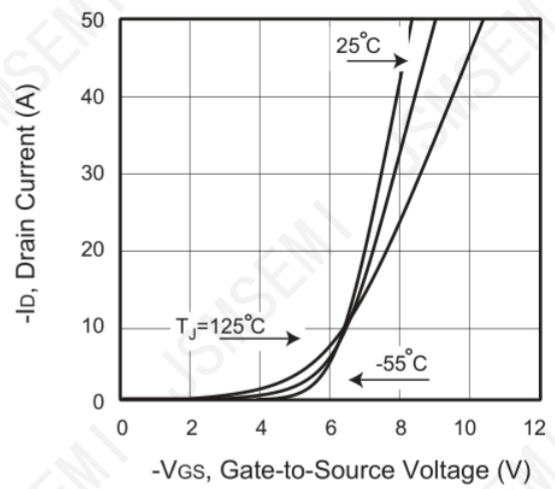


Figure 2. Transfer Characteristics

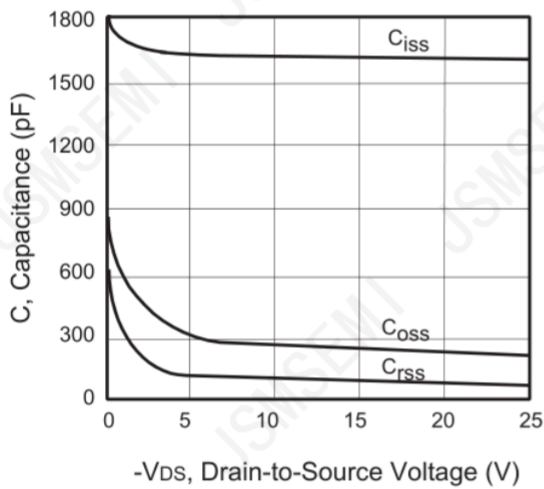


Figure 3. Capacitance

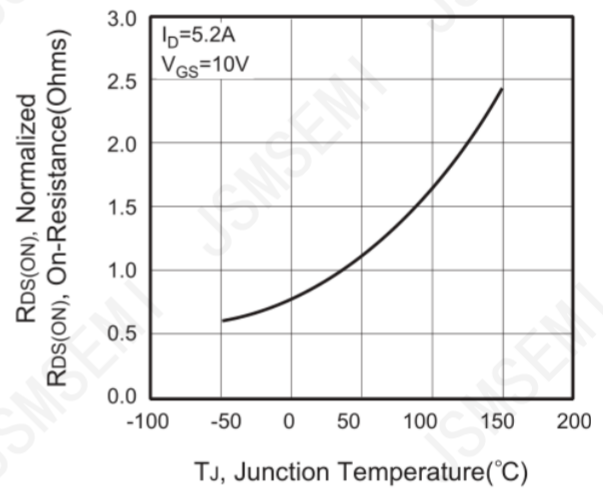


Figure 4. On-Resistance Variation with Temperature

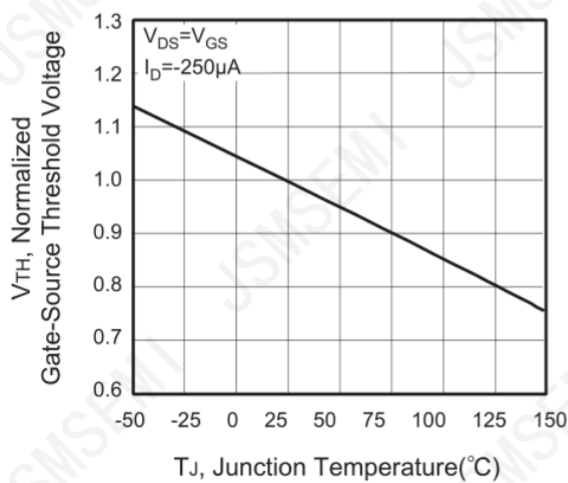


Figure 5. Gate Threshold Variation with Temperature

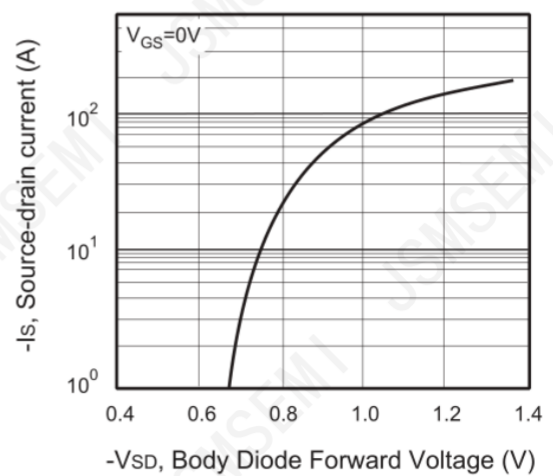


Figure 6. Body Diode Forward Voltage Variation with Source Current

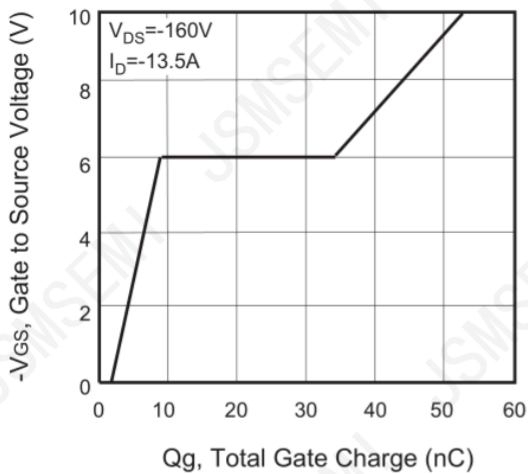


Figure 7. Gate Charge

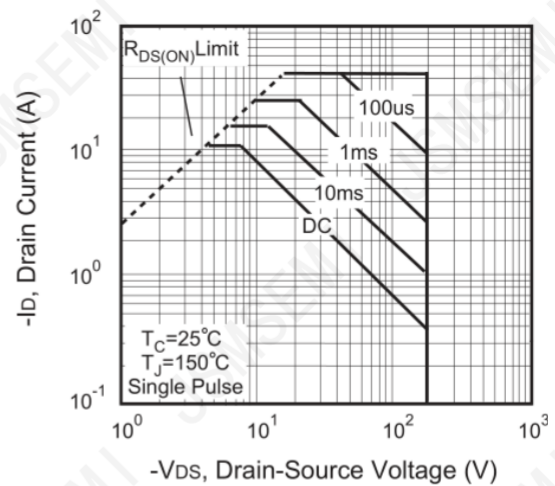


Figure 8. Maximum Safe Operating Area

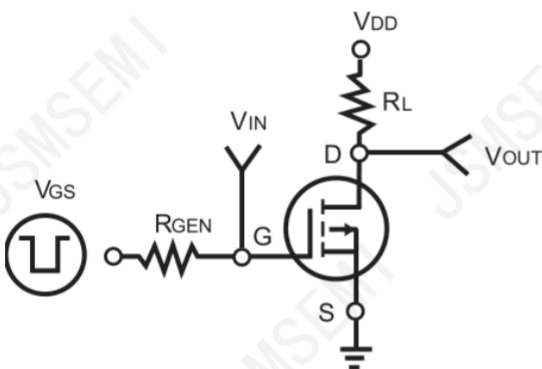


Figure 9. Switching Test Circuit

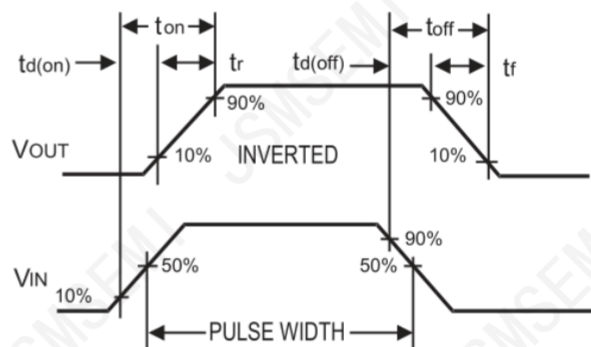


Figure 10. Switching Waveforms

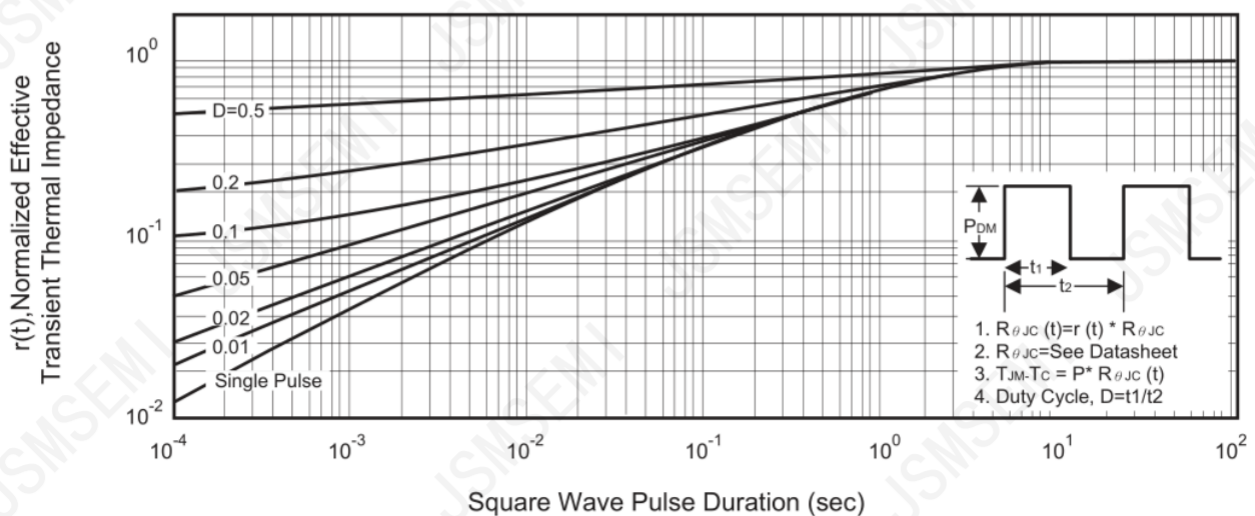
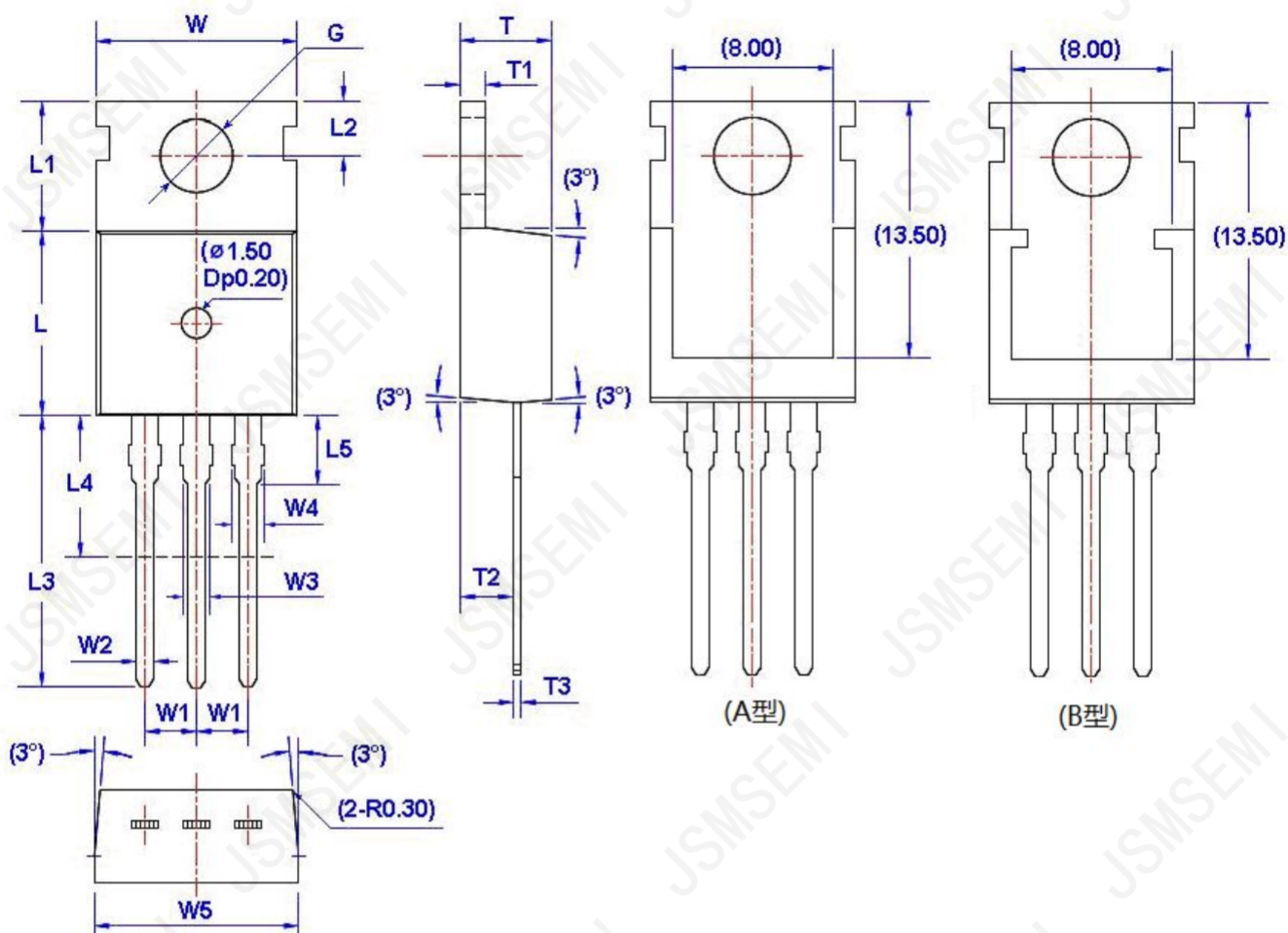


Figure 11. Normalized Thermal Transient Impedance Curve

Package Outline: TO-220



Unit: mm

Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.66	10.28	W5	9.80	10.20	L4**	6.20	6.60	T3	0.45	0.60
W1	2.54 (TYP)		L	9.00	9.40	L5	2.79	3.30	G(Φ)	3.50	3.70
W2	0.70	0.95	L1	6.40	6.80	T	4.30	4.70			
W3	1.17	1.37	L2	2.70	2.90	T1	1.15	1.40			
W4*	1.32	1.72	L3	12.70	14.27	T2	2.20	2.60			