



钛迪半导体  
Tudi Semiconductor

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## Product Specification

TUDI-ISL83488

3.3V, Low Power, High Speed or Slew Rate Limited,  
RS-485/RS-422 Transceivers

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## Features

- ? Single 3.3V power supply (10% tolerance)
- ? Compatible with 5V logic
- ? Single unit load allows to 32 devices on the bus
- ? Limited slope rate version for error-free data transfer up to 250kbps
- ? Low current shutdown mode
- ? mode input voltage range of -7V to 12V
- ? Full-duplex half-duplex pinout
- ? Current limiting and thermal shutdown for driver over-protection
- ? Lead-free (RoHS compliant)

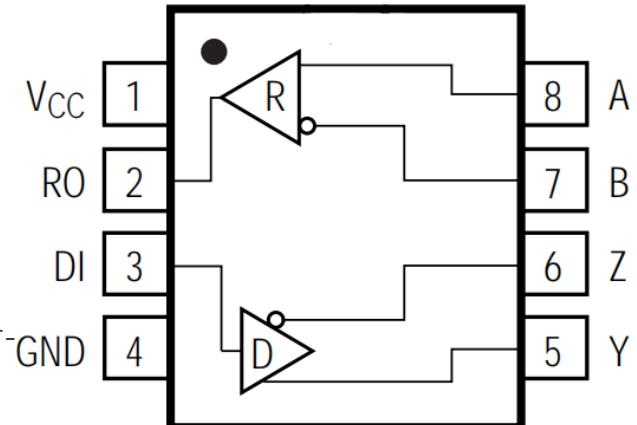


Figure 1. Pin Diagram

## Description

The ISL83488 is a transceiver for balanced communication that operates with 3.3V power supplies and meets the RS485 and RS-422 standards. The device is specified within a 10% tolerance range (3V to 3.6V).

The ISL83488 features EMI-Limited drivers that reduce EMI and minimize reflections from unterminated transmission lines or unterminated branches in multipoint applications. Logic inputs (e.g., DI and DE) accept signals above 5.5V, making them compatible with 5V logic families. The receive (Rx) has a “safe-in-open” design that ensures a logic high output if the Rx input is floating. All devices present a “single unit load” to RS-485 buses, and the number of transceivers on a network can reach up to 32.

The driver (Tx) output has short-circuit protection, if the voltage exceeds the power supply. Additionally, an on-chip thermal shutdown circuit disables the Tx output to prevent damage when the power dissipation is too high.

The ISL83488 offers space-saving 8 Ld packaging for applications that do not require Rx and Tx output disable functionality (point-to-point and -422).

## Applications

- Factory automation
- Security networks
- Building environmental control systems
- Industrial/process control networks
- Level translators (for example, RS-232 to RS-422)
- RS-232 “Extension Cords”



## Pin description

Pin number	Pin name	Pin function
1	VCC	Power supply:3.0V VCC 5.5V
2	RO	Receiver output. If A-B is greater than or equal to +200mV, RO output is high level; if A-B is less than or equal to -200mV, RO output is low level.
3	DI	DI driver input. A low level on DI causes the in-phase terminal Y output to be low and the out-of-phase terminal Z output to be high; a high level on DI causes the in-phase terminal Y output to be high and the out-of-phase terminal Z output to be low.
4	GND	Landing
5	Y	Drive in-phase output terminal
6	Z	The inverting output of the driver
7	B	Receiver inverting input
8	A	Receiver in-phase input

## Extreme parameter

Parameter	Symbol	Big or small	Unit
Welding temperature range		300	°C
Operating temperature range		-40~125	°C
Storage operating temperature range		-60~150	°C
Continuous power consumption	SOP8	400	mW
	DIP8	700	mW
Supply voltage	VCC	+7	V
Control the port voltage	DI	-0.3~VCC+0.3	V
Bus-side input voltage	A、B	-8~13	V
Receiver output voltage	RO	-0.3~VCC+0.3	V

The maximum limit parameter value is the value beyond which irreversible damage to the device may occur. Under these conditions, the device will not function properly and continuous operation at the maximum allowable rating may affect the reliability of the device. All voltage reference points are ground.



Parameter	Symbol	Test condition	Minimum	Typical case	Maximum	Unit
supply current						
Supply current	Icc	DI=0 or VCC		240	400	μA
ESD protect						
A、B、Y、Z		Mannequin (HBM)		±16		kV
Other ports		Mannequin (HBM)		±6		kV
Drive switch characteristics						
Drive input to output propagation delay(low to high)	tDPLH	RDIFF=54 Ω, CL1=CL2=100pF (see Figure 3 and Figure 4)		15	35	ns
Drive input to output propagation delay(high to low)	tDPHL			15	35	ns
tDPLH-tDPHL	tSKEW1			7	10	ns
Rise time /fall time	tDR,tDF			10	25	ns
Acceptor The propagation delay from input to output is from low to high	tRPLH	See Figure 5 and Figure 6 VID 2.0V; The rise and fall time VID is less than 15ns	20	60	90	ns
Acceptor The propagation delay from input to output is from high to low	tRPHL		20	60	90	ns
tRPLH-tRPHL	tSKEW2			7	10	ns
DC electrical characteristics of the driver						
High-level input	VIH	DI	2.0			V
Low level input	VIL	DI			0.8	V
Logic input current	IIN1	DI	-2		2	μA
Differential output of the driver(non-loaded)	VoD1			5		V
Drive differential output	VoD2	Graph 2,RL=27Ω	1.5		VCC	V
		Graph 2,RL=50Ω	2		VCC	
The current output is short-circuited to high	IosD1	Short circuit to 0V~12V	35		250	mA
The current output is short-circuited to low	IosD2	Short circuit to -7V~0V	-250		-35	mA
Change in the amplitude of the output voltage (NOTE1)	△VoD	Graph 2,RL=27Ω			0.2	V
Output common mode voltage	Voc	Graph 2,RL=27Ω			3	V
Change in the amplitude of the common-mode output voltage(NOTE1)	△Voc	Graph 2,RL=27Ω			0.2	V



Parameter	Symbol	Test condition	Minimum	Typical case	Maximum	Unit
DC electrical characteristics of the receiver						
Positive input threshold voltage	VIT+	$-7V \leq V_{CM} \leq 12V$			+200	mV
Reverse input threshold voltage	VIT-	$7V \leq V_{CM} \leq 12V$	-200			mV
Enter the hysteresis voltage	V <sub>hys</sub>	$-7V \leq V_{CM} \leq 12V$	10	30		mV
Input current(A,B)	I <sub>IN2</sub>	V <sub>CC</sub> =0 or 3.3V V <sub>IN</sub> =12 V			125	μA
		V <sub>CC</sub> =0 or 3.3V V <sub>IN</sub> =-7 V	-100			μA
Three state input leakage current	I <sub>oZ</sub> R	$0.4V < V_o < 2.4V$			±1	μA
Receiver input resistance	R <sub>IN</sub>	$-7V \leq V_{CM} \leq 12V$	96			kΩ
Receiver short circuit current	I <sub>oS</sub> R	$0V \leq V_o \leq V_{CC}$	±7		±95	mA
High level output voltage	V <sub>oH</sub>	I <sub>oUT</sub> =-4mA, V <sub>ID</sub> =+200 mV	V <sub>CC</sub> -1.5			V
Low level output voltage	V <sub>oL</sub>	I <sub>oUT</sub> =+4mA, V <sub>ID</sub> =-200 mV			0.4	V

(If not otherwise specified, V<sub>CC</sub>=3.3V±10%, Temp=TMIN~TMAX, typical value is V<sub>CC</sub>=+3.3 V, Temp = 25 )

## Test Circuit

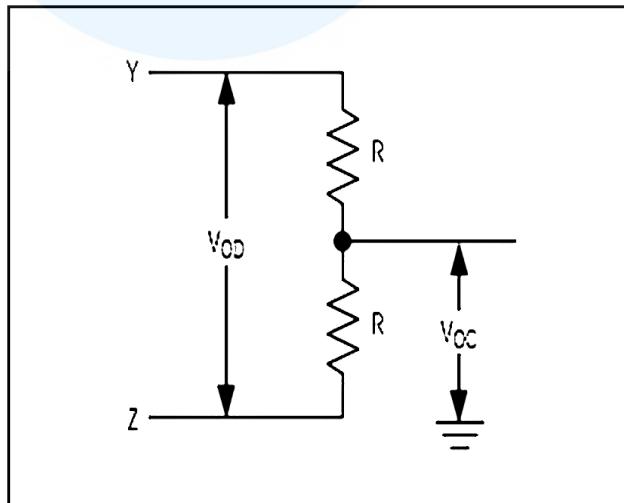


Figure 2 DC test load of the driver

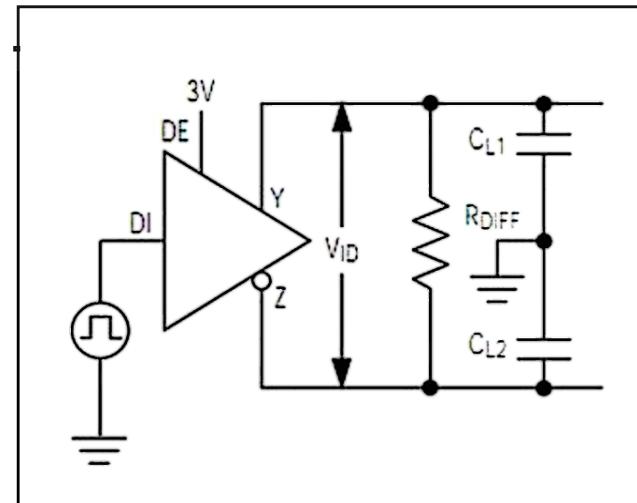


Figure 3 Driver timing test circuit

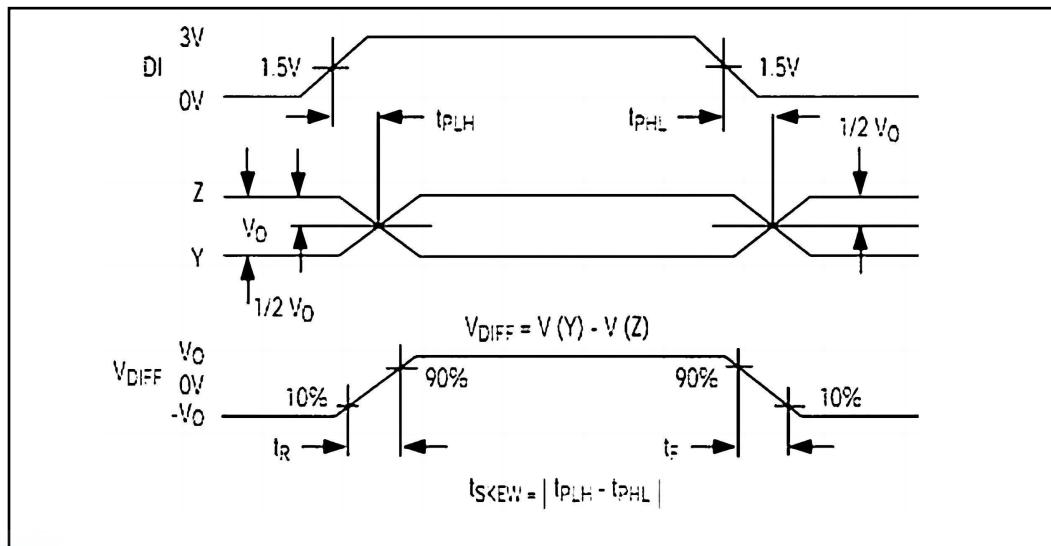


Figure 4 Driver propagation delay

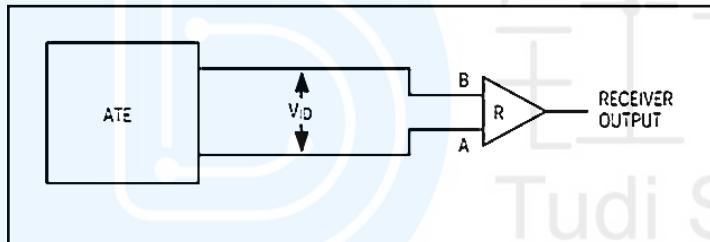


Figure 5 Receiver propagation delay test circuit

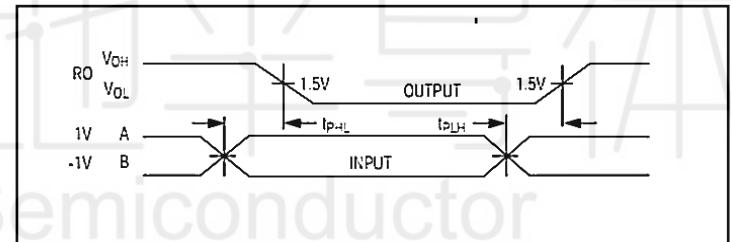


Figure 6 Receiver propagation delay timing

## Additional description

### resume

The 83488 is a full-duplex, high-speed transceiver for RS-485/RS-422 communication, incorporating driver and a receiver. It features fail-safe, overvoltage protection, and overcurrent protection. The 83488 achieves error-free data transmission up to 250Kbps.

### The bus is connected to 32 transceivers

The input impedance of the standard RS485 receiver is 12k (1 unit load), and the standard driver can drive up to 2 unit loads. The receiver of the 83488 transceiver has an input impedance of 1/8 unit load (96k), allowing up 32 transceivers to be connected in parallel on the same communication bus. These devices can be combined arbitrarily, or combined with other RS485 transceivers as long as the total load does not exceed 32 unit loads, they can be connected to the same bus.

### Drive output protection

Protection against excessive output current and dissipation by fault or bus contention is provided by overcurrent and overvoltage protection mechanisms, with fast short-circuit protection throughout the common-mode voltage range (see Typical Operating Characteristics).

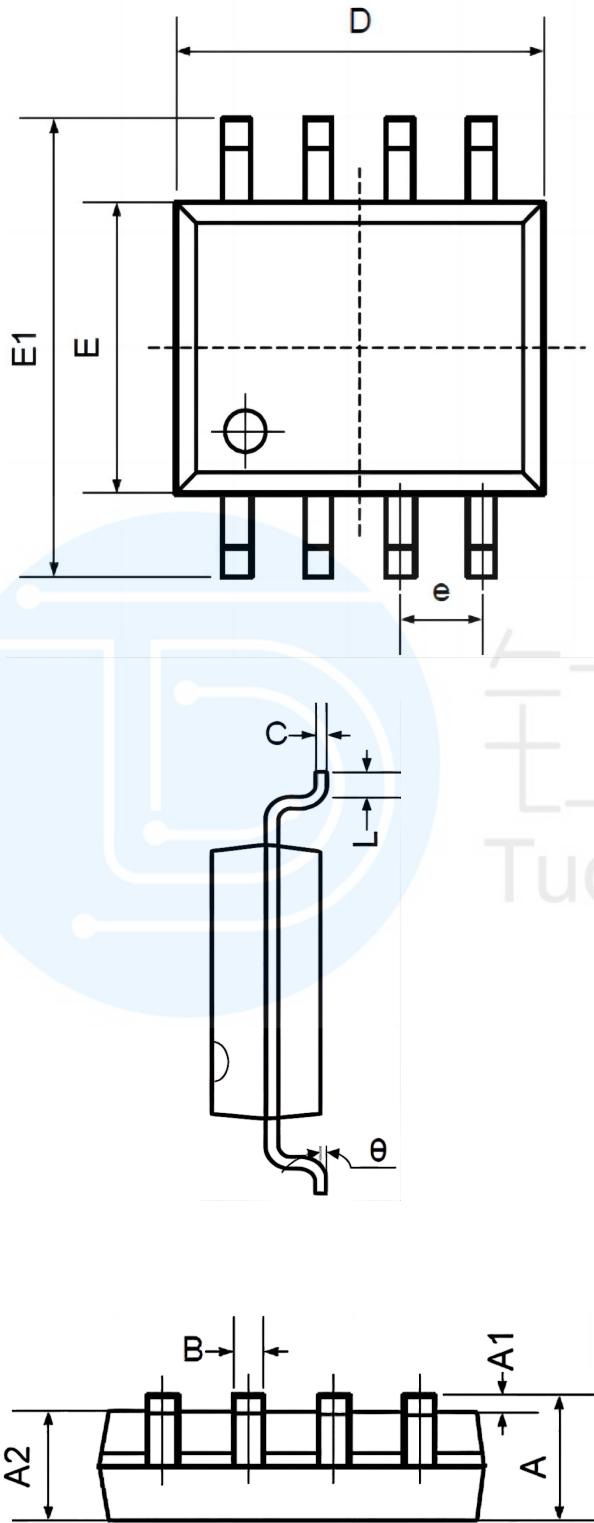


## Order information

Order Number	Package	Package Quantity	Marking On The park	Temperature
ISL83488IBZ-T-TUDI	SOP8	Tape,Reel,2500	83488IBZ	- 40°C to 85°C
ISL83488IUZ-T-TUDI	MSOP8	Tape,Reel,2500	83488IUZ	
ISL83488IPZ-TUDI	DIP8	Tube,50,A box of 2000	ISL83488IP	



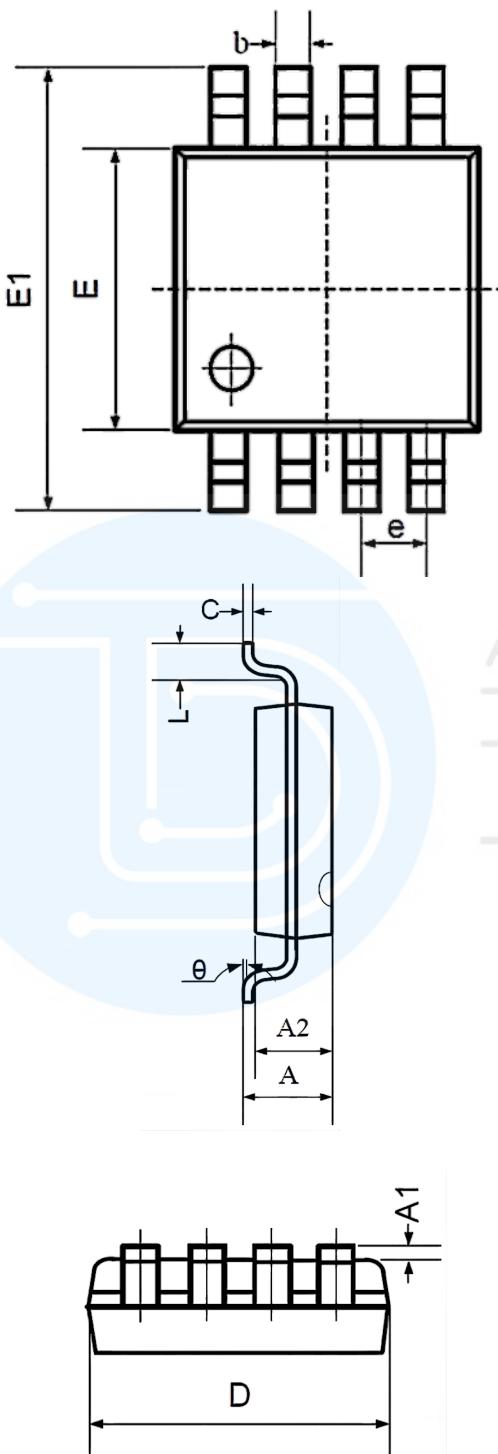
## Package SOP8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
B	0.330	0.510	0.013	0.020
C	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
e	1.270TYP		0.050TYP	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



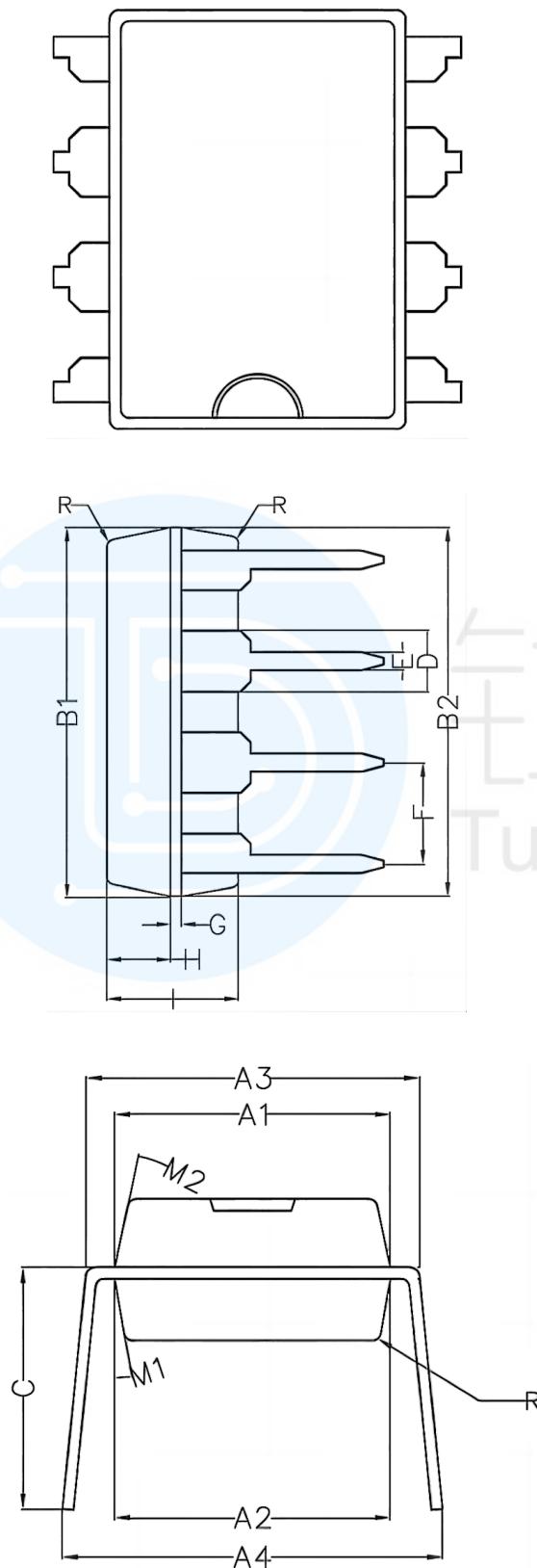
## Package MSOP8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.200	0.031	0.047
A1	0.000	0.200	0.000	0.008
A2	0.760	0.970	0.030	0.038
b	0.30 TYP		0.012 TYP	
C	0.15 TYP		0.006 TYP	
D	2.900	3.100	0.114	0.122
e	0.65 TYP		0.026 TYP	
E	2.900	3.100	0.114	0.122
E1	4.700	5.100	0.185	0.201
L	0.410	0.650	0.016	0.026
θ	0°	6°	0°	6°



## Package DIP8



Symbol	Min	Non	Max
A1	6.28	6.33	6.38
A2	6.33	6.38	6.43
A3	7.52	7.62	7.72
A4	7.80	8.40	9.00
B1	9.15	9.20	9.25
B2	9.20	9.25	9.30
C		5.57	
D		1.52	
E	0.43	0.45	0.47
F		2.54	
G		0.25	
H	1.54	1.59	1.64
工	3.22	3.27	3.32
R		0.20	
M1	9°	10°	11°
M2	11°	12°	13°



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