

AD8542ARZ-REEL7-HX

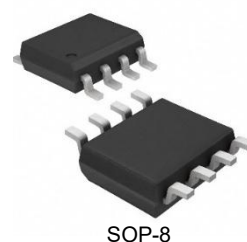
Micro-Power 1 MHz, Low-Noise, RRIO, 2.7V CMOS Amplifiers

General Description

The AD8542ARZ-REEL7-HX family of operational amplifiers, including single-, dual-, and quad- channel options, is specifically designed for cost-sensitive systems and applications. These amplifiers feature rail-to-rail input and output swings, low quiescent current (typically 75 μA), wide bandwidth (1 MHz), and very low noise (25 $\text{nV}/\sqrt{\text{Hz}}$ at 1 kHz), making them highly suitable for battery-powered applications that require a balance between cost and performance. Examples of such applications include audio outputs, consumer electronics, smoke detectors, portable medical devices, and white goods. The low input bias current allows these amplifiers to be used with high impedance sources.

The robust design of the AD8542ARZ-REEL7-HX amplifiers offers ease-of-use for circuit designers, with unity-gain stability even with capacitive loads up to 500 pF, integrated RF/EMI rejection filter, no phase reversal in overdrive conditions, and high electro-static discharge (ESD) protection (5-kV HBM).

The AD8542ARZ-REEL7-HX amplifiers are optimized for operation at voltages ranging from +2.7 V (± 0.9 V) to +5.5 V (± 2.75 V) within a temperature range of 0 $^{\circ}\text{C}$ to 70 $^{\circ}\text{C}$. They can also operate at voltages from +2.0 V (± 1.0 V) to +5.5 V (± 2.75 V) over an extended temperature range of -40 $^{\circ}\text{C}$ to +125 $^{\circ}\text{C}$.



SOP-8

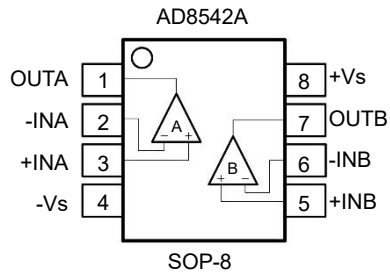
Features

- Rail-to-Rail Input and Output
- Low Input Offset Voltage: 0.5 mV
- Precision Amplifiers for Cost-Sensitive Systems
- Single 2.7 V to 5.5 V Supply Voltage Range at 0 $^{\circ}\text{C}$ to 70 $^{\circ}\text{C}$
- Extended Temperature Range: -40 $^{\circ}\text{C}$ to +125 $^{\circ}\text{C}$
- Low Noise: 25 $\text{nV}/\sqrt{\text{Hz}}$ at 1 kHz
- Micro-Power: 45 μA Supply Current Per Amplifier
- Internal RF/EMI Filter
- 1 MHz GBW for Unity-Gain Stable

Applications

- Sensor Signal Conditioning
Sensor Interfaces, Loop-Powered, Active Filters
- Wireless Sensors
Home Security, Remote Sensing, Wireless Metering
- Battery-Powered Instruments
Consumer, Industrial, Medical, Notebooks
- Audio Outputs

PIN CONFIGURATIONS



Pin Description

Symbol	Description
-IN	Inverting input of the amplifier. The voltage range is from ($V_{S-} - 0.1V$) to ($V_{S+} + 0.1V$).
+IN	Non-inverting input of the amplifier. This pin has the same voltage range as -IN.
+Vs	Positive power supply.
-Vs	Negative power supply.
OUT	Amplifier output.

Limiting Value

Parameter	Absolute Maximum Rating
Supply Voltage, V_{S+} to V_{S-}	10.0 V
Signal Input Terminals: Voltage, Current	$V_{S-} - 4V$ to $V_{S+} + 4V$, ± 10 mA
Output Short-Circuit	Continuous
Storage Temperature Range, T_{stg}	-65°C to $+150^{\circ}\text{C}$
Junction Temperature, T_J	150°C
Lead Temperature Range (Soldering 10 sec)	260°C

Electrical Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
OFFSET VOLTAGE						
V _{OS}	Input offset voltage			0.5		mV
		T _A = -40 to +125 °C			±2.8	
V _{OS TC}	Offset voltage drift	T _A =-40 to +125 °C		±1	3	µV/°C
P _{SRR}	Power supply rejection ratio	V _S = 2.0 to 5.5 V, V _{CM} < V _{S+} -2V	80	110		dB
		T _A = -40 to +125 °C	45			
INPUT BIAS CURRENT						
I _B	Input bias current			1		pA
		T _A = +85		150		
		T _A = +125 °C		500		
I _{OS}	Input offset current			1		
NOISE						

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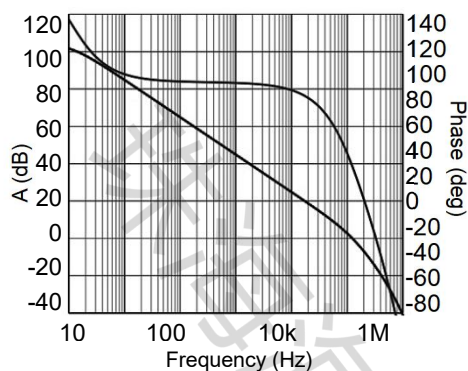
V_n	Input voltage noise	$f = 0.1$ to 10 Hz		5.6		$\mu\text{VP-P}$
e_n	Input voltage noise density	$f = 10$ kHz		22		$\text{nV}/\sqrt{\text{Hz}}$
		$f = 1$ kHz		25		
I_n	Input current noise density					
INPUT VOLTAGE						
V_{CM}	Common-mode voltage range		$V_{\text{S-}}-0.1$		$V_{\text{S+}}-0.1$	V
CMRR	Common-mode rejection ratio	$V_{\text{S}} = 5.5$ V, $V_{\text{CM}} = -0.1$ to 5.6 V	70	83		dB
		$V_{\text{CM}}=0$ to5.3V, $T_{\text{A}}=-40$ to $+125^{\circ}\text{C}$	65			
		$V_{\text{S}} = 2.0$ V, $V_{\text{CM}} = -0.1$ to 2.1 V	65	77		
		$V_{\text{CM}}=0$ to 2.7V, $T_{\text{A}}=-40$ to $+125^{\circ}\text{C}$	60			
INPUT IMPEDANCE						
C_{IN}	Input capacitance	Differential		2.0		pF
		Common mode		3.5		
OPEN-LOOP						
A_{VOL}	Open-loop voltage A_{VOL} gain	$R_{\text{L}} = 25$ k Ω , $V_{\text{O}}= 0.05$ to 3.5 V	90	105		dB
		$T_{\text{A}} = -40$ to $+125^{\circ}\text{C}$	85			
		$R_{\text{L}} = 2$ k Ω , $V_{\text{O}} = 0.15$ to 3.5 V	85	100		
		$T_{\text{A}} = -40$ to $+125^{\circ}\text{C}$	80			
FREQUENCY RESPONSE						
GBW	Gain bandwidth product			1		MHz
SR	Slew rate	$G=+1, C_{\text{L}}=100\text{pF}, V_{\text{O}}=1$ to3.5V		1		V/ μs
THD+N	Total harmonic distortion+noise	$G=+1, f=1$ kHz, $V_{\text{O}} = 1V_{\text{RMS}}$		0.002		%
t_{s}	Settling time	To 0.1%, $G = +1$, 1V step		1		μs
		To 0.01%, $G = +1$, 1V step		1		
t_{OR}	Overload recovery time	To 0.1%, $V_{\text{IN}} * \text{Gain} > V_{\text{S}}$		2		
OUTPUT						
V_{OH}	High output voltage swing	$R_{\text{L}} = 25$ k Ω	$V_{\text{S+}}-9$	$V_{\text{S+}}-5$		mV
		$R_{\text{L}} = 2$ k Ω	$V_{\text{S+}}-95$	$V_{\text{S+}}-63$		
V_{OL}	Low output voltage swing	$R_{\text{L}} = 25$ k Ω		$V_{\text{S-}}+3.5$	$V_{\text{S-}}+6$	
		$R_{\text{L}} = 2$ k Ω		$V_{\text{S-}}+43$	$V_{\text{S-}}+65$	
POWER SUPPLY						
V_{S}	Operating supply voltage	$T_{\text{A}} = 0$ to $+70^{\circ}\text{C}$	2.7	5.5		V
		$T_{\text{A}} = -40$ to $+125^{\circ}\text{C}$	2.0	5.5		
I_{Q}	Quiescent urrent(peramplifier)			45	125	μA
		$T_{\text{A}} = -40$ to $+125^{\circ}\text{C}$			160	
THERMAL CHARACTERISTICS						
T_{A}	Operating temperature range		-40		+125	$^{\circ}\text{C}$
θ_{JA}	Package Thermal Resistance	SOT23-5L		190		$^{\circ}\text{C}/\text{W}$
		SOP-8		125		
		SOP-14		115		

Note

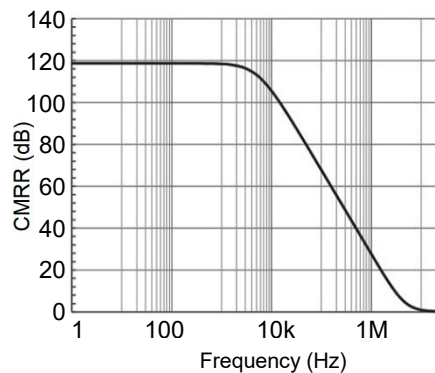
$V_S = 5.0$ V, $T_A = +25^\circ\text{C}$, $V_{CM} = V_S / 2$, $V_O = V_S / 2$, and $R_L = 10$ k Ω connected to $V_S / 2$, unless otherwise noted.

TYPICAL PERFORMANCE CHARACTERISTICS

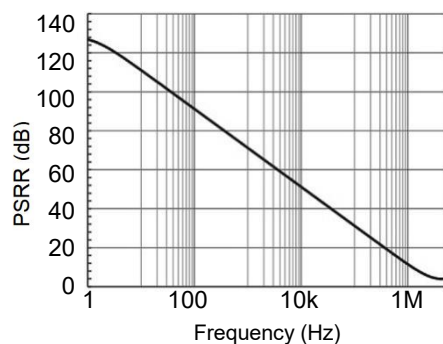
At $T_A = +25^\circ\text{C}$, $V_{CM} = V_S / 2$, and $R_L = 10\text{k}\Omega$ connected to $V_S / 2$, unless otherwise noted.



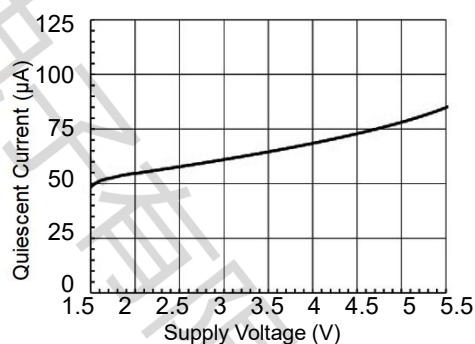
Open-loop Gain and Phase as a function of Frequency.



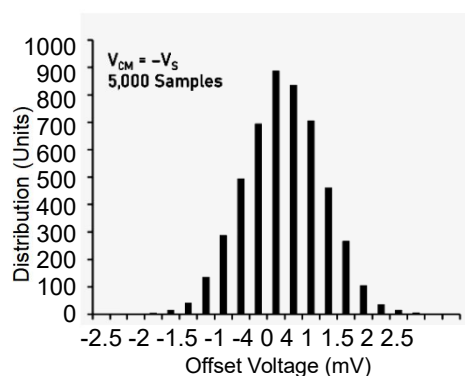
Common-mode Rejection Ratio as a function of Frequency.



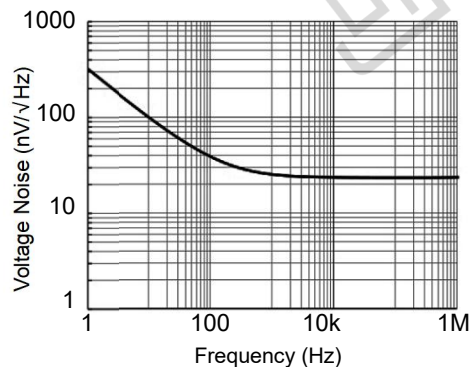
Power Supply Rejection Ratio as a function of Frequency.



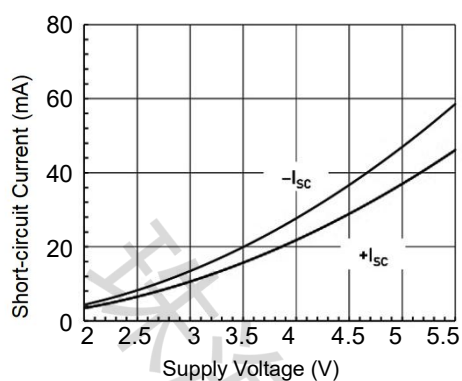
Quiescent Current as a function of Supply Voltage.



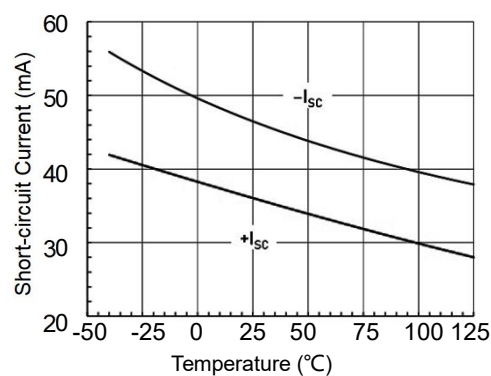
Offset Voltage Production Distribution



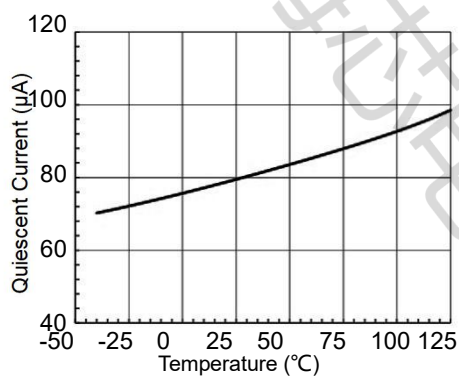
Input Voltage Noise Spectral Density as a function of Frequency.



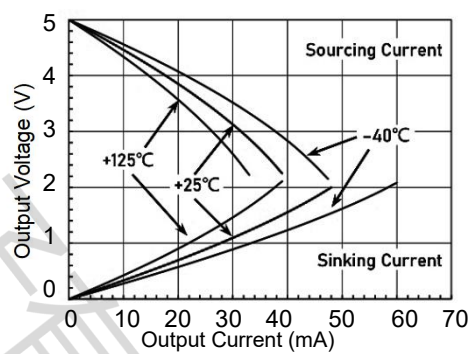
Short-circuit Current as a function of Supply Voltage.



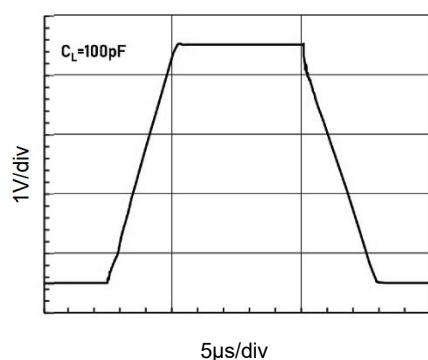
Short-circuit Current as a function of Temperature.



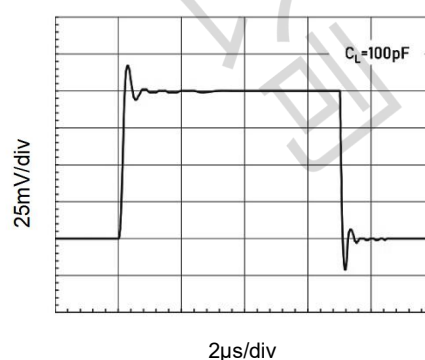
Quiescent Current as a function of Temperature.



Output Voltage Swing as a function of Output Current.

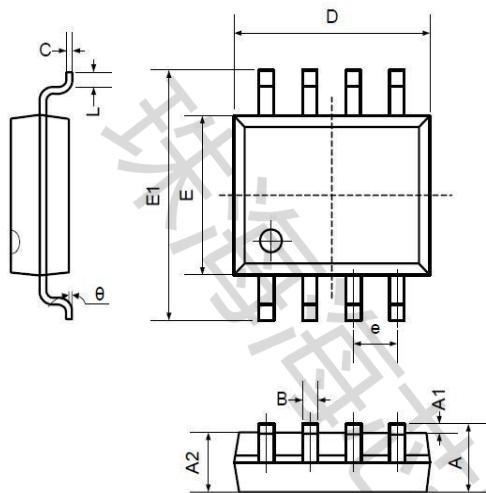


Large Signal Step Response.

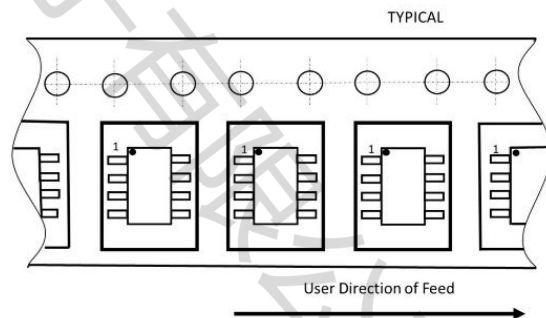
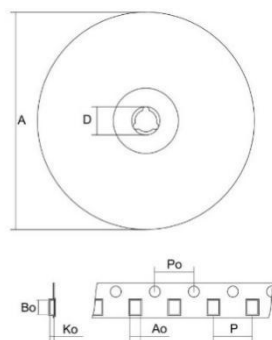


Small Signal Step Response.

SOP-8 (Package Outline Dimensions)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
B	0.330	0.510	0.013	0.020
C	0.190	0.250	0.007	0.010
D	4.780	5.000	0.188	0.197
E	3.800	4.000	0.150	0.157
E1	5.800	6.300	0.228	0.248
e	1.270TYP		0.050TYP	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Part Number	Package Type	package	quantity
AD8542ARZ-REEL7-HX	SOP-8	Taping	2500

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