

**Features**

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications



SOD-123FL

**Mechanical Data**

- Case: SOD-123FL
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 15mg 0.00048oz

**Pinning**

PIN	DESCRIPTION
1	Cathode
2	Anode

**Absolute Maximum Ratings and Electrical characteristics**

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 %

Parameter	Symbols	PMEG4010CEH,115-JSM		Units
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	40		V
Maximum RMS voltage	V <sub>RMS</sub>	28		V
Maximum DC Blocking Voltage	V <sub>DC</sub>	40		V
Maximum Average Forward Rectified Current	I <sub>F(AV)</sub>	1.0		A
Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	30		A
Max Instantaneous Forward Voltage at 1 A	V <sub>F</sub>	0.55		V
Maximum DC Reverse Current at Rated DC Reverse Voltage	I <sub>R</sub>	0.3 10		mA
Typical Junction Capacitance <sup>(1)</sup>	C <sub>j</sub>	110		pF
Typical Thermal Resistance <sup>(2)</sup>	R <sub>θJA</sub>	100		°C/W
Operating Junction Temperature Range	T <sub>j</sub>	-55 ~ +125		°C
Storage Temperature Range	T <sub>stg</sub>	-55 ~ +150		°C

( 1 ) Measured at 1 MHz and applied reverse voltage of 4 V D.C

( 2 ) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.

Fig.1 Forward Current Derating Curve

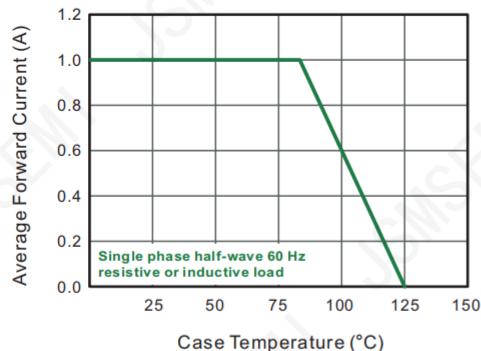


Fig.2 Typical Reverse Characteristics

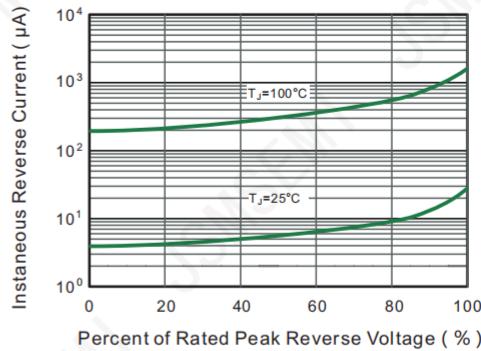


Fig.3 Typical Forward Characteristic

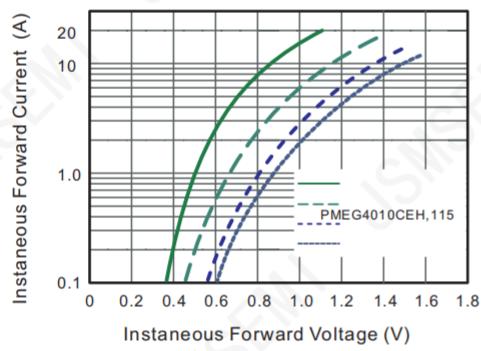


Fig.4 Typical Junction Capacitance

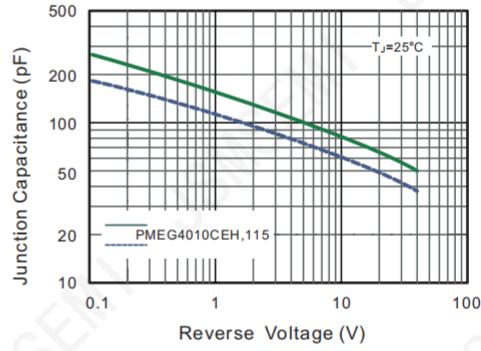


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

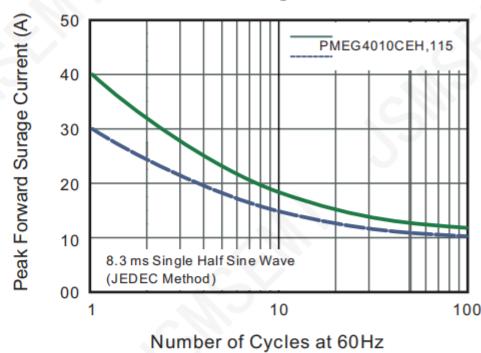
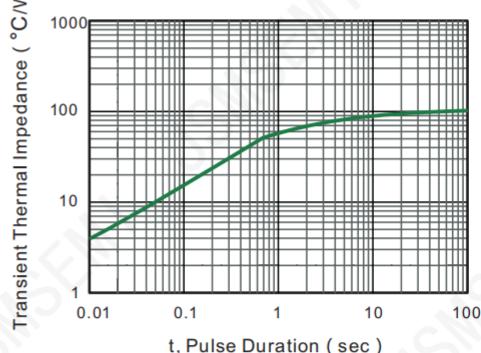


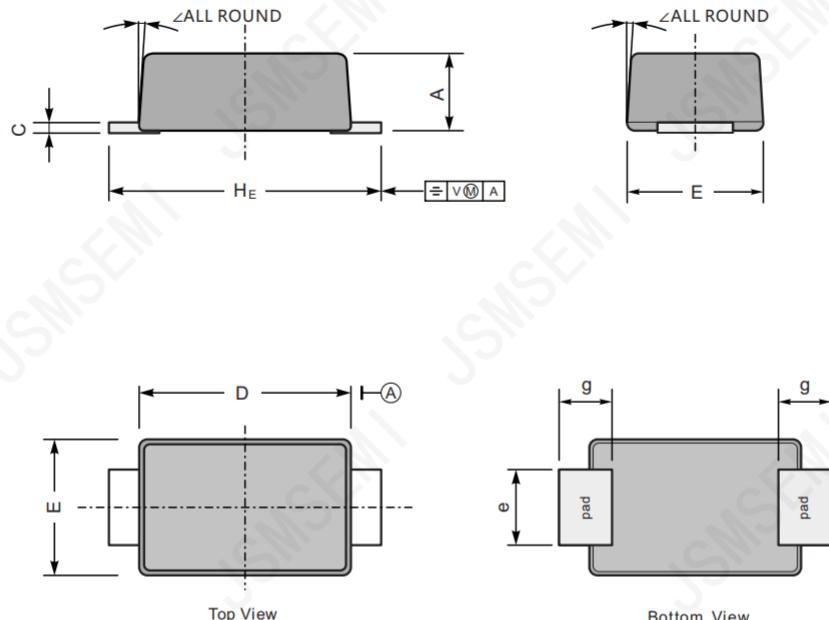
Fig.6- Typical Transient Thermal Impedance



## Package Outline

Plastic surface mounted package; 2 leads

SOD-123FL



UNIT		A	C	D	E	e	g	H <sub>E</sub>	<
mm	max	1.1	0.20	2.9	1.9	1.1	0.9	3.8	7°
	min	0.9	0.12	2.6	1.7	0.8	0.7	3.5	
mil	max	43	7.9	114	75	43	35	150	7°
	min	35	4.7	102	67	31	28	138	

## Revision History

Rev.	Change	Date
V1.0	Initial version	6/27/2021

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