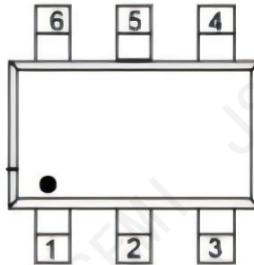


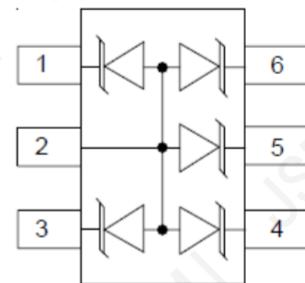
Description

The PESD3V3L5UY,115-JSM is a ESD array designed to protect five I/O lines against ESD and latch-up surges. Utilizing advanced technology, it offers high peak pulse power handling and fast response time, making it suitable for voltage-sensitive electronic devices. The device features a compact SOT-363 package, low leakage current, and complies with RoHS standards, ensuring reliability in various applications.



Features

- 100W (8/20μs) Peak Pulse Power
- ESD and Latch-Up Protection
- SOT-363 Package
- RoHS Compliant
- Matte Tin Lead finish (Pb-Free)
- Protect Five I/O Lines
- Meet IEC61000-4-2 Level 4:
 Contact Discharge > 20kV
 Air Discharge > 20kV



SOT-363

Applications

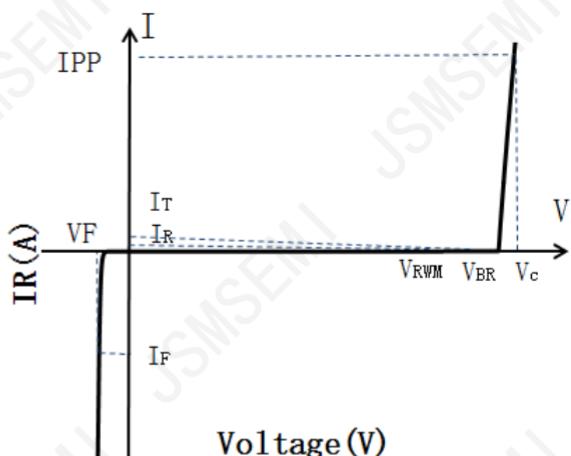
- Peripherals
- Audio Players
- Digital Cameras
- Cordless Phones
- Portable Instrumentation
- Notebooks and Handhelds
- Personal Digital Assistants (PDA,s)
- Cellular Handsets and Accessories

Maximum Ratings($T_A=25^\circ\text{C}$)

Symbol	Parameter	Value	Unit
PPK	Peak Pulse Power	100	W
IPP	Peak Pulse Current	6	A
V _{ESD} (Contact)	Contact ESD Voltage per IEC61000-4-2	20	kV
V _{ESD} (Air)	Air ESD Voltage per IEC61000-4-2	20	kV
T _J	Junction Temperature	-55 to +125	°C
T _{TG}	Storage Temperature	-55 to +150	°C

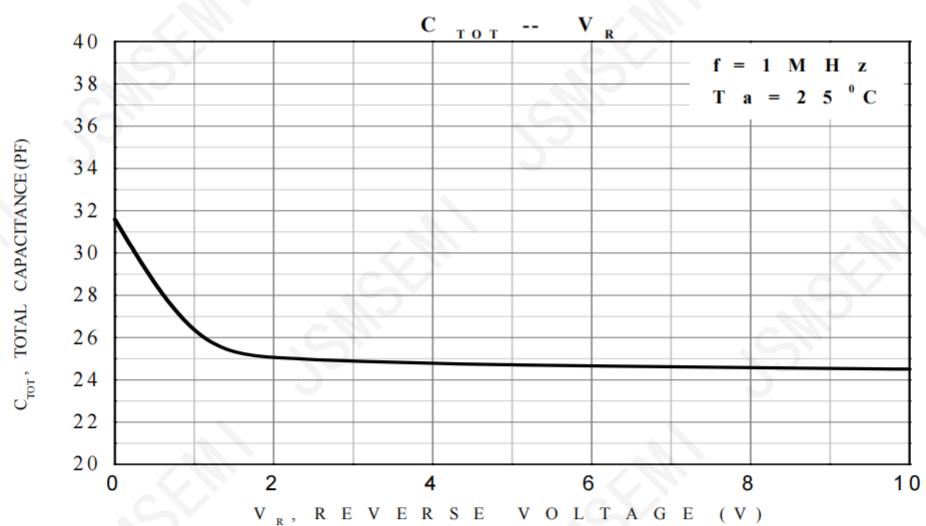
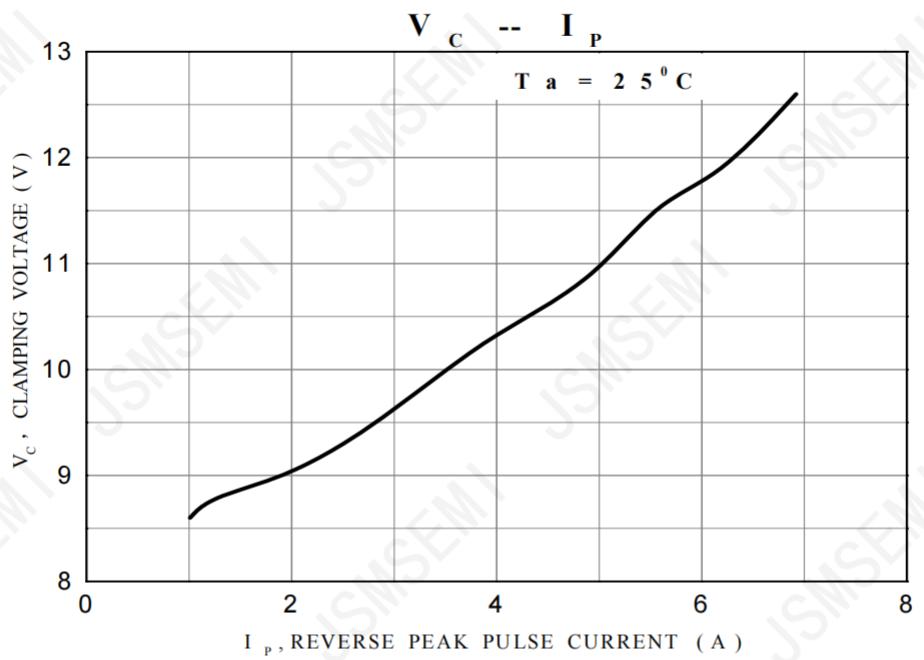
Maximum Ratings(T_A=25 °C)

Symbol	Parameter
IT	Test Current
IPP	Maximum Reverse Peak Pulse Current
V _c	Clamping Voltage @I _c

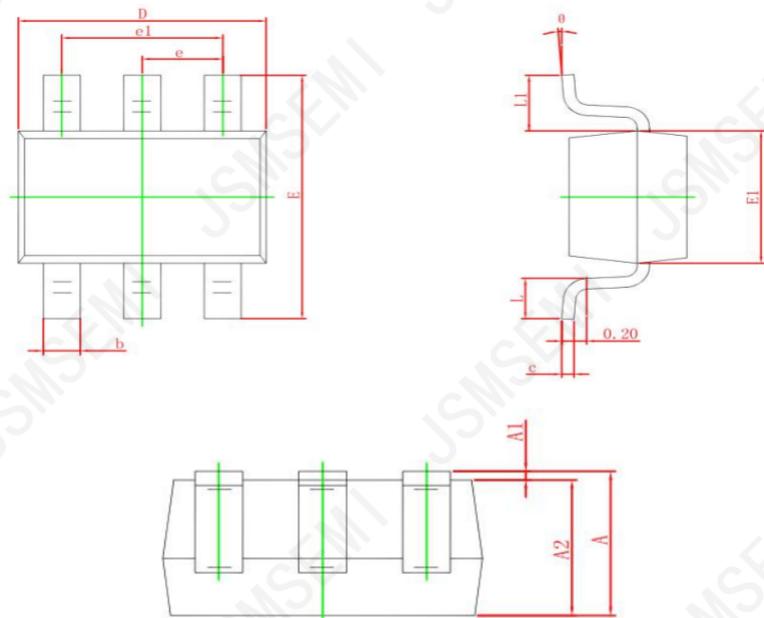

Electrical Characteristics(T_A=25 °C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{RWM}	Reverse Working Peak Voltage				5	V
V _{BR}	Reverse Breakdown Voltage	IT=1mA	6		8	V
I _R	Reverse Leakage Current	VRWM=5V			5	µA
V _c	Clamping Voltage	IPP=1A(8/20µs)			10	V
V _c	Clamping Voltage	IPP=6A(8/20µs)			18	V
C _J	Capacitance	VR=0V,f=1MHz		50		pF

Typical Performance Curves



Package Outline Dimensions



Symbol	Dim in mm		
	Min	Nor	Max
A	0.90	1.00	1.10
A1	0.00	0.05	0.10
A2	0.90	0.95	1.00
b	0.15	0.25	0.35
c	0.08	0.12	0.15
D	2.05	2.10	2.25
E	2.15	2.30	2.45
E1	1.15	1.25	1.35
e	0.650TPY.		
e1	1.2	1.3	1.4
L	0.26	0.36	0.46
L1	0.525REF.		
θ	0°	4°	8°

Revision History

Rev.	Change	Date
V1.0	Initial version	6/27/2021

Important Notice

JSMSEMI Semiconductor (JSMSEMI) PRODUCTS ARE NEITHER DESIGNED NOR INTENDED FOR USE IN MILITARY AND/OR AEROSPACE, AUTOMOTIVE OR MEDICAL DEVICES OR SYSTEMS UNLESS THE SPECIFIC JSMSEMI PRODUCTS ARE SPECIFICALLY DESIGNATED BY JSMSEMI FOR SUCH USE. BUYERS ACKNOWLEDGE AND AGREE THAT ANY SUCH USE OF JSMSEMI PRODUCTS WHICH JSMSEMI HAS NOT DESIGNATED FOR USE IN MILITARY AND/OR AEROSPACE, AUTOMOTIVE OR MEDICAL DEVICES OR SYSTEMS IS SOLELY AT THE BUYER'S RISK.

JSMSEMI assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using JSMSEMI products.

Resale of JSMSEMI products or services with statements different from or beyond the parameters stated by JSMSEMI for that product or service voids all express and any implied warranties for the associated JSMSEMI product or service. JSMSEMI is not responsible or liable for any such statements.

JSMSEMI All Rights Reserved. Information and data in this document are owned by JSMSEMI wholly and may not be edited, reproduced, or redistributed in any way without the express written consent from JSMSEMI.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the JSMSEMI product that you intend to use.

For additional information please contact Kevin@jsmsemi.com or visit www.jsmsemi.com