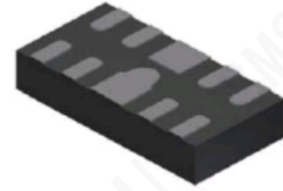


Features

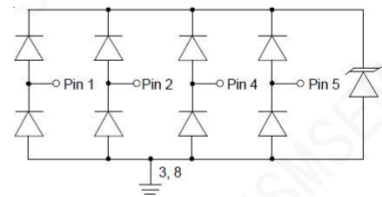
- ◆ 50 W(8/20 μ s)Peak Pulse Power
- ◆ Low Capacitance ESD Protection
- ◆ Flow Through DFN2.5x1.0-10L Package
- ◆ RoHS Compliant
- ◆ Matte Tin Lead finish(Pb-Free)
- ◆ Protect Four High Speed Data Lines
- ◆ Meet IEC61000-4-2 Level 4:

Contact Discharge>20kV

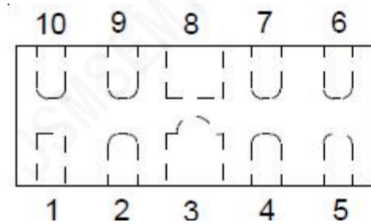
Air Discharge>25kV



Circuit Diagram



PIN Diagram



Applications

- ◆ PCI Express
- ◆ MDDI Ports
- ◆ eSATA Interfaces
- ◆ Display Port Interface
- ◆ Digital Visual Interface (DVI)
- ◆ High Definition Multi-Media Interface(HDMI)

Ordering information

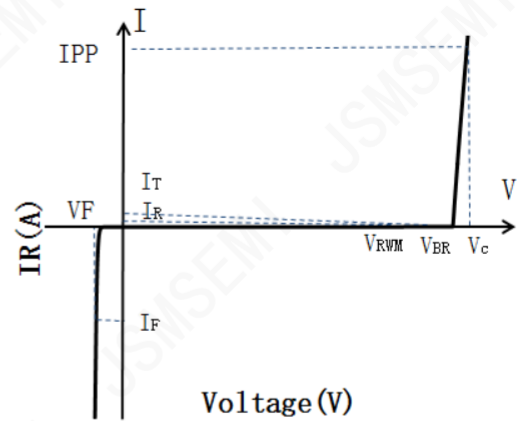
Device	Package	ReelSize	Qty/Reel
PUSB3FR4Z-JSM	DFN2510-10L	7 inch	3000

Maximum Ratings(Ta=25 °C)

Symbol	Parameter	Value	Unit
PPK	Peak Pulse Power	50	W
IPP	Peak Pulse Current	4.5	A
VESD(Contact)	Contact ESD Voltage per IEC61000-4-2	20	kV
VESD(Air)	Air ESD Voltage per IEC61000-4-2	25	kV
TJ	Junction Temperature	-55 to+125	°C
TSTG	Storage Temperature	-55 to+150	°C

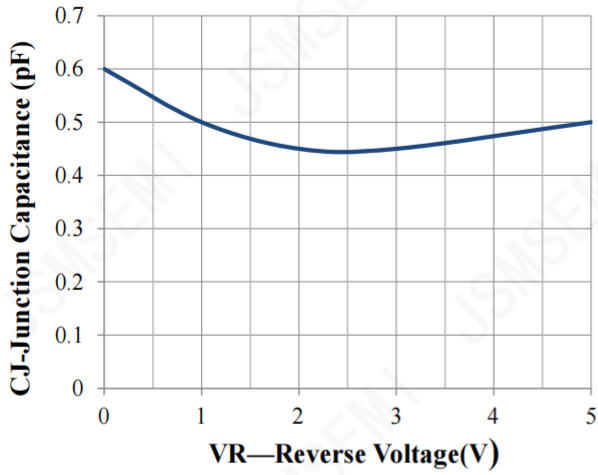
Portion Electronics Parameter

Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C

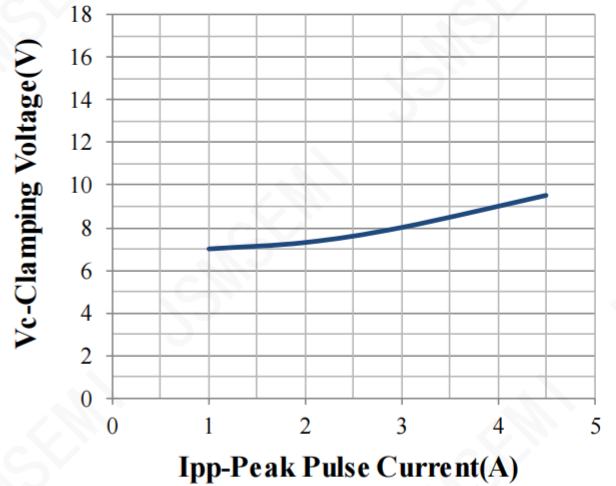

Electrical Characteristics($T_a=25^\circ\text{C}$)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
VRWM	Reverse Working Peak Voltage				3.3	V
VBR	Reverse Breakdown Voltage	$I_T=1\text{mA}$	4	5.2	6.5	V
IR	Reverse Leakage Current	$VRWM = 3.3\text{V}$			0.1	μA
VC	Clamping Voltage	$I_{PP}=4.5\text{A}(8/20\mu\text{s})$			11	V
CJ	Capacitance	$VR=0\text{V}, f=1\text{MHz}$ Between I/O pins		0.3	0.4	pF
CJ	Capacitance	$VR=0\text{V}, f=1\text{MHz}$ Any I/O pin to ground		0.6	0.7	pF

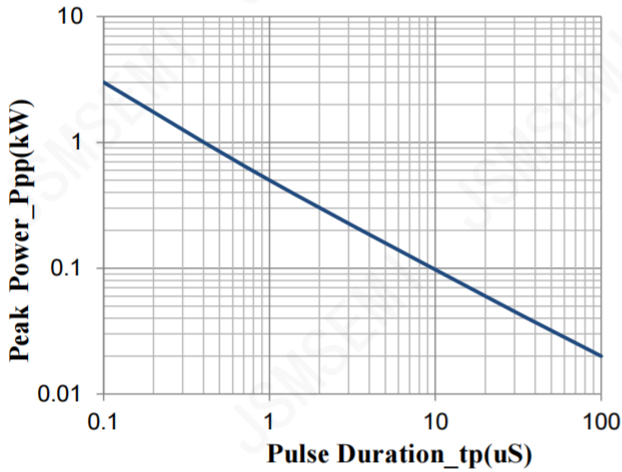
Typical Performance Curves



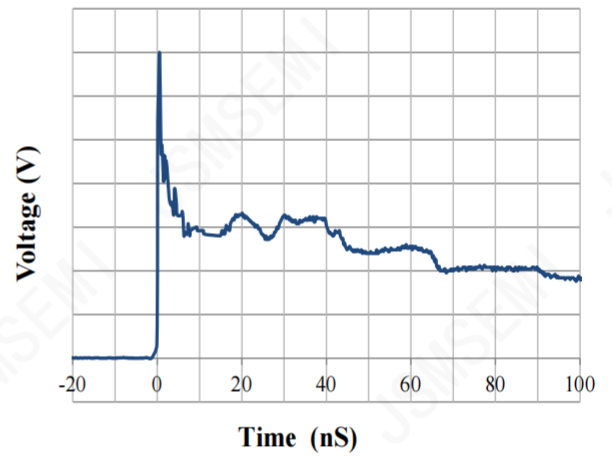
Junction Capacitance vs. Reverse Voltage



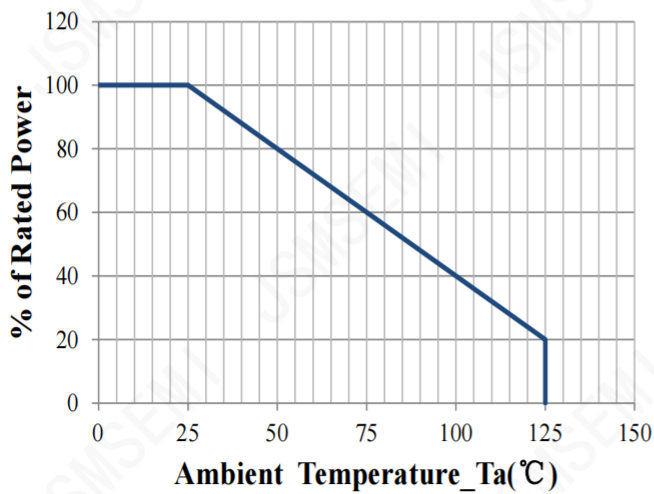
Clamping Voltage vs. Peak Pulse Current



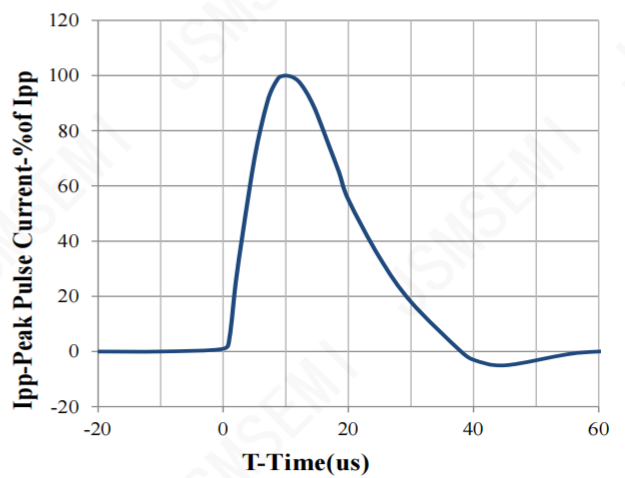
Peak Pulse Power vs. Pulse Time



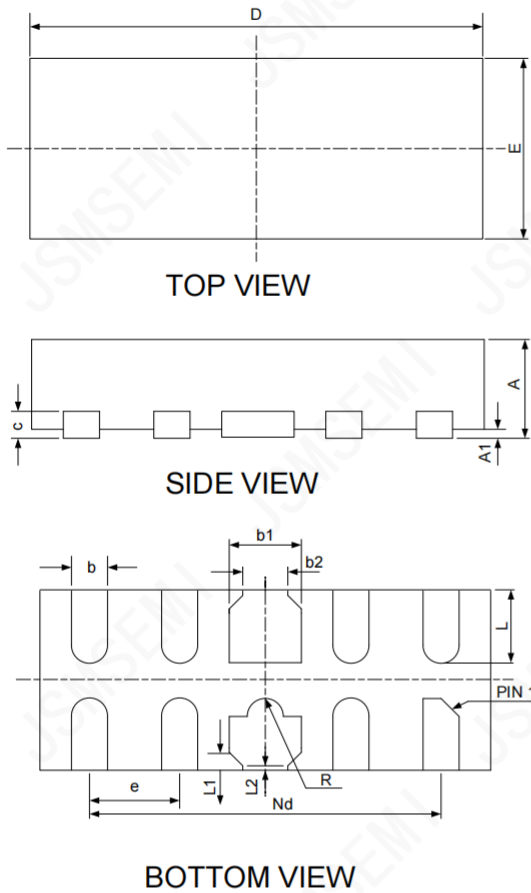
IEC61000-4-2 Pulse Waveform



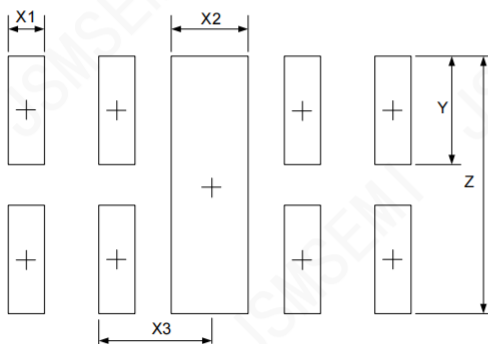
Power Derating Curve



8 X 20us Pulse Waveform

DFN2510-10L Package Outline Drawing


SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.45	0.50	0.55	0.018	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.15	0.20	0.25	0.006	0.008	0.010
b1	0.35	0.40	0.45	0.014	0.016	0.018
b2	0.20	0.25	0.30	0.008	0.010	0.012
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.45	2.50	2.55	0.098	0.100	0.102
e	0.50BSC			0.020BSC		
Nd	2.00BSC			0.080BSC		
E	0.95	1.00	1.05	0.038	0.040	0.042
L	0.35	0.40	0.45	0.014	0.016	0.018
L1	0.075REF			0.003REF		
L2	0.050REF			0.002REF		
h	0.08	0.12	0.15	0.003	0.005	0.006
R	0.05	0.10	0.15	0.002	0.004	0.006

Suggested Land Pattern


SYM	DIMENSIONS	
	MILLIMETERS	INCHES
X1	0.200	0.008
X2	0.400	0.016
X3	0.500	0.020
Y	0.600	0.024
Z	1.400	0.056

Revision History

Rev.	Change	Date
V1.0	Initial version	6/27/2021

Important Notice

JSMSEMI Semiconductor (JSMSEMI) PRODUCTS ARE NEITHER DESIGNED NOR INTENDED FOR USE IN MILITARY AND/OR AEROSPACE, AUTOMOTIVE OR MEDICAL DEVICES OR SYSTEMS UNLESS THE SPECIFIC JSMSEMI PRODUCTS ARE SPECIFICALLY DESIGNATED BY JSMSEMI FOR SUCH USE. BUYERS ACKNOWLEDGE AND AGREE THAT ANY SUCH USE OF JSMSEMI PRODUCTS WHICH JSMSEMI HAS NOT DESIGNATED FOR USE IN MILITARY AND/OR AEROSPACE, AUTOMOTIVE OR MEDICAL DEVICES OR SYSTEMS IS SOLELY AT THE BUYER' S RISK.

JSMSEMI assumes no liability for application assistance or customer product design. Customers are responsible for their products and applications using JSMSEMI products.

Resale of JSMSEMI products or services with statements diferent from or beyond the parameters stated by JSMSEMI for that product or service voids all express and any implied warranties for the associated JSMSEMI product or s ervice. JSMSEMI is not responsible or liable for any such statements.

JSMSEMI All Rights Reserved. Information and data in this document are owned by JSMSEMI wholly and may not be edited, reproduced, or redistributed in any way without the express written consent from JSMSEMI.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the JSMSEMI product that you intend to use.

For additional information please contact Kevin@jsmsemi.com or visit www.jsmsemi.com