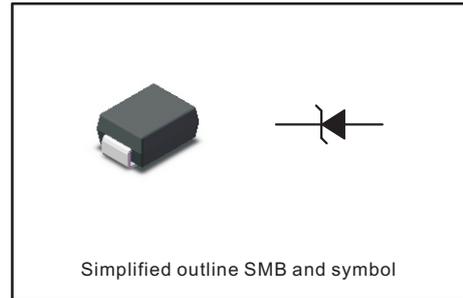


**SURFACE MOUNT SCHOTTKY BARRIER RECTIFIERS**
**FEATURES**

- Metal-Semiconductor junction with guard ring
- Epitaxial construction
- Low forward voltage drop
- High current capability
- The plastic material carries UL recognition 94V-0
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

**PINNING**

PIN	DESCRIPTION
1	Cathode
2	Anode


**MECHANICAL DATA**

- Case: Molded Plastic
- Polarity: Color band denotes cathode
- Weight: 0.007 ounces, 0.21 grams

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**

Type Number	SYMBOL	SS 22	SS 23	SS 24	SS 245	SS 25	SS 26	SS 28	SS 210	SS 215	SS 220	SS 225	Unit	
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	20	30	40	45	50	60	80	100	150	200	250	V	
Maximum RMS Voltage	$V_{RMS}$	14	21	28	31	35	42	56	70	105	140	175	V	
Maximum DC Blocking Voltage	$V_{DC}$	20	30	40	45	50	60	80	100	150	200	250	V	
Average Rectified Output Current @ $T_L = 100^\circ C$	$I_{F(AV)}$	2.0											A	
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	50											A	
Forward Voltage @ $I_F = 2.0A$	$V_{FM}$	0.55			0.7		0.85		0.92		0.95		V	
Peak Reverse Current @ $T_A = 25^\circ C$	$I_R$	0.1						0.05						mA
At Rated DC Blocking Voltage @ $T_A = 100^\circ C$		10						5						mA
$I^2t$ Rating for fusing ( $t < 8.3ms$ )	$I^2t$	10.37											$A^2s$	
Typical Junction Capacitance (Note 2)	$C_J$	80						40						pF
Typical Thermal Resistance	$R_{\theta JA}$	95											$^\circ C/W$	
Operating Temperature Range	$T_J$	-55 to +150											$^\circ C$	
Storage Temperature Range	$T_{STG}$	-55 to +150											$^\circ C$	

Note: 1. Pulse Test with  $PW = 300\mu sec$ , 1% Duty Cycle.

2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

Fig. 1 Forward Current Derating Curve

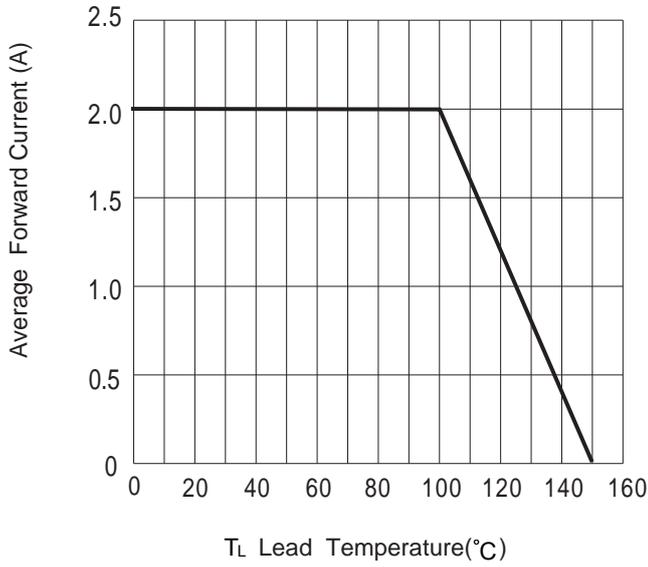


Fig. 2 Typ. Forward Characteristics

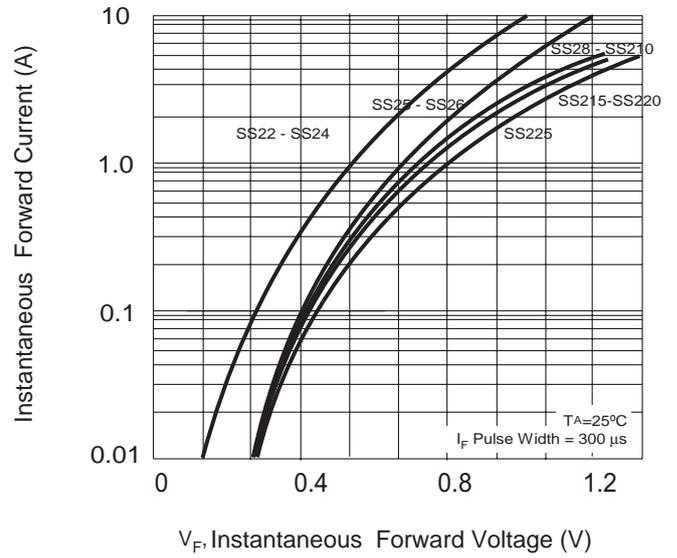


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

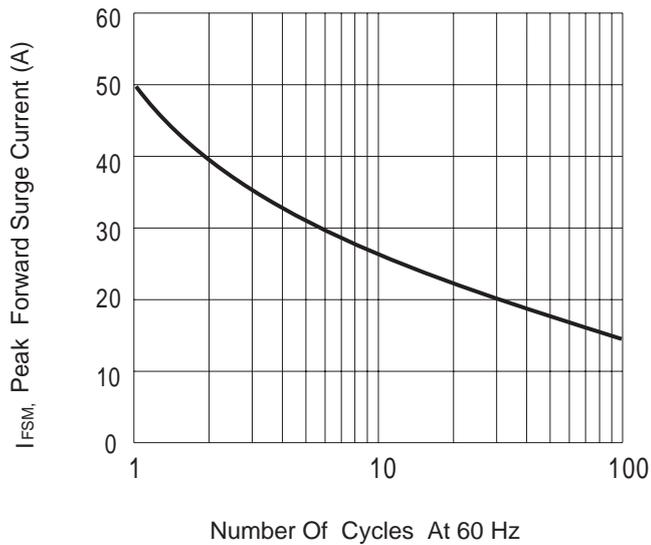


Fig.4 Typical Reverse Characteristics

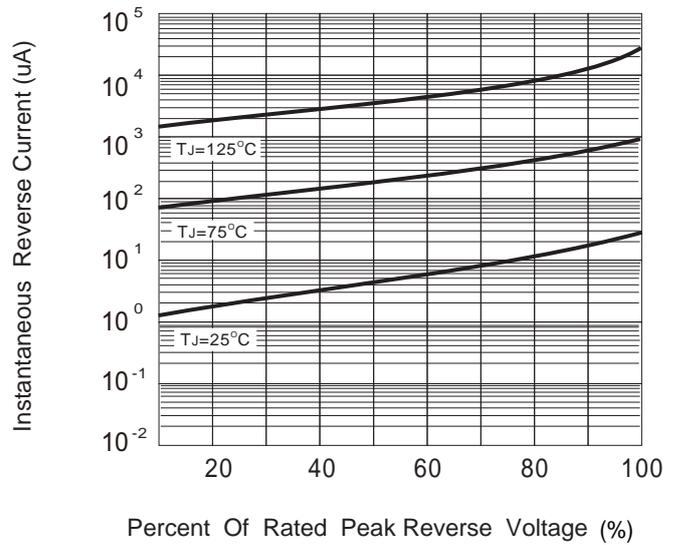
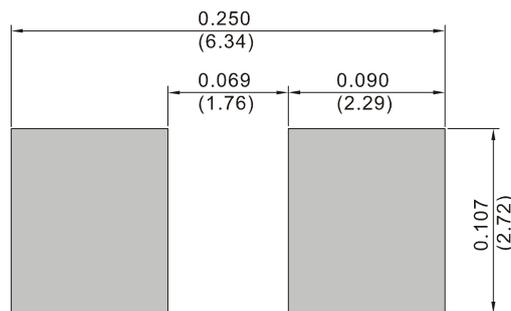
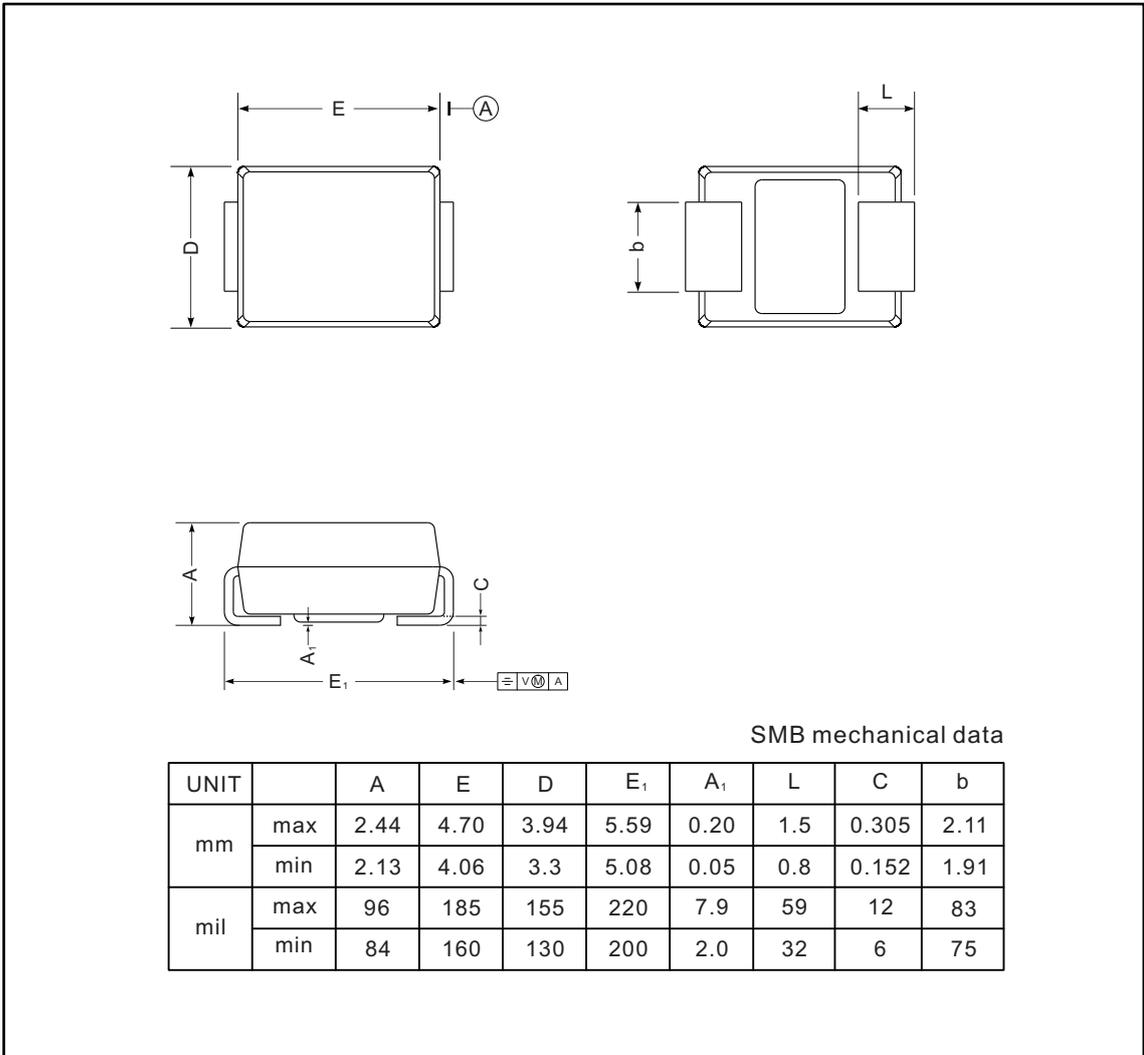


Fig.5 Mounting PAD Layout



## PACKAGE OUTLINE

Plastic surface mounted package; 2 leads



### The recommended mounting pad size

