



Discription

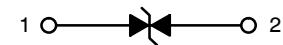
Low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in a DFN1006-2L(SOD-882) leadless ultra small Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.



Features

- ★ Bidirectional ESD protection of one line
- ★ Low operating voltage: 5.0V
- ★ Low clamping voltage $VC = 10.5V$ @ 100A
- ★ Response time is typically <1ns
- ★ Ultra Low Leakage: nA Level
- ★ IEC 61000-4-2: level 4 (ESD)
- ★ IEC 61000-4-5 (surge): IPPMQ100 A

DFN1006-2L



Applications

- ★ Portable electronics
- ★ Computers and peripherals
- ★ Audio and video equipment
- ★ Cellular handsets and accessories
- ★ Communication systems
- ★ Power supplies

Circuit Diagram

Ordering Information

Product ID	Pack	Qty(PCS)
ESD5V0LB	DFN1006-2L	10000



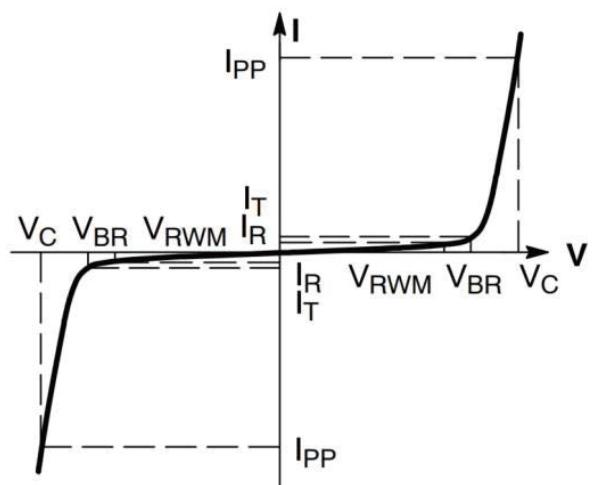
Absolute Ratings(Tamb = 25°C)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp = 8/20μs)	P _{PPM}	1000	W
Peak Pulse Current(tp = 8/20μs)	I _{PPM}	100	A
Maximum lead temperature for soldering during 10s	T _L	260	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C
Operating Temperature Range	T _{OP}	-40 to +125	°C
Maximum junction temperature	T _j	150	°C
ESD voltage IEC 61000-4-2 (air discharge)	V _{ESD}	30	kV
ESD voltage IEC 61000-4-2 (contact discharge)	V _{ESD}	30	kV

Electrical Characteristics

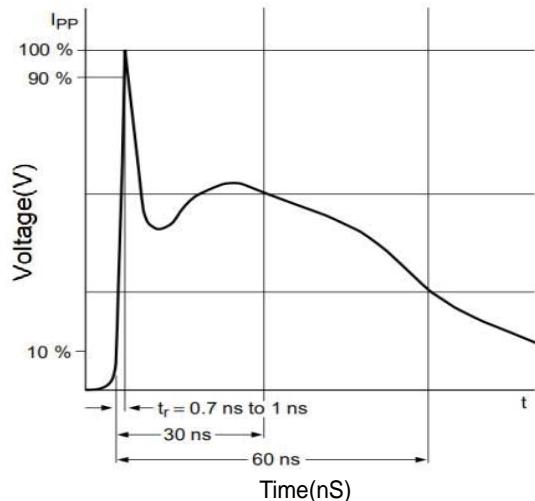
Parameter	Symbol	Min	Typ	Max	Unit	Condition
Reverse Working Voltage	V _{RWM}	--	--	5.0	V	
Breakdown Voltage	V _{BR}	5.8	--	7.0	V	I _T =1mA
Leakage Current I _{Leak}	I _R	--	--	100	nA	V _{RWM} =5.0V
Clamping Voltage	V _C	--	7.5	9.0	V	I _{PP} =50A, Tp=8/20μs
Clamping Voltage	V _C	--	9.0	10.5	V	I _{PP} =100A, Tp=8/20μs
Junction Capacitance	C _J	--	200	250	pF	V _R =0V, f=1MHz

Symbol	Parameter
I _{PPM}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Reverse Leakage Current @ V _{RWM}
I _T	Test Current
V _{BR}	Breakdown Voltage @ I _T

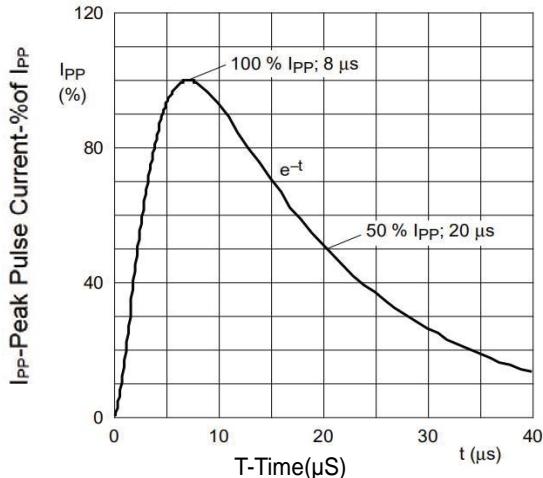




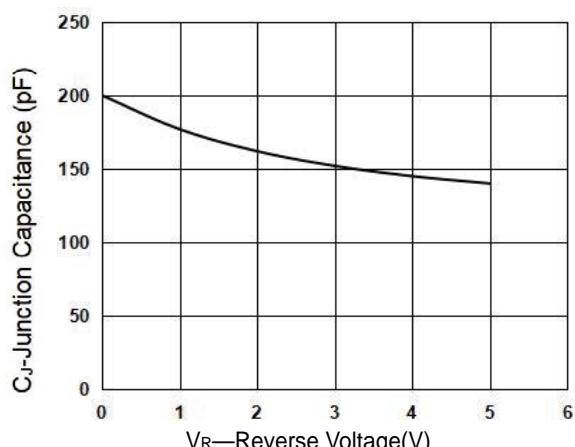
Typical Characteristics



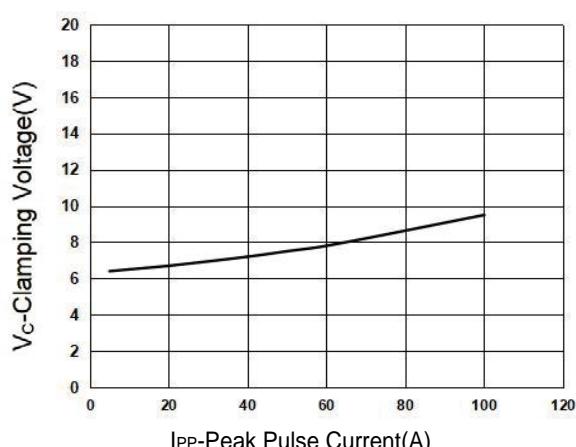
IEC61000-4-2 Pulse Waveform



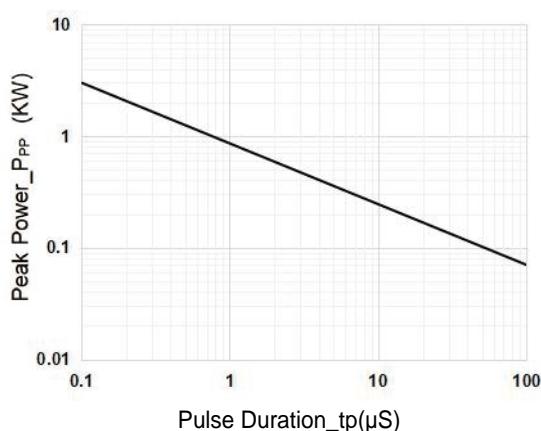
IEC61000-4-5 8X20μs Pulse Waveform



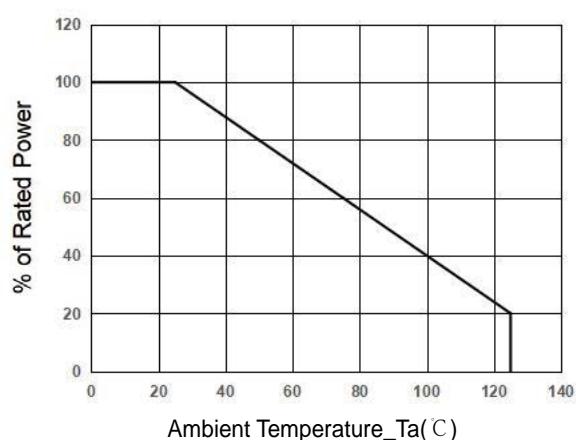
Junction Capacitance vs. Reverse Voltage



Clamping Voltage vs. Peak Pulse Current



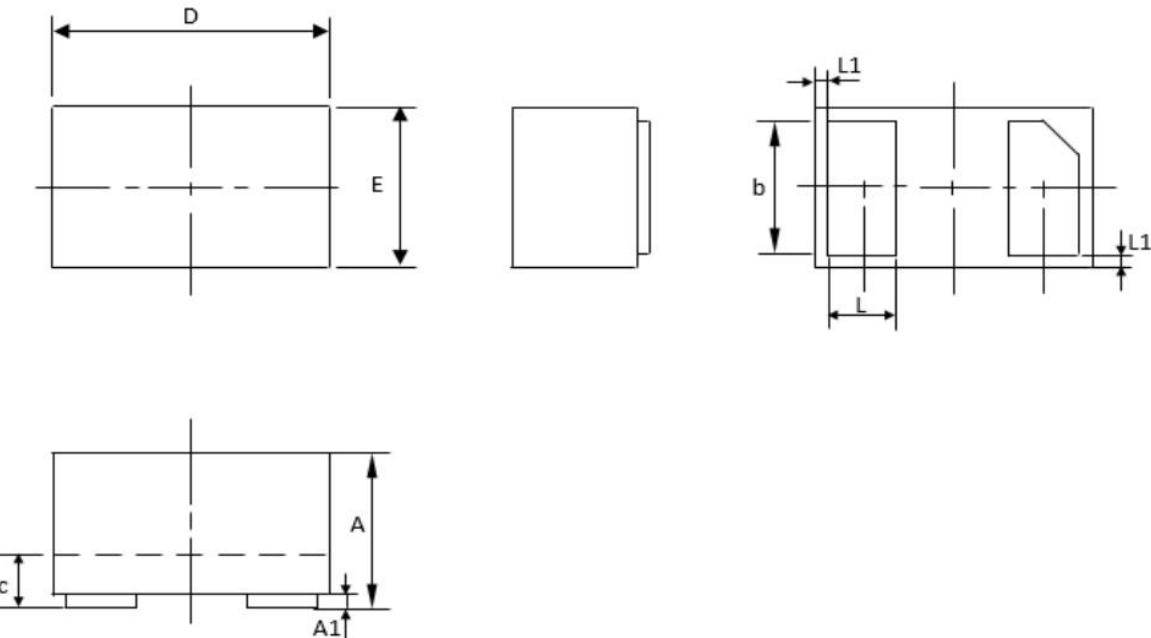
Peak Pulse Power vs. Pulse Time



Power Derating Curve



Outline And Dimensions



DFN1006-2L			
Dim	Min	Typ.	Max
A	0.46	0.48	0.50
A1	0	0.02	0.05
b	0.45	0.5	0.55
c	0.1	0.12	0.14
D	0.95	1.00	1.05
E	0.55	0.60	0.65
L	0.20	0.25	0.30
L1	0.035	0.05	0.065
h	0.07	0.12	0.17

All Dimensions in mm



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.