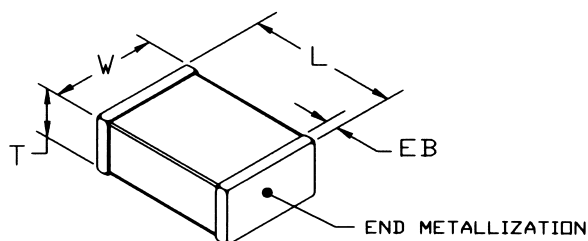


RoHS COMPLIANT



SPECIFICATIONS:

LEGACY P/N: 101R14W104KV4T

ELECTRICAL CHARACTERISTICS:

DIELECTRIC	W= X7R
RATED VOLTAGE	101= 100 VDC
CAPACITANCE RANGE (EIA)	104 = 0.10 μ F
CAPACITANCE TOLERANCE	K ($\pm 10\%$)
TEST PARAMETERS	1 kHz ± 50 Hz @ 1.0 VRMS, 25°C
TEMPERATURE COEFFICIENT	$\pm 15\%$ (-55°C TO +125°C)
DISSIPATION FACTOR	< 2.5% @ 1kHz, 25°C
INSULATION RESISTANCE	500 Ohm-Farad OR 50 G-Ohms, whichever is less @ WVDC, 25°C (@ 125°C IR is 10% of 25°C requirement)
DIELECTRIC STRENGTH	2.5 X WVDC, 50 mA max

MECHANICAL CHARACTERISTICS: CP: Size 0603 Unit:mm

LENGTH "L"	WIDTH "W"	THICKNESS "T"	ENDBAND "EB"
1.6 $\pm$ 0.1	0.8 $\pm$ 0.1	0.8 $\pm$ 0.15	0.30 $\pm$ 0.1

SUBFAMILY DESCRIPTION: TF - Surface Mount BME

MARKING DESCRIPTION: 1 - No Mark

TERMINATION DESCRIPTION GV - Ni/Sn (ROHS)

SPECIAL CODE: 001 - DEFAULT CATALOG ITEM

PACKAGING: T - 7" PAPER TAPE/REEL

				JOHANSON DIELECTRICS 		4001 Calle Tecate, Camarillo, CA 93012		
				TITLE				
REV	DESCRIPTION	APPROVED	DATE	MULTILAYER CHIP CAPACITOR				
ENG. MGR.		DATE	Q.A. MGR.	DATE	GLOBAL PART NO. TFCP101W104K1GV001T		DWG SCALE NONE	
DRN BY J. TANG		DATE 2026/1/21	CHKD BY	DATE	DWG. NO. A-7003-04-96		SHEET 1 OF 1	RE V