

## DFN1610-2L Plastic-Encapsulate ESD Protection Diodes

### Features

- Low leakage current
- Low clamping voltage
- IEC 61000-4-2 (ESD Air):  $\pm 30\text{kV}$
- IEC 61000-4-2 (ESD Contact):  $\pm 30\text{kV}$
- IEC 61000-4-5 (Lightning 8/20 $\mu\text{s}$ ): 120A

### Applications

- Mobile Phone, Digital cameras
- Battery Protection
- Power Line Protection
- Vbat pin for Mobile Devices
- Hand Held Portable Applications

### Function Diagram



**Reverse Working Voltage**  
7.0 V Max.  
**High Capacitance**  
400 pF (Typ.)

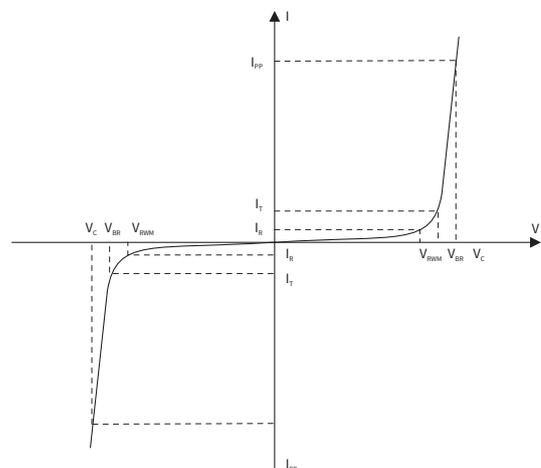


### Maximum Ratings (Ta=25°C Unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>ESD</sub>	Electrostatic Discharge Voltage	ESD per IEC 61000-4-2( Air )	$\pm 30$	KV
		ESD per IEC 61000-4-2( Contact)	$\pm 30$	KV
P <sub>PP</sub>	Peak Pulse Power	tp = 8/20 $\mu\text{s}$	1900	W
I <sub>PP</sub>	Rated Peak Pulse Current	tp = 8/20 $\mu\text{s}$	120	A
T <sub>J</sub>	Operating JunctionTemperature Range	—	-55 to +125	°C
T <sub>STG</sub>	Operating JunctionTemperature Range	—	-55 to +125	°C

### Electrical Parameter

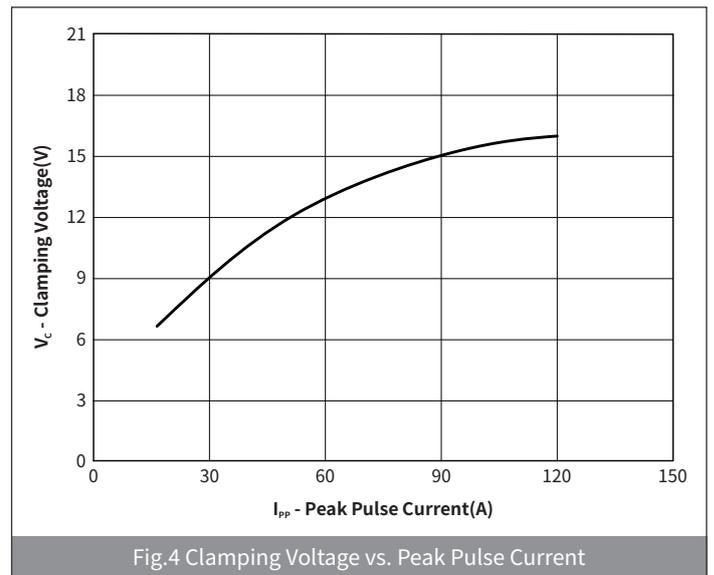
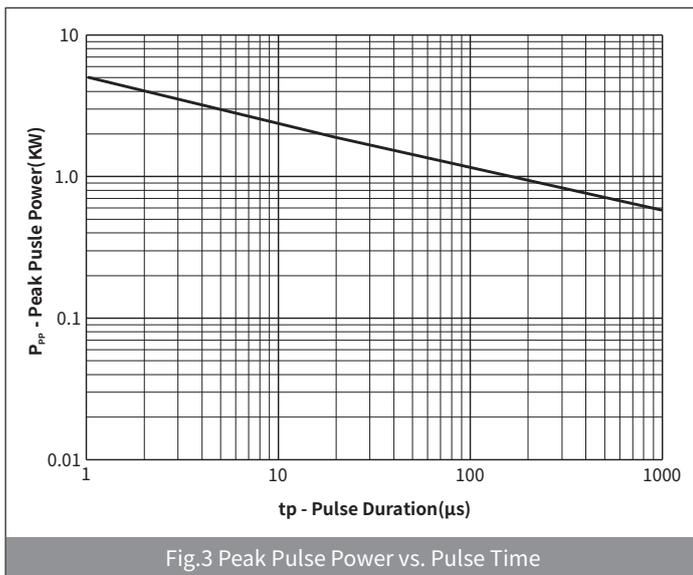
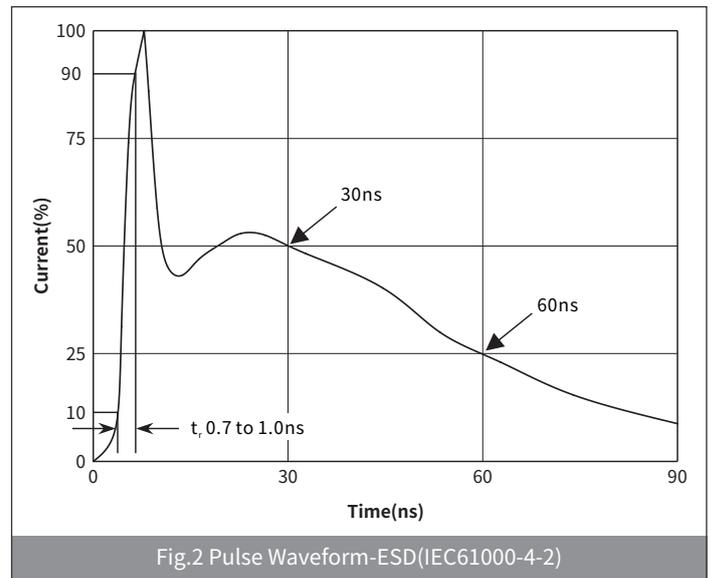
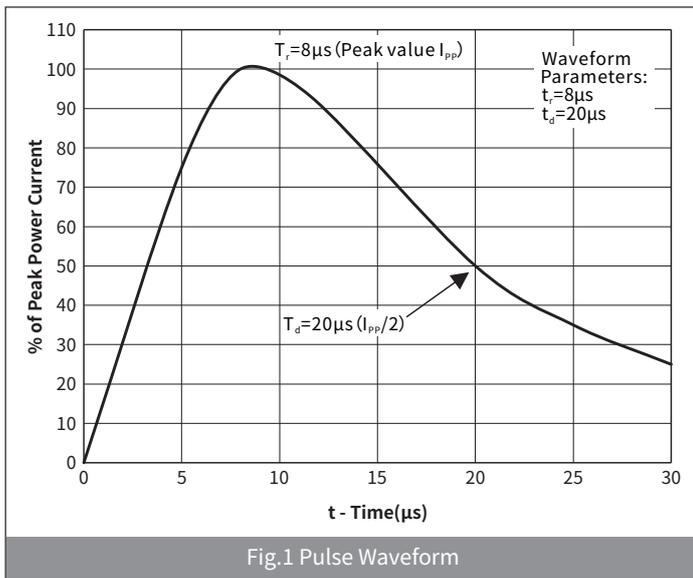
SYMBOL	PARAMETER
V <sub>C</sub>	Clamping Voltage @ I <sub>PP</sub>
V <sub>BR</sub>	Breakdown Voltage @ I <sub>T</sub>
I <sub>PP</sub>	Peak Pulse Current
I <sub>T</sub>	Test Current
I <sub>R</sub>	Reverse Leakage Current @ VRWM
V <sub>RWM</sub>	Peak Reverse Working Voltage
P <sub>PP</sub>	Peak Pulse Power Dissipation
C <sub>J</sub>	Junction Capacitance @ V <sub>R</sub> =0V,f=1MHz
I <sub>F</sub>	Forward Current
V <sub>F</sub>	Forward Voltage @I <sub>F</sub>



● **Electrical Characteristics** (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	CONDITION	Min	Typ	Max	UNIT
Peak Reverse Working Voltage	$V_{RWM}$	$T_a=25^\circ\text{C}$	—	—	7.0	V
Breakdown Voltage	$V_{BR}$	$I_T=1.0\text{mA}, T_a=25^\circ\text{C}$	7.6	—	—	V
Reverse Leakage Current	$I_R$	$V_{RWM}=7.0\text{V}, T_a=25^\circ\text{C}$	—	0.1	0.5	$\mu\text{A}$
Clamping Voltage	$V_C$	$I_{PP}=120\text{A}, t_p=8/20\mu\text{s}$	—	16	—	V
Junction Capacitance	$C_J$	$V_{RWM}=0\text{V}, f=1\text{MHz}$	—	400	—	pF

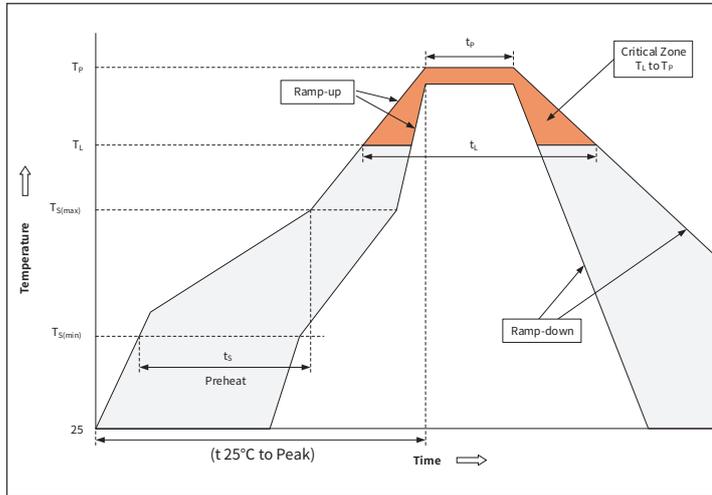
● **Ratings And Characteristics Curves** (Ta=25°C Unless otherwise specified)



## Ordering Information

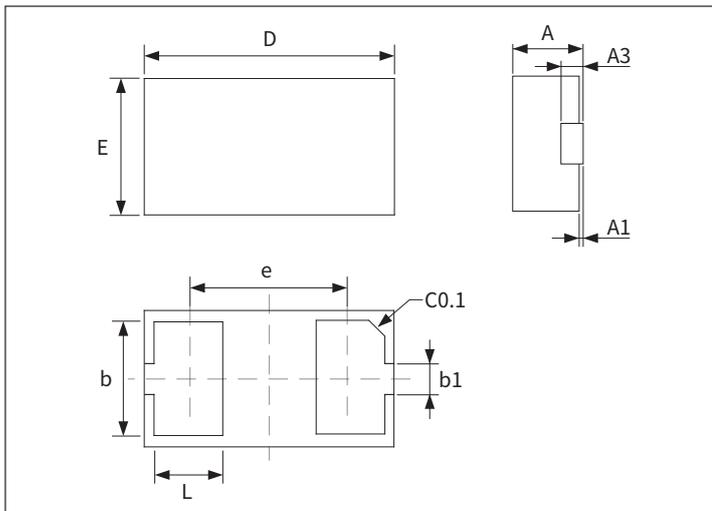
PREFERED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
H7VH16B	DFN1610-2L	1.575×0.975×0.485	7" REEL	3000

## Recommended Soldering Conditions



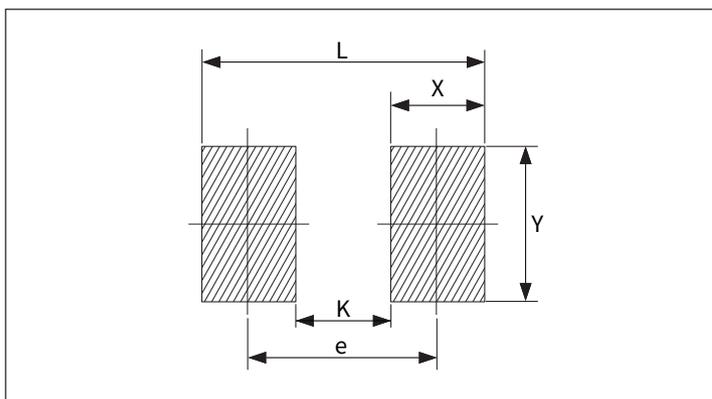
Profile Feature		Pb-Free Assembly
Pre-heat	Temperature Min ( $T_{S(min)}$ )	+150°C
	Temperature Max ( $T_{S(max)}$ )	+200°C
	Time (Min to Max) ( $t_s$ )	60-180 secs.
Average ramp up rate (Liquid us Temp ( $T_L$ ) to peak)		3°C/sec. Max
$T_{S(max)}$ to $T_L$ - Ramp-up Rate		3°C/sec. Max
Reflow	Temperature ( $T_L$ ) (Liquid us)	+217°C
	Temperature ( $t_L$ )	60-150 secs.
Peak Temp ( $T_P$ )		+260(+0/-5)°C
Time within 5°C of actual Peak Temp ( $t_p$ )		20-40secs
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp ( $T_P$ )		8 min. Max
Do not exceed		+260°C

## Package Outline Dimensions (DFN1610-2L)



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.46	0.56	0.018	0.022
A1	0.01	0.05	0.001	0.003
b	0.75	0.85	0.030	0.033
b1	0.25	0.35	0.010	0.014
D	1.55	1.65	0.061	0.065
E	0.95	1.05	0.037	0.041
e	1.10BSC		0.043 BSC	
L	0.35	0.45	0.014	0.018
A3	0.127REF		0.005REF	

## Suggested Pad Layout



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
X	0.57	0.67	0.024	0.028
Y	0.95	1.05	0.039	0.043
L	1.79	1.89	0.072	0.076
e	1.17	1.27	0.048	0.052
K	0.55	0.65	0.024	0.028