

## 1. Description

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The series are an ultra low capacitance TVS array designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to data an transmission lines from overvoltage caused by electrostatic discharge (ESD), cable discharge events (CDE) and lightning.

## 3. Features

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- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOD-323 surface mount package
- Protects one I/O line
- Peak power dissipation of 350W under 8/20 $\mu$ s waveform
- Working voltage: 3.3V, 5V, 12V and 15V

## 2. Applications

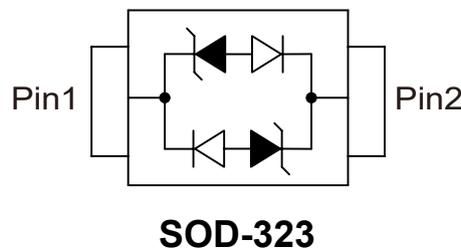
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- USB 2.0 and USB 3.0 interface
- 10/100/1000 Ethernet
- Personal digital assistants (PDA)
- Serial ATA protection
- Digital visual interface (DVI)
- Wireless system devices
- Handhelds and notebooks
- Digital cameras
- RF interface

- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn 260~270°C
- Flammability rating UL 94V-0

## 4. Pinning information

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## 5. Absolute Maximum Ratings

Parameter	Symbol	Value	Units
Peak pulse power (tp=8/20μs waveform)	P <sub>PP</sub>	350	W
ESD voltage (Contact discharge)	V <sub>ESD</sub>	±8	kV
ESD voltage (Air discharge)		±15	kV
Lead soldering temperature	T <sub>L</sub>	260	°C
Storage & Junction temperature range	T <sub>STG</sub> , T <sub>J</sub>	-55 to 150	°C



## 6.1 Electrical Characteristics (T<sub>J</sub>=25°C)

UDD32C03L01 (Marking: AC)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Reverse stand-off voltage	V <sub>RWM</sub>				3.3	V
Reverse breakdown voltage	V <sub>BR</sub>	I <sub>BR</sub> =1mA	4			V
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =3.3V			5	μA
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =1A			7	V
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =5A			15	V
Peak Pulse Current (tp=8/20μs)	I <sub>pp</sub>				19	A
Off state junction capacitance	C <sub>J</sub>	0Vdc, f=1MHz		0.8		pF

UDD32C05L01 (Marking: BC)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Reverse stand-off voltage	V <sub>RWM</sub>				5	V
Reverse breakdown voltage	V <sub>BR</sub>	I <sub>BR</sub> =1mA	6			V
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =5V			5	μA
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =1A			9.8	V
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =5A			18.3	V
Peak Pulse Current (tp=8/20μs)	I <sub>pp</sub>				17	A
Off state junction capacitance	C <sub>J</sub>	0Vdc, f=1MHz		0.8		pF



## 6.2 Electrical Characteristics (T<sub>J</sub>=25°C)

UDD32C12L01 (Marking: DC)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Reverse stand-off voltage	V <sub>RWM</sub>				12	V
Reverse breakdown voltage	V <sub>BR</sub>	I <sub>BR</sub> =1mA	13.3			V
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =12V			1	μA
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =1A			19	V
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =5A			28.6	V
Peak Pulse Current (tp=8/20μs)	I <sub>PP</sub>				7	A
Off state junction capacitance	C <sub>J</sub>	0Vdc, f=1MHz		0.8		pF

UDD32C15L01 (Marking: EC)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Reverse stand-off voltage	V <sub>RWM</sub>				15	V
Reverse breakdown voltage	V <sub>BR</sub>	I <sub>BR</sub> =1mA	16.7			V
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =15V			1	μA
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =1A			24	V
Clamping voltage (tp=8/20μs)	V <sub>C</sub>	I <sub>pp</sub> =5A			35	V
Off state junction capacitance	C <sub>J</sub>	0Vdc, f=1MHz		0.8		pF



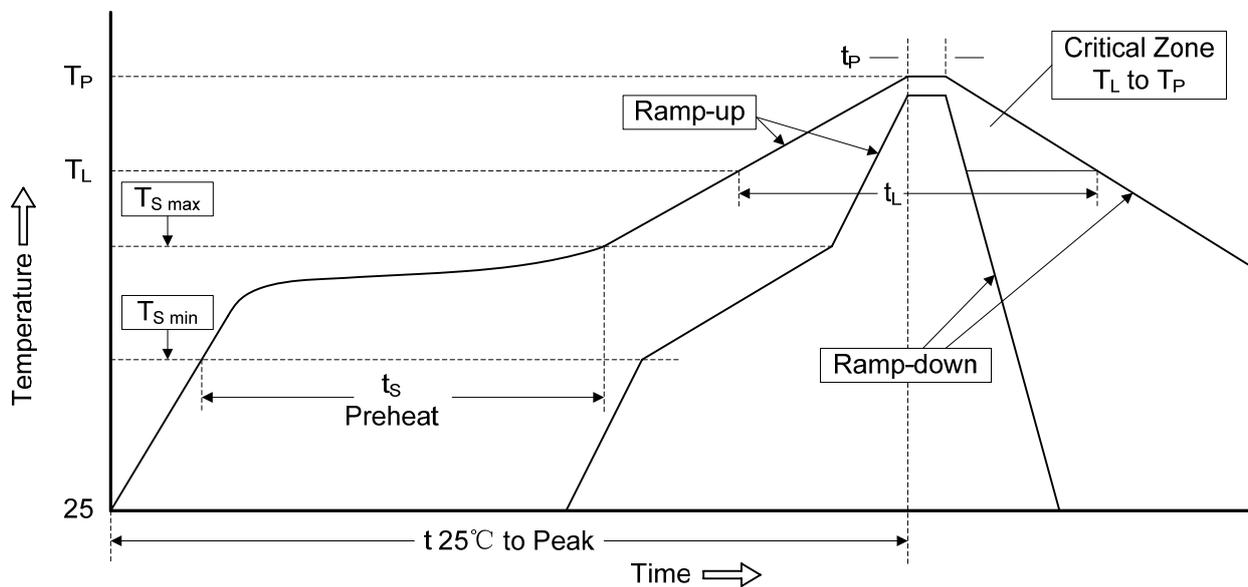
## 7. Typical characteristic

<p>Peak Power Dissipation (%)</p> <p><math>T_A</math>, Ambient Temperature (<math>^{\circ}C</math>)</p>	<p>% of Peak Power Current</p> <p>t, Time (<math>\mu s</math>)</p> <p>Peak value <math>I_{pp}</math></p> <p>Waveform Parameters:  <math>t_r = 8\mu s</math>  <math>t_f = 20\mu s</math></p> <p><math>t_d = t_r/2</math></p>
<p>Figure 1: Power Derating Curve</p>	<p>Figure 2: Pulse Waveform</p>
<p><math>P_{PP}</math>, Peak Pulse Power (W)</p> <p><math>t_p</math>, Pulse Duration (<math>\mu s</math>)</p>	<p>Tek Run: 1.00GS/s [.....T.....]</p> <p>Sample trigger</p> <p>CH1 5V</p> <p>M 50.0ns</p>
<p>Figure 3: Non-Repetitive Peak Pulse vs Pulse Time</p>	<p>Figure 4: ESD Clamping (8kV Contact IEC61000-4-2)</p>



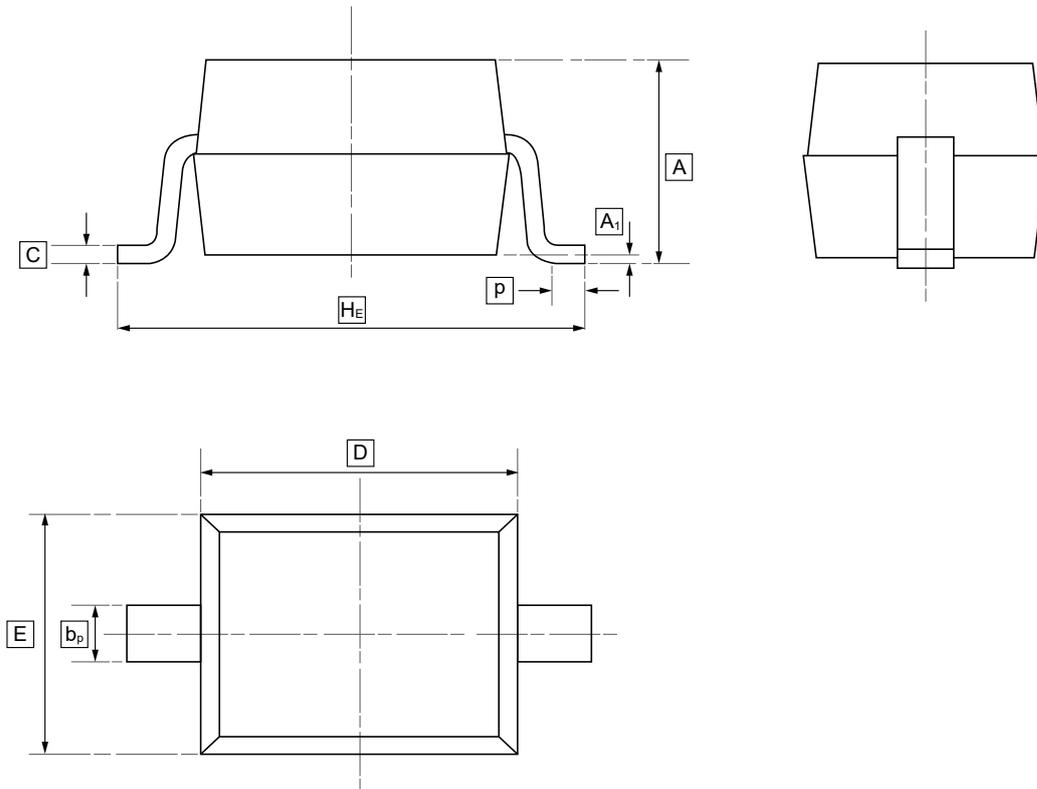
## 8.Recommended Soldering Conditions

Profile Feature		Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )		3°C/second max.
Preheat	-Temperature Min ( $T_{s(min)}$ )	150°C
	-Temperature Max ( $T_{s(max)}$ )	200°C
	-Time (Min to Max) ( $t_s$ )	60-180 secs.
$T_{s(max)}$ to $T_L$ - Ramp-up Rate		3°C/sec. Max
Time maintained above	-Temperature ( $T_L$ )	217°C
	-Time ( $T_L$ )	60-150 secs.
Peak Temperature ( $T_p$ )		260°C
Time within 5°C of actual Peak Temperature ( $T_p$ )		20-40 seconds
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temperature		8 min. Max





## 9.SOD-323 Package Outline Dimensions

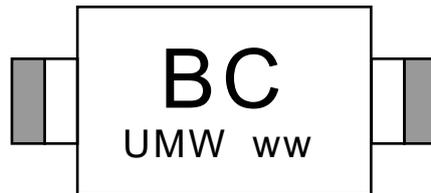


### DIMENSIONS (mm are the original dimensions)

Symbol	A	bp	C	D	E	HE	A <sub>1</sub>	p
Min	0.90	0.25	0.10	1.60	1.15	2.30	0.01	0.20
Max	1.20	0.40	0.15	1.80	1.35	2.80	0.10	0.50



## 10. Ordering information



ww: Batch Code

Order Code	Marking	Package	Base QTY	Delivery Mode
UMW UDD32C03L01	AC	SOD-323	3000	Tape and reel
UMW UDD32C05L01	BC	SOD-323	3000	Tape and reel
UMW UDD32C12L01	DC	SOD-323	3000	Tape and reel
UMW UDD32C15L01	EC	SOD-323	3000	Tape and reel



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