

# TDS:EMIC

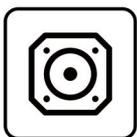
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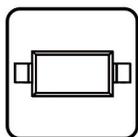
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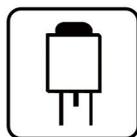
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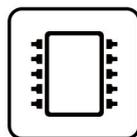
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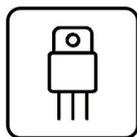
二三極管



LDO穩壓器



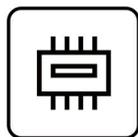
觸摸芯片



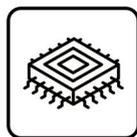
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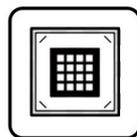
運算放大器



存儲芯片



MCU



串口通信

# 74LVC2G07DBVR-TD

產品規格說明書

## 74LVC2G07 Dual Buffer and Driver With Open-Drain Outputs

### 1. General Description

#### 1.1 Description

This dual buffer and driver is designed for 1.65-V to 5.5-V VCC operation.

The output of the 74LVC2G07 device is open drain and can be connected to other open-drain outputs to implement active-low wired-OR or active-high wired-AND functions. The maximum sink current is 32 mA.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

#### 1.2 Features

- Dual Open-Drain Buffer Configuration
- Support Translation-Up and Down
- Low Power Consumption, 10- $\mu$ A Max ICC

- Supports 5-V VCC Operation
- Inputs and Open-Drain Outputs Accept Voltages Up to 5.5 V
- $\pm 24$ -mA Output Drive at 3.3 V
- $I_{off}$  Supports Live Insertion, Partial-Power-Down Mode, and Back-Drive Protection

#### 1.3 Device Information

PART NUMBER	PACKAGE
74LVC2G07	SOT23-6(LT)
	SC70-6(CT)
	DSBGA(AG)
	DFN(BD)
	DFN(CD)

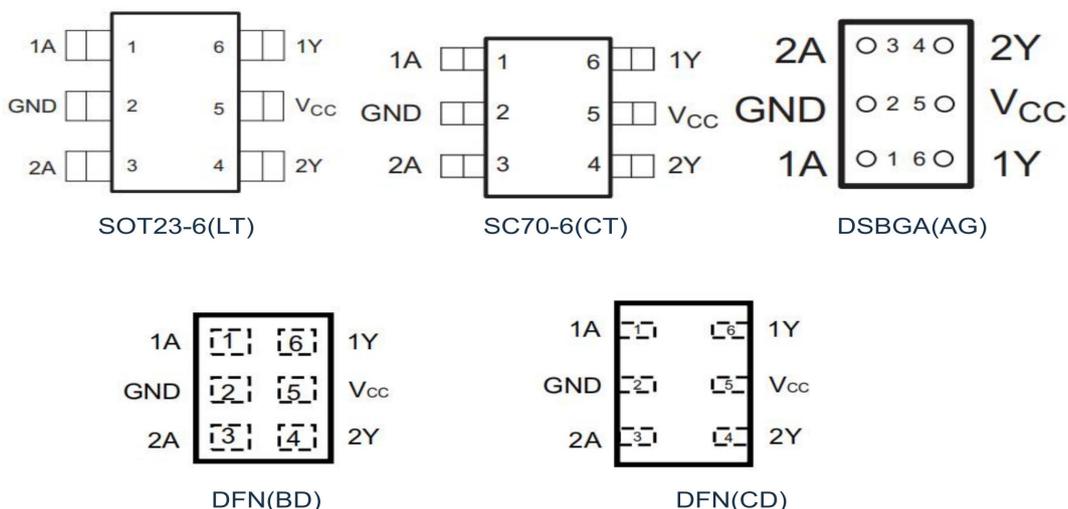


Figure 2.1 Pin Configuration

PIN		I/O	FUNCTION
NAME	No.		
1A	1	I	Gate 1 logic signal
1Y	6	O	Gate 1 Output signal
2A	3	I	Gate 2 logic signal
2Y	4	O	Gate 2 Output signal
GND	2	-	Ground
VCC	5	-	Supply Voltage

## 3. System Diagram

### 3.1 Logic Diagram

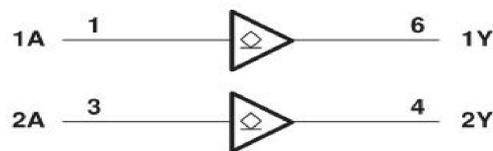


Figure 3.1: 74LVC2G07 Logic Diagram

### 3.2 Function Table

Input	Output
A	Y
1	Z
0	0

X = don't care, 1=High State, 0=Low State,Z=High Impedance

## 4. Specifications

### 4.1 Absolute Maximum Ratings

Symbol	Parameter	MIN	MAX	Unit
V <sub>CC</sub>	Supply Voltage	-0.5	6.5	V
V <sub>I</sub>	Input Voltage Range	-0.5	6.5	V
V <sub>O</sub>	Voltage Range(applied to any output in the high-impedance or power-off state) <sup>(1)</sup>	-0.5	6.5	V
	Voltage Range(applied to any output in the high or low state)	-0.5	V <sub>CC</sub> + 0.5	V
I <sub>O</sub>	Continuous Output Current		±50	mA
T <sub>J</sub>	Junction Temperature		125	°C
T <sub>OP</sub>	Operating Temperature	-40	85	°C

Absolute maximum ratings are those values beyond which the device could be permanently damaged. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under normal operating conditions.

(1) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

### 4.2 Electrical Characteristics

#### 4.2.1 DC Specifications

(T<sub>a</sub>=25°C, voltages are referenced to GND (ground=0V), unless otherwise specified)

Symbol	Parameter	Test Condition	MIN	TYP	MAX	Unit
V <sub>IH</sub>	High Level Input Voltage	V <sub>CC</sub> =1.65V to 1.95V	0.65V <sub>CC</sub>	--	--	V
		V <sub>CC</sub> =2.3V to 2.7V	1.7	--	--	V
		V <sub>CC</sub> =3V to 5.5V	0.7V <sub>CC</sub>	--	--	V
V <sub>IL</sub>	Low Level Input Voltage	V <sub>CC</sub> =1.65V to 1.95V	--	--	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> =2.3V to 2.7V	--	--	0.7	V
		V <sub>CC</sub> =3V to 3.6V	--	--	0.8	V
		V <sub>CC</sub> =4.5V to 5.5V	--	--	0.3V <sub>CC</sub>	V
I <sub>OL</sub>	Low Level Output Current	V <sub>CC</sub> =1.65V	--	--	4	mA
		V <sub>CC</sub> =2.3V	--	--	8	mA
		V <sub>CC</sub> =3V	--	--	16	mA
		V <sub>CC</sub> =4.5V	--	--	24	mA
			--	--	32	mA

Symbol	Parameter	Test Condition	MIN	TYP	MAX	Unit
V <sub>OL</sub>	Low Level Output Voltage	V <sub>CC</sub> =1.65V to 5.5V, I <sub>OH</sub> =100uA	--	--	0.1	V
		V <sub>CC</sub> =1.65V, I <sub>O</sub> =4mA	--	0.09	--	V
		V <sub>CC</sub> =2.3V, I <sub>O</sub> =8mA	--	0.1	--	V
		V <sub>CC</sub> =3V, I <sub>O</sub> =16mA	--	0.15	--	V
		V <sub>CC</sub> =3V, I <sub>O</sub> =24mA	--	0.25	--	V
		V <sub>CC</sub> =4.5V, I <sub>O</sub> =32mA	--	0.25	--	V
I <sub>I</sub>	Inputs Leakage Current	V <sub>CC</sub> =0 to 5.5V, V <sub>I</sub> =V <sub>CC</sub> or GND	--	0	±1	uA
I <sub>off</sub>	Power Off Leakage Current	V <sub>CC</sub> =0V, V <sub>I</sub> or V <sub>O</sub> =5.5V	--	0	±10	uA
I <sub>oz</sub>	3-State Output OFF-State Current	V <sub>CC</sub> =3.6V, V <sub>O</sub> = 0 to 5.5V	--	0	10	uA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>CC</sub> =1.65V to 5.5V, V <sub>I</sub> =V <sub>CC</sub> or GND, I <sub>O</sub> =0	--	0	10	uA
ΔI <sub>CC</sub>	Additional Quiescent Supply Current Per Input Pin	V <sub>CC</sub> =3V to 5.5V, one input at V <sub>CC</sub> -0.6V, Other inputs at V <sub>CC</sub> or GND	--	--	500	uA

## 5. Application Information

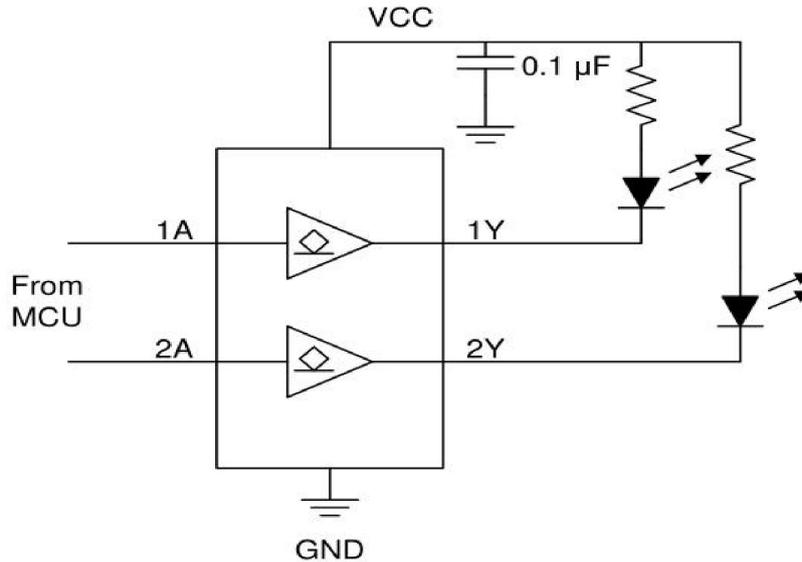


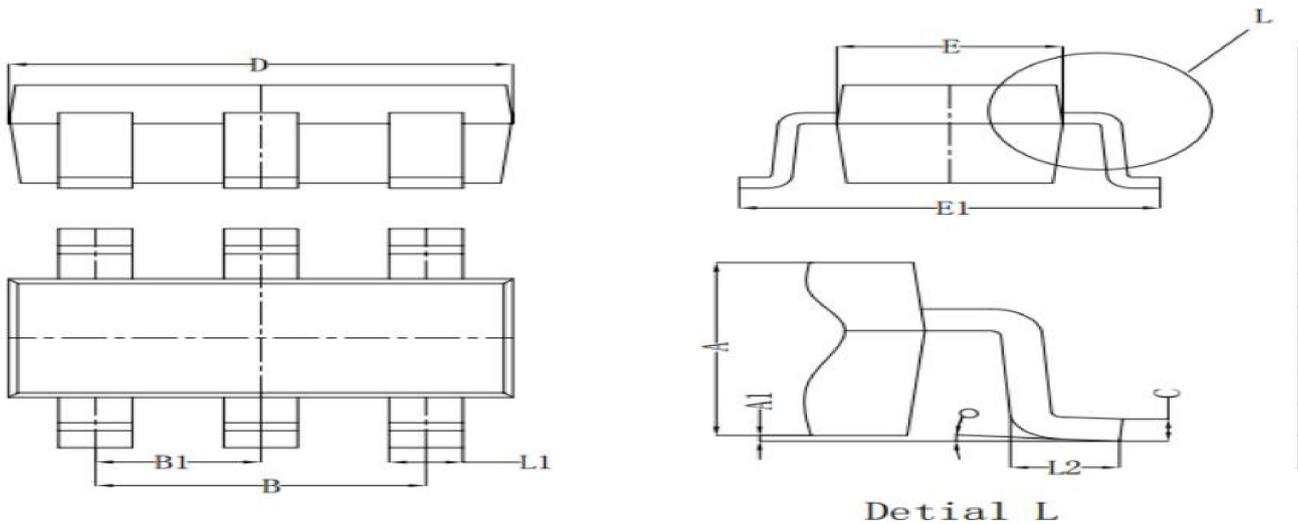
Figure 5.1: Application Schematic

## 6. Ordering Information

Orderable Device	Package Type	Pins	Packing	Package Qty
74LVC2G07LT06ARCQ	SOT23	6	Tape & Reel	3000
74LVC2G07CT06ARCQ	SC70	6	Tape & Reel	3000
74LVC2G07AG06ARCQ	DSBGA	6	Tape & Reel	3000
74LVC2G07BD06AREQ	DFN(BD)	6	Tape & Reel	5000
74LVC2G07CD06AREQ	DFN(CD)	6	Tape & Reel	5000

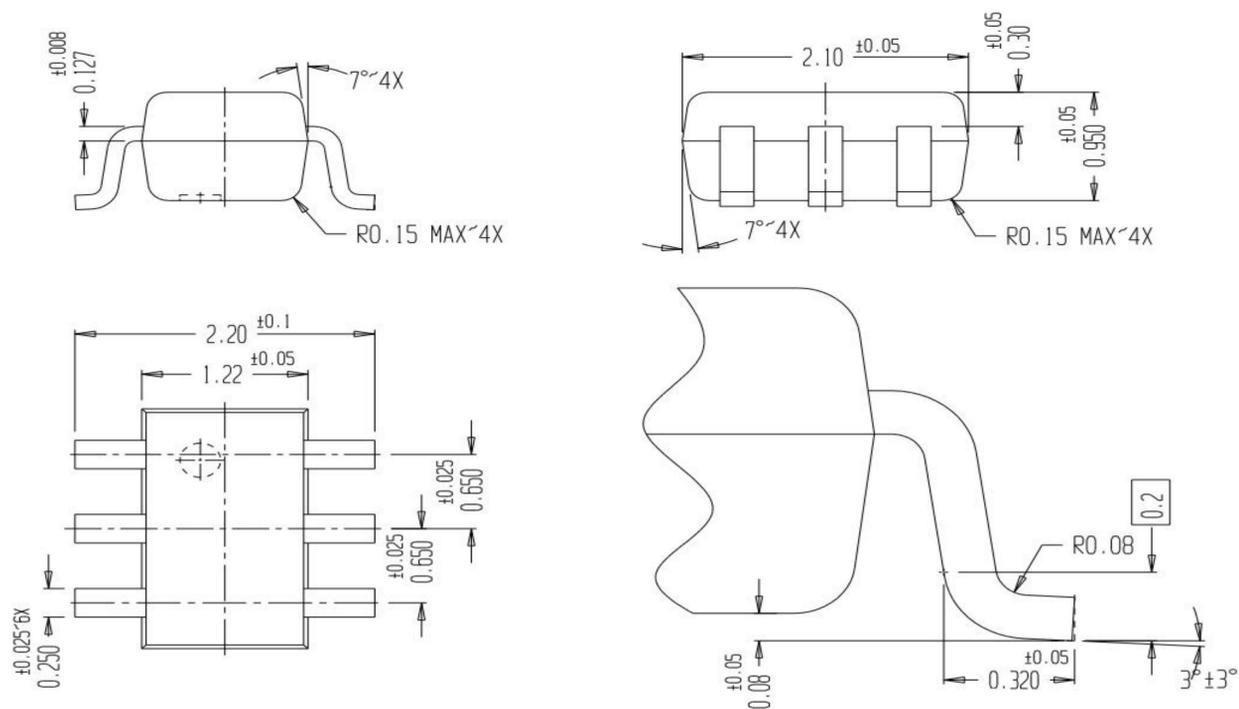
## 7. Package Information

### 7.1 SOT23-6

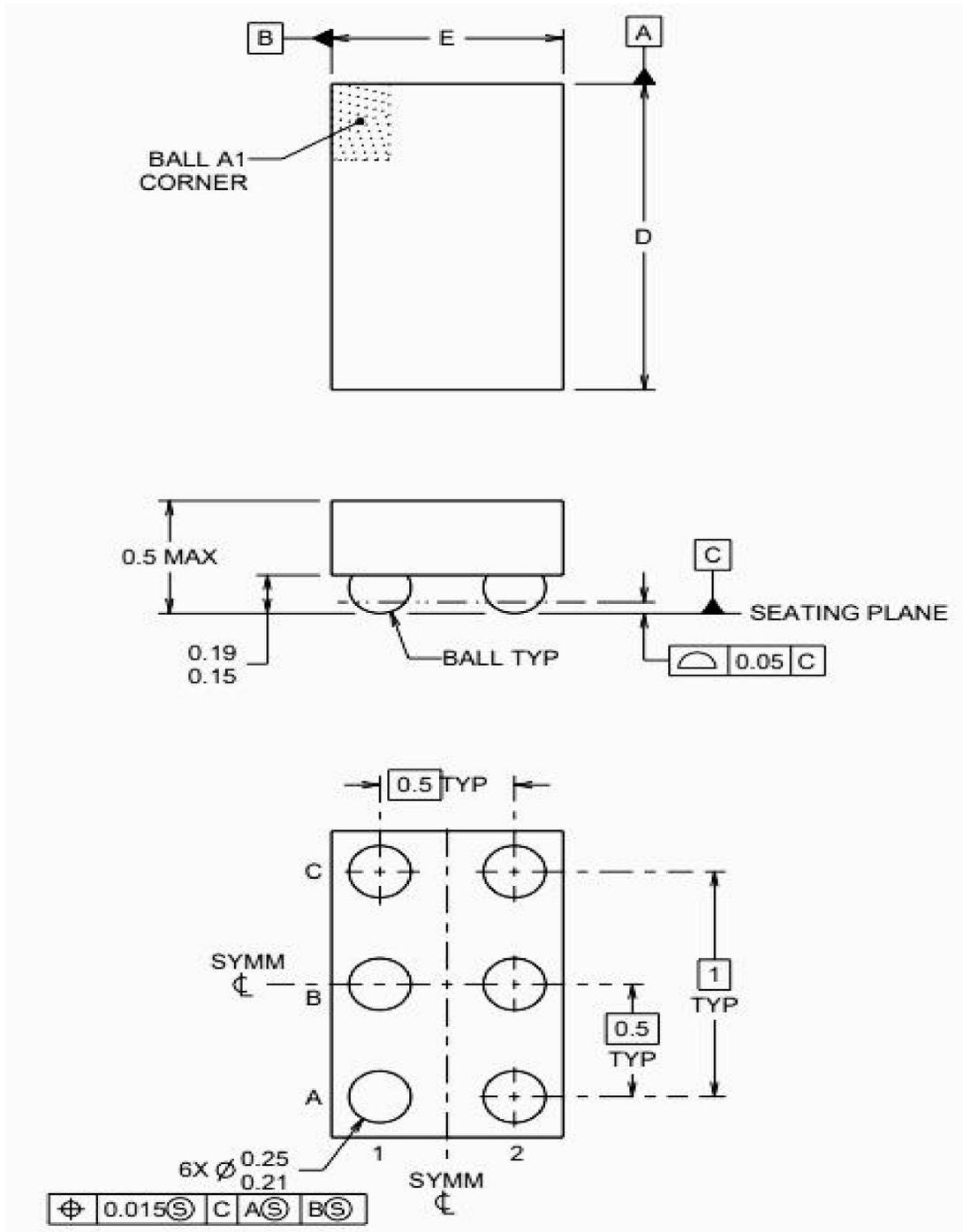


Symbol	Dim in mm		
	Min	Nor	Max
A	1.050	1.100	1.150
A1	0.000	0.050	0.100
L1	0.300	0.400	0.500
C	0.100	0.150	0.200
D	2.820	2.920	3.020
E	1.500	1.600	1.700
E1	2.650	2.800	2.950
B	1.800	1.900	2.000
B1	0.950 TYP		
L2	0.300	0.450	0.600
φ	0°	4°	8°

## 7.2 SC70-6



## 7.3 DSBGA(AG)-6





## 7.5 DFN(CD)-6

