

General Description

The HSCB2004A is the high performance complementary N-ch and P-ch MOSFETs with high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications. The HSCB2004A meet the RoHS and Green Product requirement with full function reliability approved.

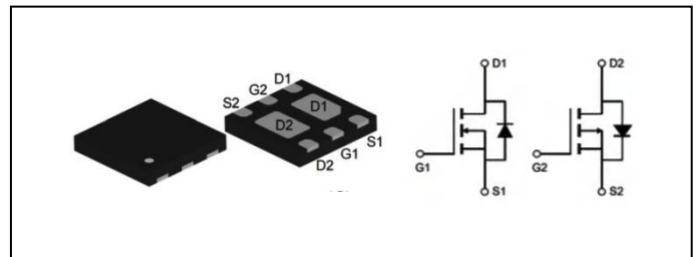
Features

- Green Device Available
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Advanced high cell density Trench Technology

Product Summary

BVDSS	RDSON	ID
20V	16mΩ	5A
-20V	27mΩ	-5A

DFN2*2-6L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
		Steady State	Steady State	
V _{DS}	Drain-Source Voltage	20	-20	V
V _{GS}	Gate-Source Voltage	±12	±12	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	5	-5	A
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ -4.5V ¹	3.2	-3.2	A
I _{DM}	Pulsed Drain Current ²	20	-20	A
P _D @T _A =25°C	Total Power Dissipation ³	1.6	1.6	W
T _{STG}	Storage Temperature Range	-55 to 150	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	60	°C/W

N-Ch and P-Ch Fast Switching MOSFETs
N-Channel Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V, I_D=3A$	---	16	22	m Ω
		$V_{GS}=2.5V, I_D=2A$	---	22	28	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	0.4	0.7	1	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{DS}=16V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	9	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=10V, V_{GS}=4.5V, I_D=3A$	---	7.6	---	nC
Q_{gs}	Gate-Source Charge		---	0.7	---	
Q_{gd}	Gate-Drain Charge		---	2.5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=10V, V_{GS}=4.5V, R_G=3.3\Omega, I_D=3A$	---	10	---	ns
T_r	Rise Time		---	12	---	
$T_{d(off)}$	Turn-Off Delay Time		---	34	---	
T_f	Fall Time		---	30	---	
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1\text{MHz}$	---	557	---	pF
C_{oss}	Output Capacitance		---	69	---	
C_{rss}	Reverse Transfer Capacitance		---	55	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	---	---	4	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150 $^\circ\text{C}$ junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

N-Channel Typical Characteristics

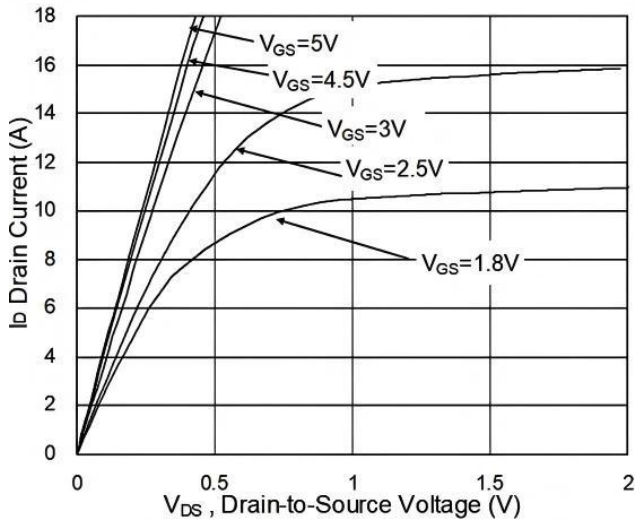


Fig.1 Typical Output Characteristics

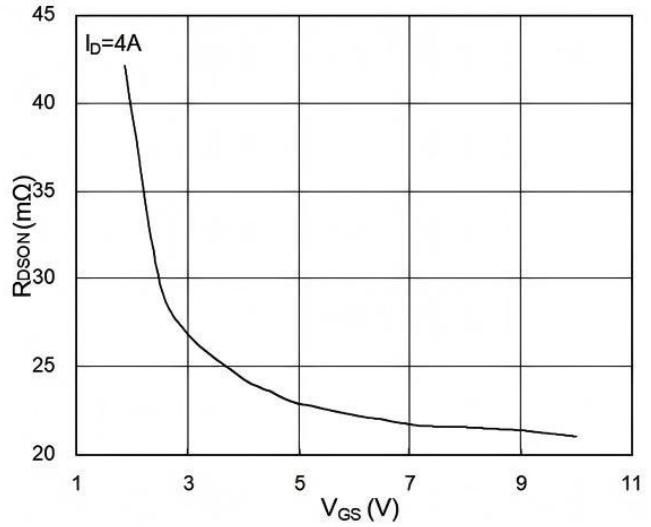


Fig.2 On-Resistance vs G-S Voltage

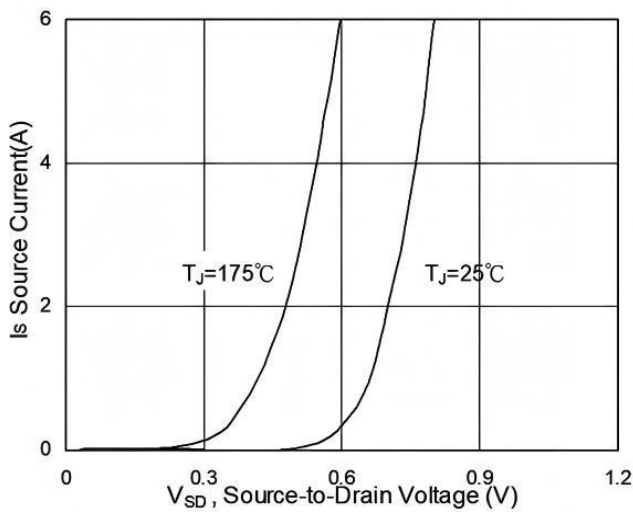


Fig.3 Source Drain Forward Characteristics

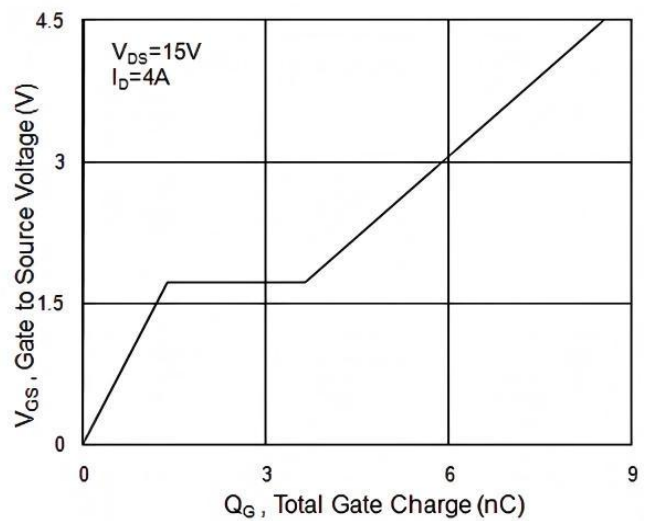


Fig.4 Gate-Charge Characteristics

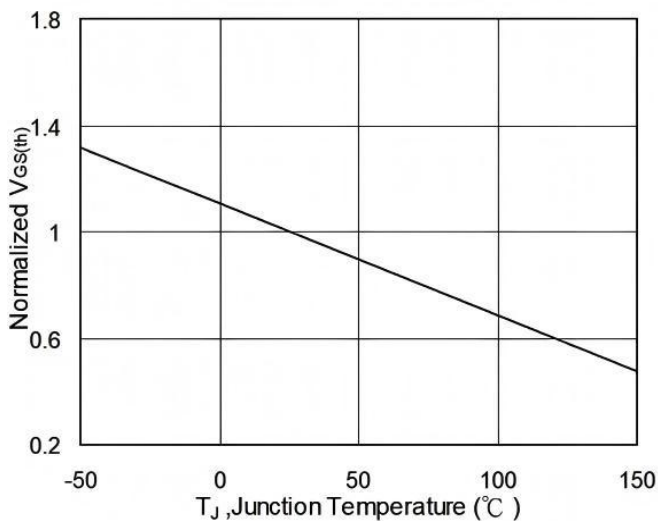


Fig.5 Normalized $V_{GS(th)}$ vs T_J

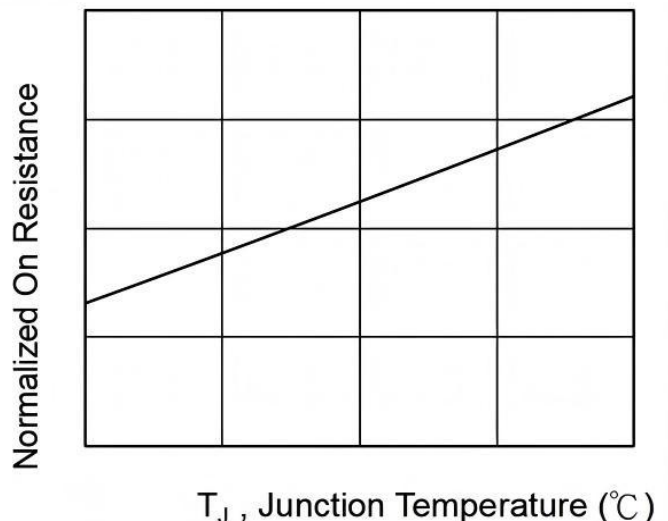


Fig.6 Normalized $R_{DS(on)}$ vs T_J

N-Ch and P-Ch Fast Switching MOSFETs

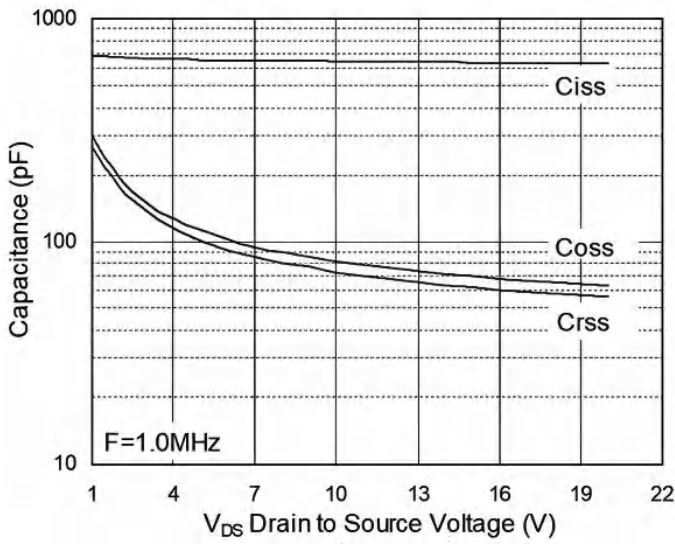


Fig.7 Capacitance

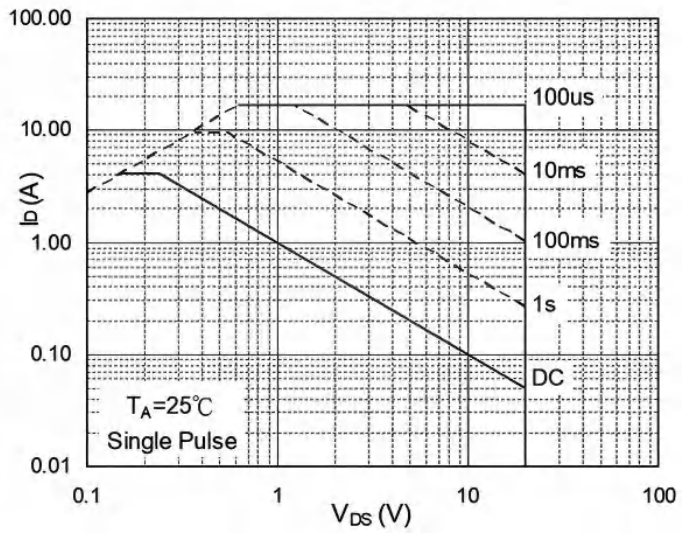


Fig.8 Safe Operating Area

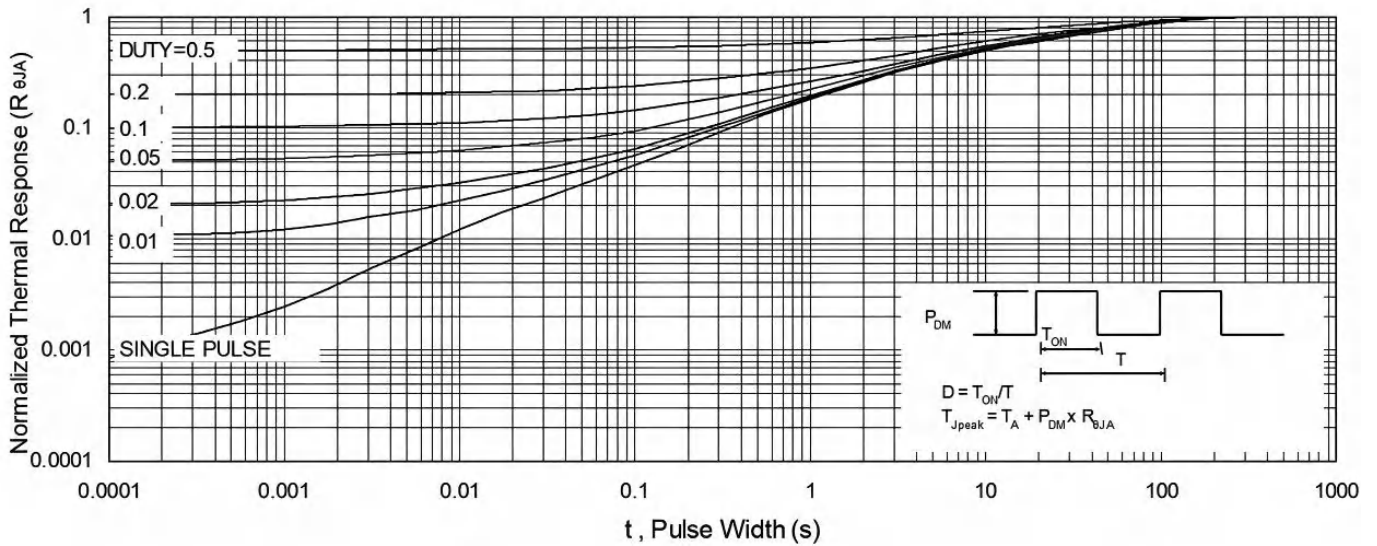


Fig.9 Normalized Maximum Transient Thermal Impedance

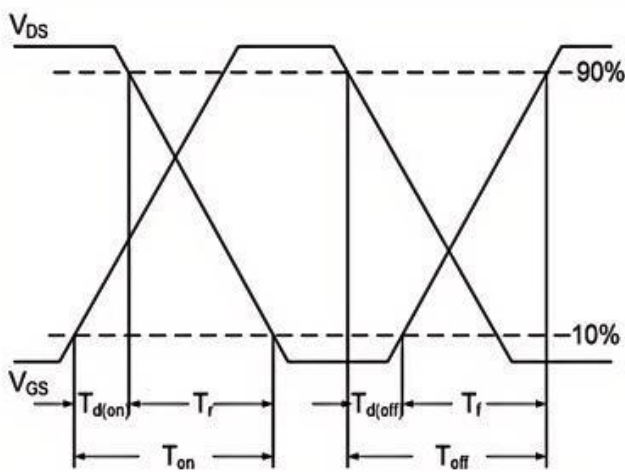


Fig.10 Switching Time Waveform

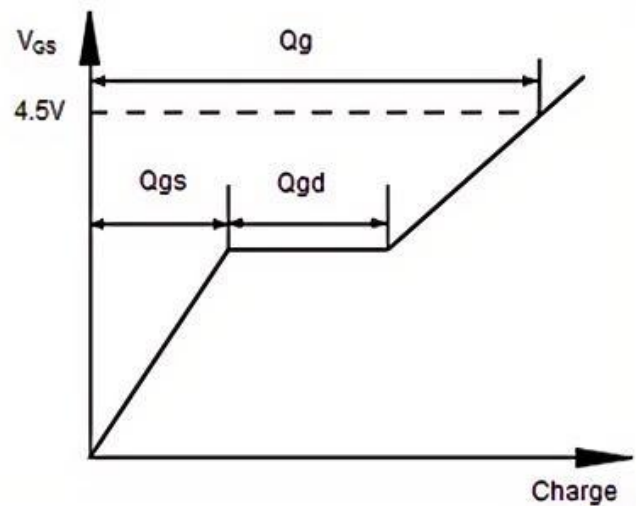


Fig.11 Gate Charge Waveform

P-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-20	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V , I _D =-4A	---	27	35	mΩ
		V _{GS} =-2.5V , I _D =-3A	---	35	45	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.4	-0.7	-1	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-16V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-16V , V _{GS} =0V , T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 12V , V _{DS} =0V	---	---	± 100	nA
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	7	---	Ω
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-10V , V _{GS} =-4.5V , I _D =-3A	---	8.8	---	nC
Q _{gs}	Gate-Source Charge		---	1.1	---	
Q _{gd}	Gate-Drain Charge		---	2.6	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-10V , V _{GS} =-4.5V , R _G =3.3Ω I _D =-3A	---	16	---	ns
T _r	Rise Time		---	32	---	
T _{d(off)}	Turn-Off Delay Time		---	46	---	
T _f	Fall Time		---	42	---	
C _{iss}	Input Capacitance	V _{DS} =-10V , V _{GS} =0V , f=1MHz	---	877	---	pF
C _{oss}	Output Capacitance		---	92	---	
C _{rss}	Reverse Transfer Capacitance		---	83	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V , Force Current	---	---	-4	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C	---	---	-1	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

P-Channel Typical Characteristics

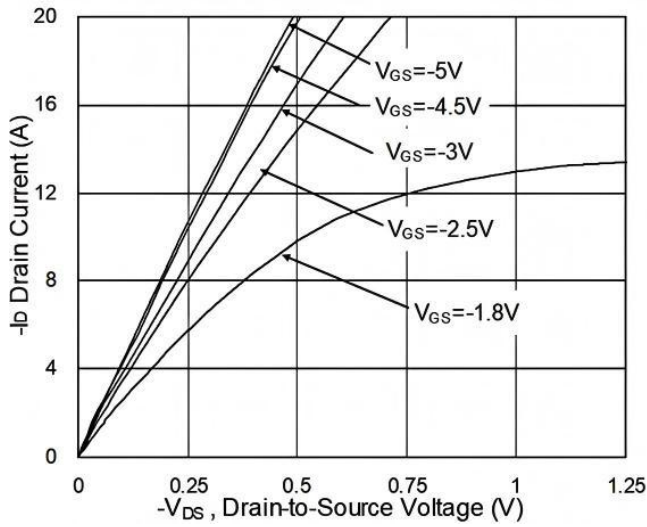


Fig.1 Typical Output Characteristics

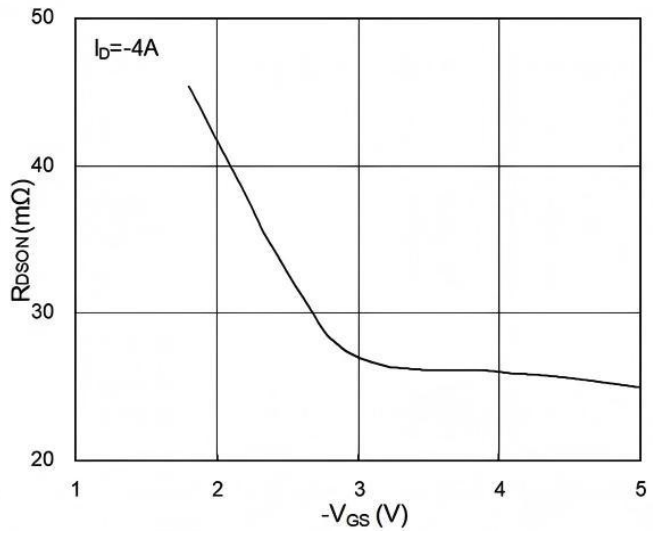


Fig.2 On-Resistance vs G-S Voltage

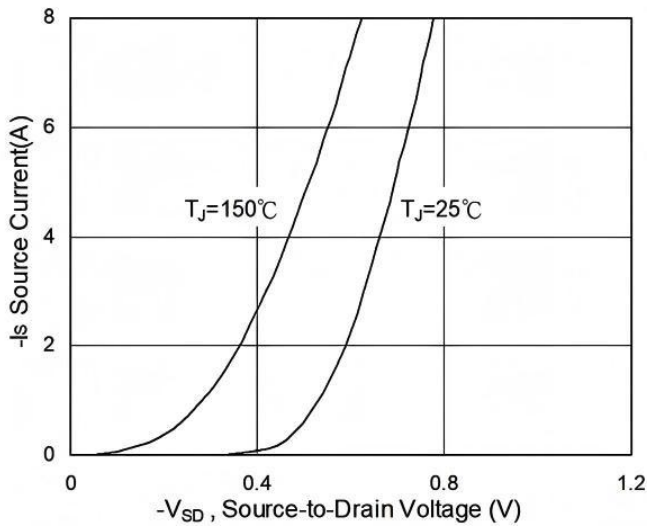


Fig.3 Source Drain Forward Characteristics

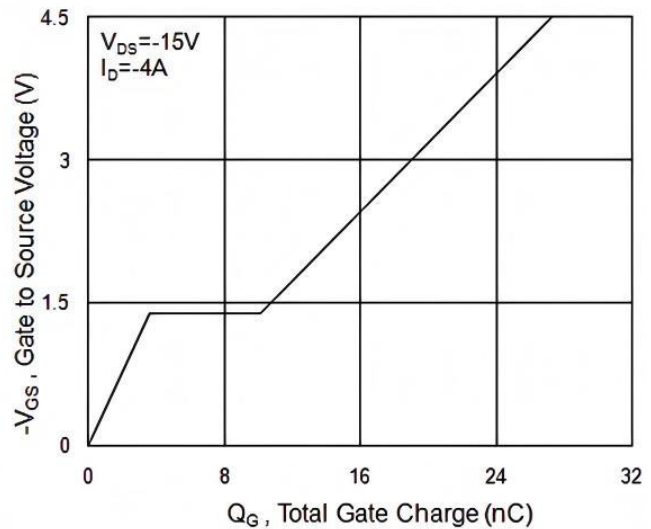


Fig.4 Gate-Charge Characteristics

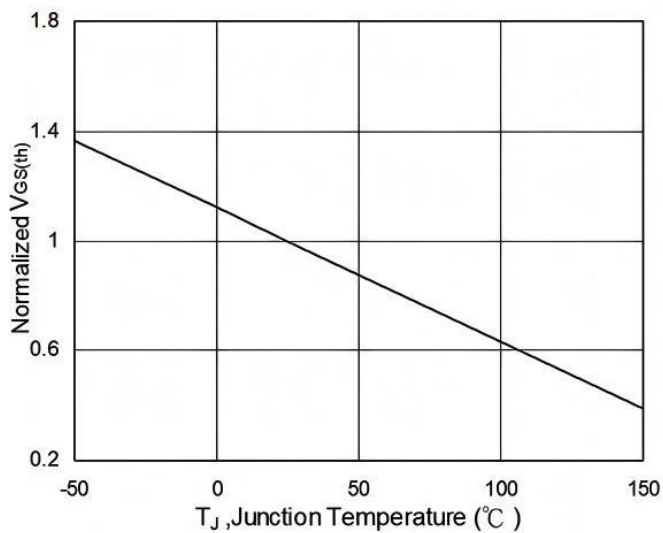


Fig.5 Normalized $V_{GS(th)}$ vs T_J

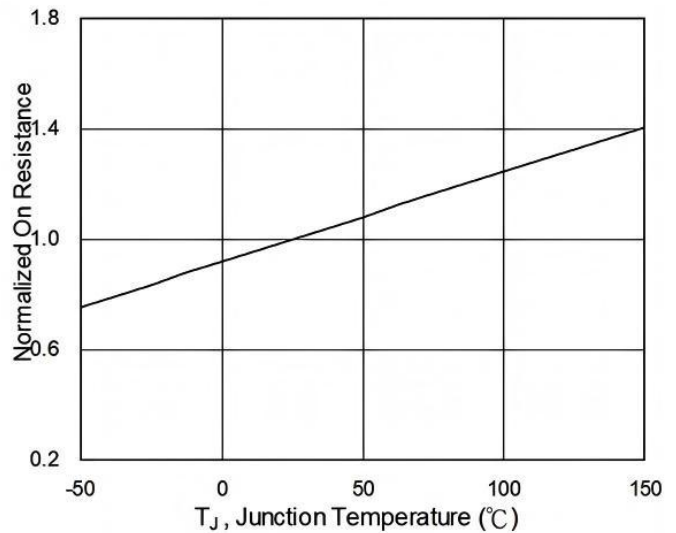


Fig.6 Normalized $R_{DS(on)}$ vs T_J

N-Ch and P-Ch Fast Switching MOSFETs

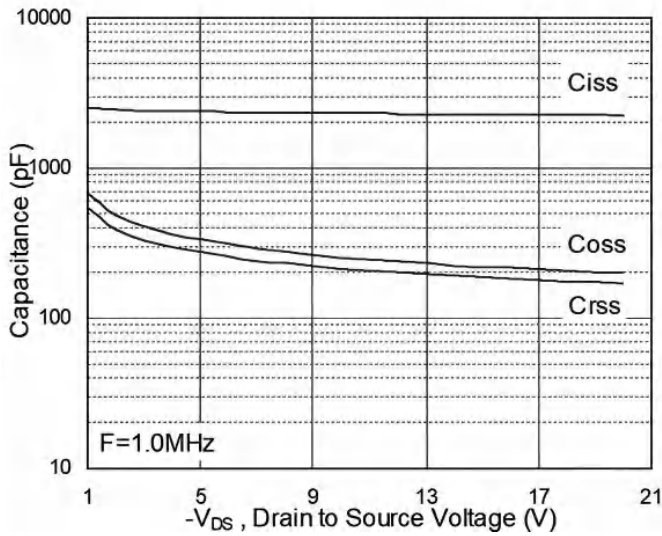


Fig.7 Capacitance

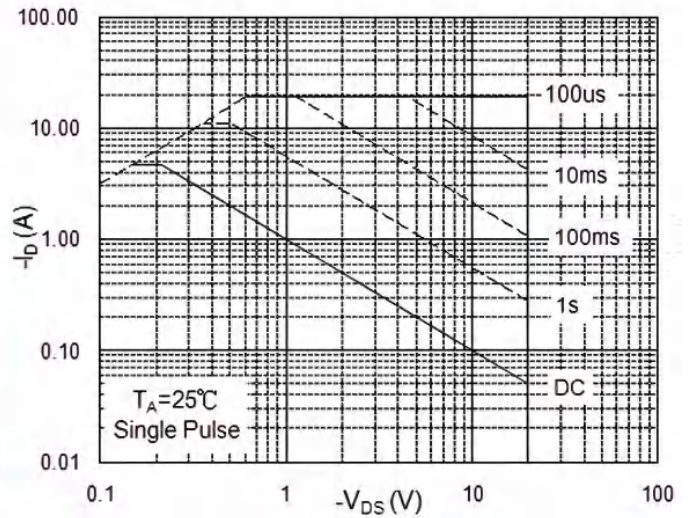


Fig.8 Safe Operating Area

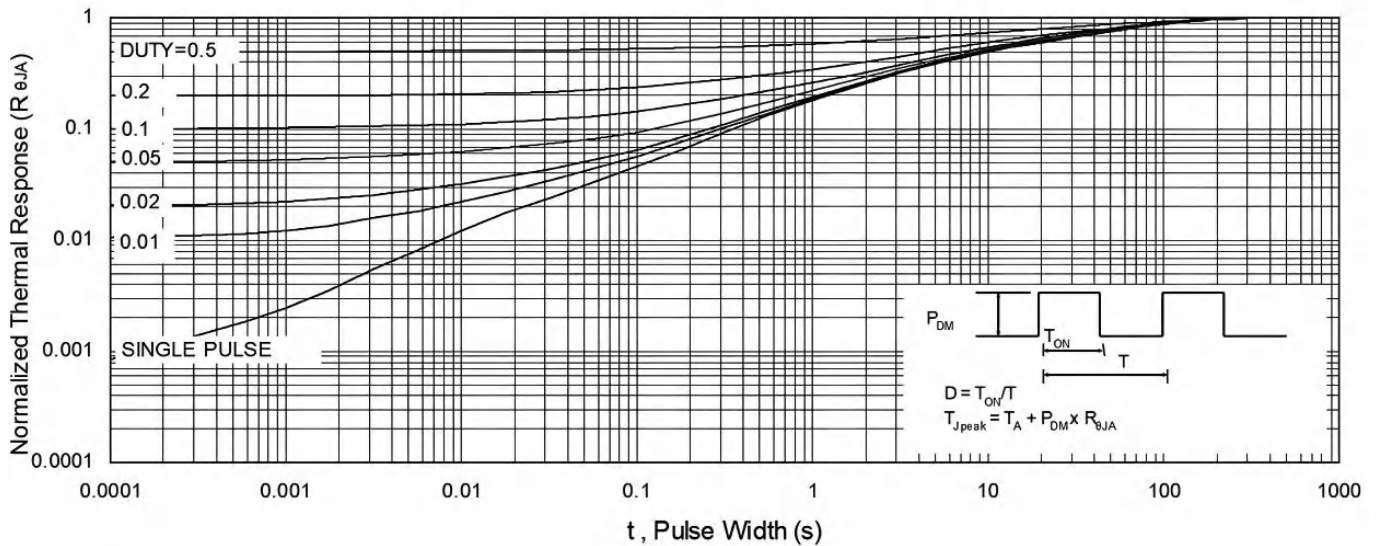


Fig.9 Normalized Maximum Transient Thermal Impedance

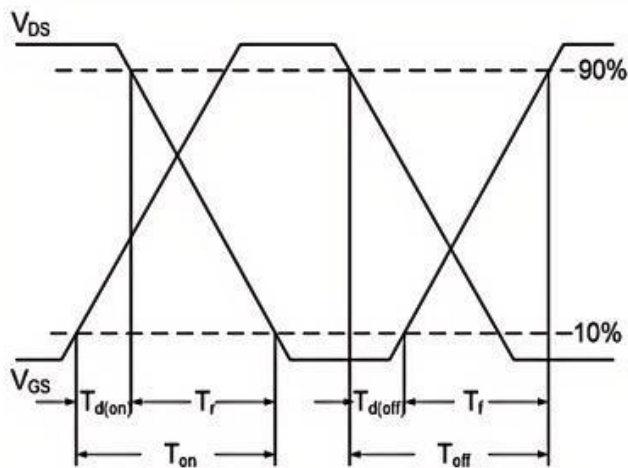


Fig.10 Switching Time Waveform

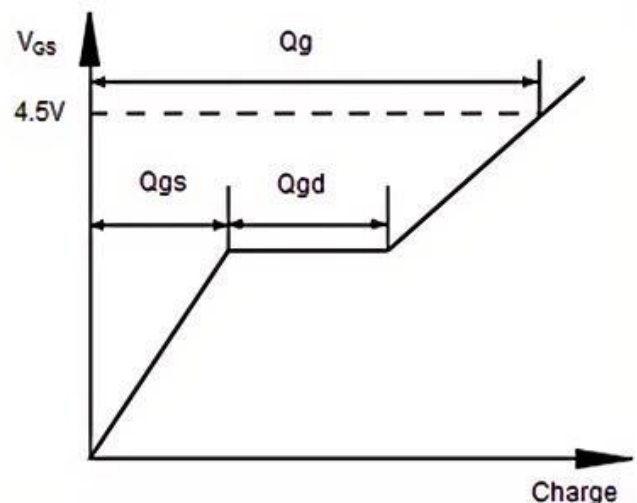
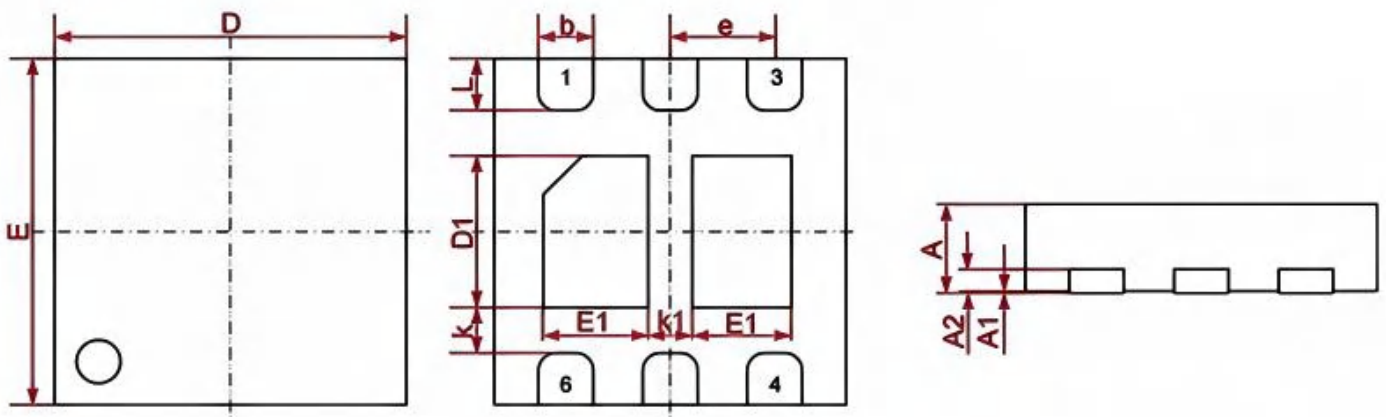


Fig.11 Gate Charge Waveform

Ordering Information

Part Number	Package code	Packaging
HSCB2004A	DFN2*2-6L-D	3000/Tape&Reel

DFN2*2-6L-D Dual Package Outline



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203 REF.		0.008 REF.	
b	0.250	0.350	0.010	0.014
D	1.900	2.100	0.075	0.083
D1	0.900	1.100	0.035	0.043
E	1.900	2.100	0.075	0.083
E1	0.520	0.720	0.020	0.028
e	0.650 TYP.		0.026 TYP.	
k	0.200 MIN.		0.008 MIN.	
K1	0.320 REF.		0.013 REF.	
L	0.200	0.300	0.008	0.012

HSCB2004A Marking:

