

Features

- Advanced SGT MOS Technology
- Low Gate Charge
- Low $R_{DS(ON)}$
- 100% EAS Guaranteed
- Green Device Available

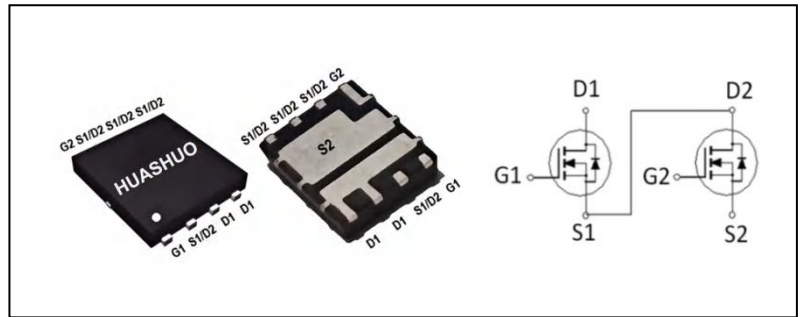
Product Summary

BVDSS	$R_{DS(ON)}$, typ	ID
30V	2.7m Ω	78A
30V	0.8m Ω	190A

Applications

- Power Management in Desktop Computer or DC/DC Converters.
- Isolated DC/DC Converters in Telecom and Industrial.

PRPAK5*6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		Die1	Die2	
V_{DS}	Drain-Source Voltage	30	30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	78	190	A
$I_D@T_C=70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	48	120	A
$I_D@T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	18	31	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	14	25	A
I_{DM}	Pulsed Drain Current ²	200	400	A
EAS	Single Pulse Avalanche Energy ³	34	130	mJ
I_{AS}	Avalanche Current	26	51	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	32	62	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ. D1	Typ. D2	Max. D1	Max. D2	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	---	75	75	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	---	4	2	$^\circ C/W$



Die1 N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	2.7	3.6	mΩ
		V _{GS} =4.5V, I _D =15A	---	4	5.5	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.4	---	Ω
Q _g	Total Gate Charge	V _{DS} =20V, V _{GS} =10V, I _D =20A	---	29	---	nC
Q _{gs}	Gate-Source Charge		---	4.6	---	
Q _{gd}	Gate-Drain Charge		---	6.8	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GS} =10V, R _G =1Ω I _D =1A	---	13	---	ns
T _r	Rise Time		---	10	---	
T _{d(off)}	Turn-Off Delay Time		---	23	---	
T _f	Fall Time		---	19	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	1526	---	pF
C _{oss}	Output Capacitance		---	739	---	
C _{rss}	Reverse Transfer Capacitance		---	120	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	78	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=51A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Die2 N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	0.8	1.2	mΩ
		V _{GS} =4.5V, I _D =15A	---	1.2	2.0	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.2	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.2	---	Ω
Q _g	Total Gate Charge	V _{DS} =20V, V _{GS} =10V, I _D =20A	---	82	---	nC
Q _{gs}	Gate-Source Charge		---	13.8	---	
Q _{gd}	Gate-Drain Charge		---	17.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =15V, V _{GS} =10V, R _G =1Ω I _D =1A	---	18.3	---	ns
T _r	Rise Time		---	15.2	---	
T _{d(off)}	Turn-Off Delay Time		---	37	---	
T _f	Fall Time		---	28	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz	---	4833	---	pF
C _{oss}	Output Capacitance		---	2353	---	
C _{rss}	Reverse Transfer Capacitance		---	288	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	100	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=-10V,L=0.1mH,I_{AS}=-40A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



N-Channel Typical Characteristics(Die1)

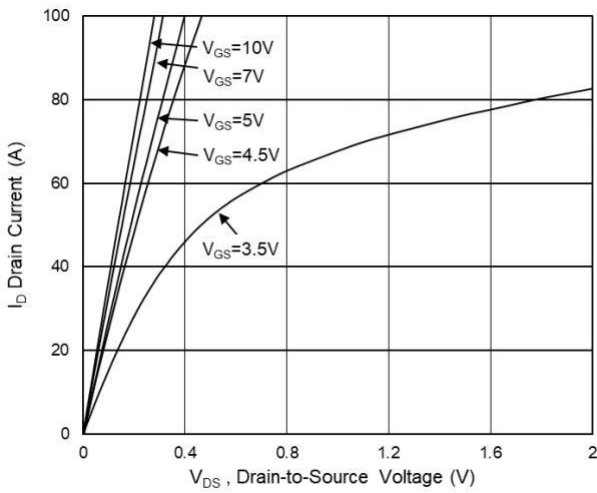


Fig.1 Typical Output Characteristics

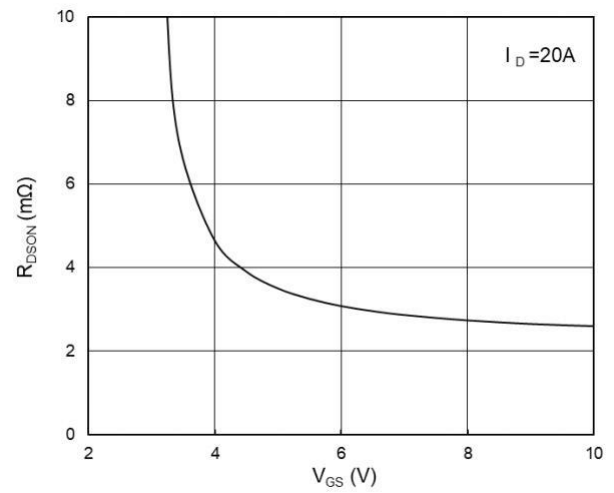


Fig.2 On-Resistance vs. G-S Voltage

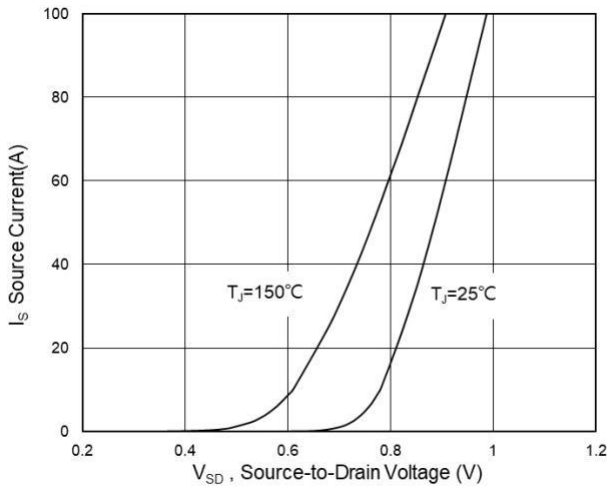


Fig.3 Forward Characteristics of Reverse

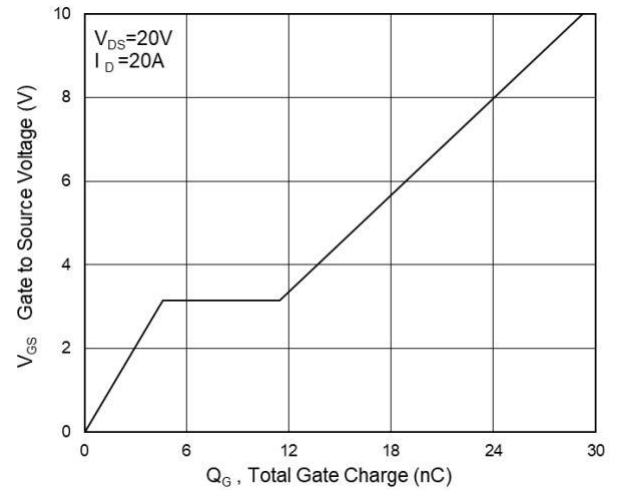


Fig.4 Gate-Charge Characteristics

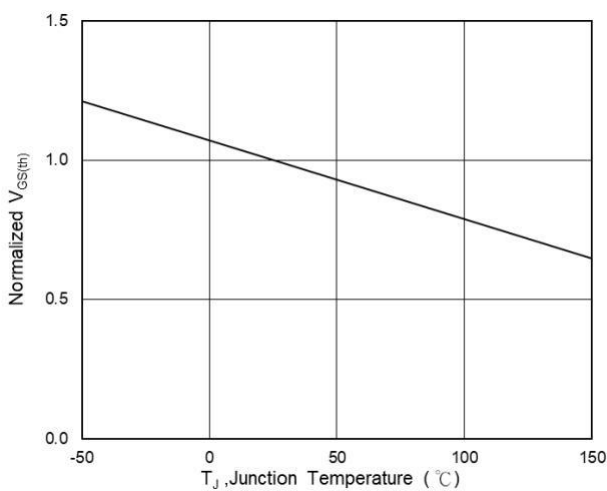


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

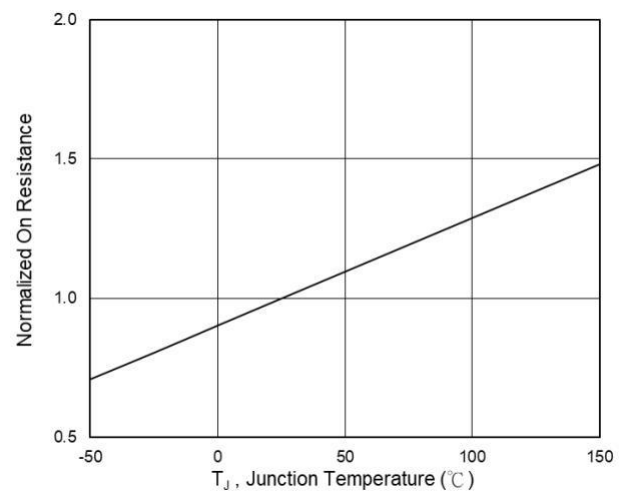


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

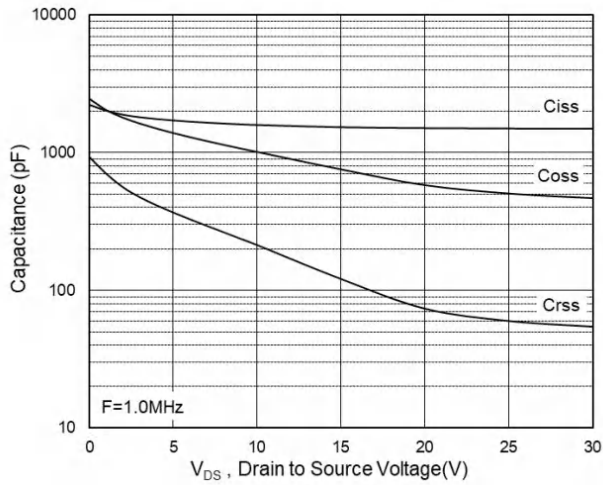


Fig.7 Capacitance

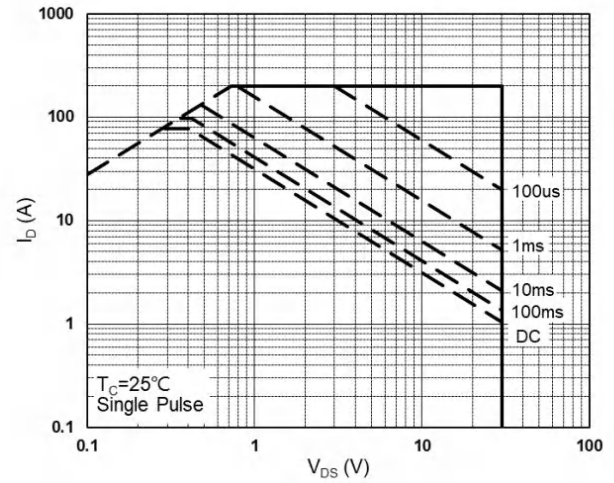


Fig.8 Safe Operating Area

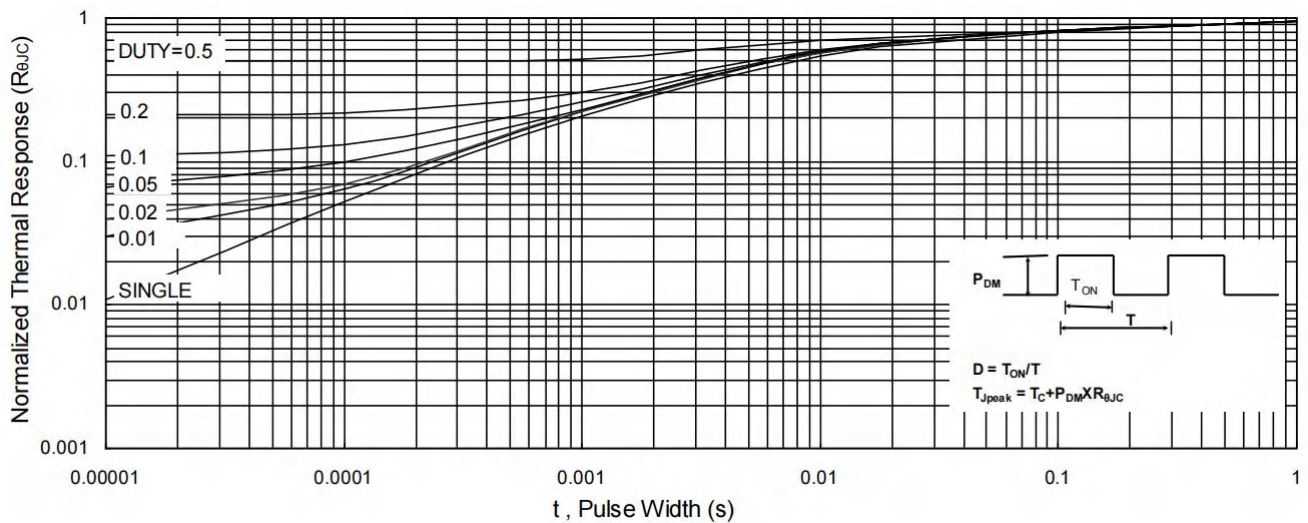


Fig.9 Normalized Maximum Transient Thermal Impedance

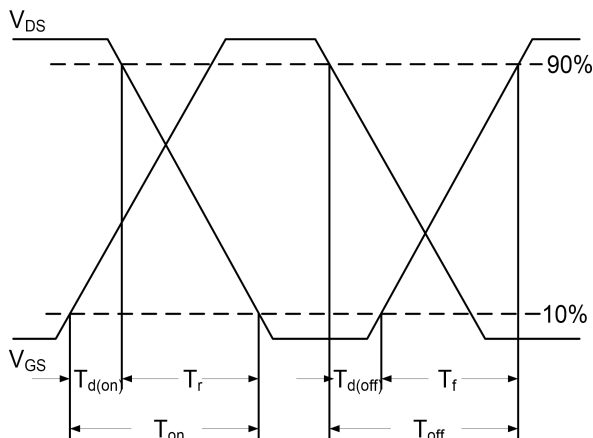


Fig.10 Switching Time Waveform

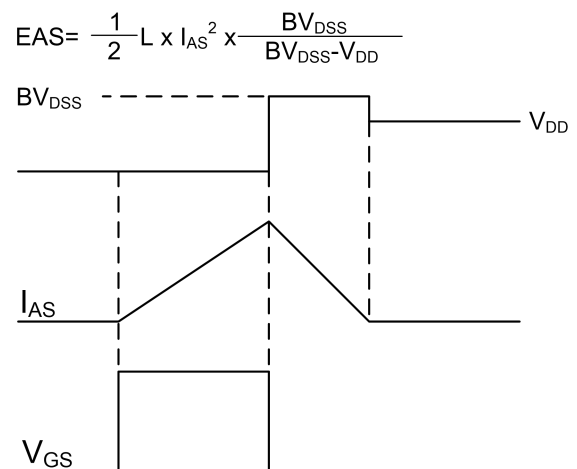


Fig.11 Unclamped Inductive Switching Wave



N-Channel Typical Characteristics(Die2)

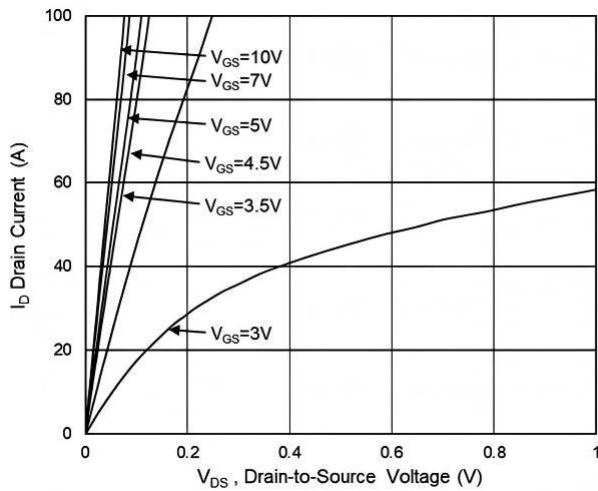


Fig.1 Typical Output Characteristics

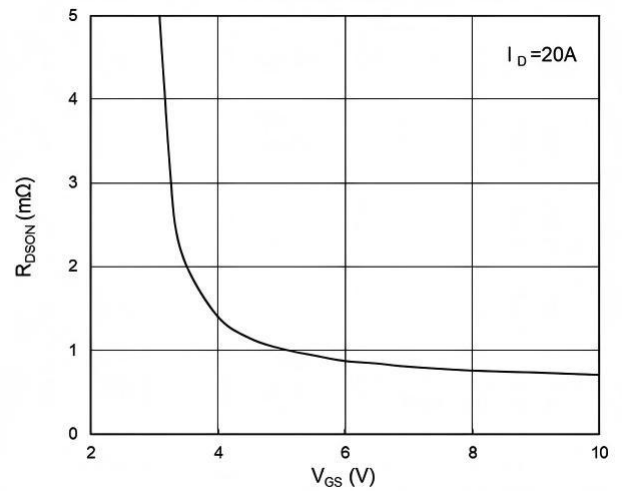


Fig.2 On-Resistance v.s Gate-Source

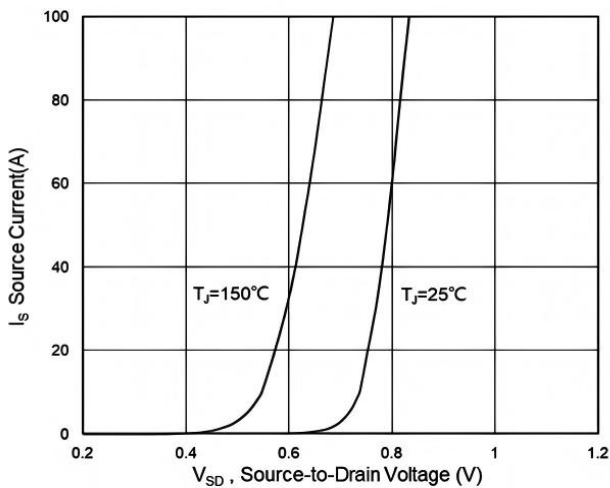


Fig.3 Forward Characteristics of Reverse

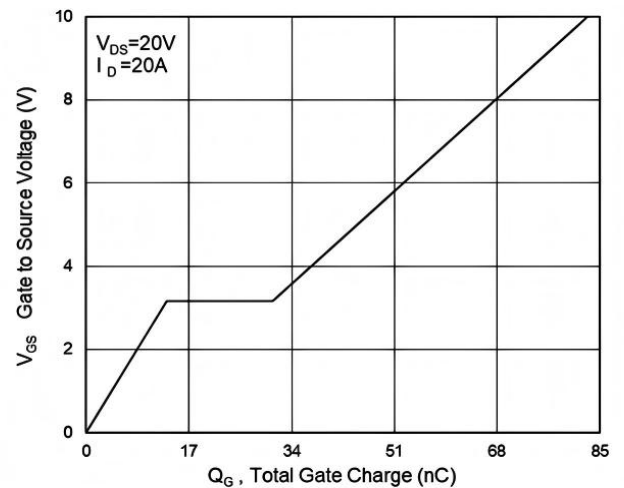


Fig.4 Gate-Charge Characteristics

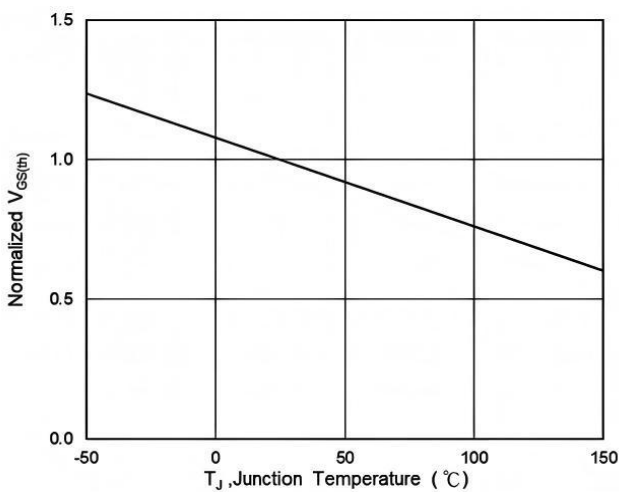


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

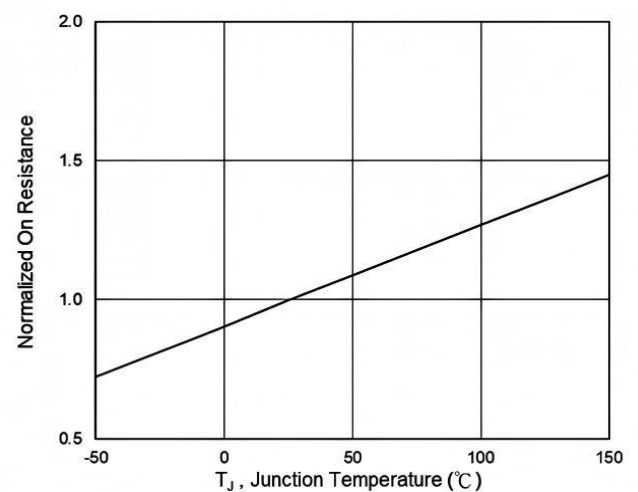


Fig.6 Normalized $R_{DS(on)}$ v.s T_J



30V DUAL Asymmetric N-CH MOSFETs

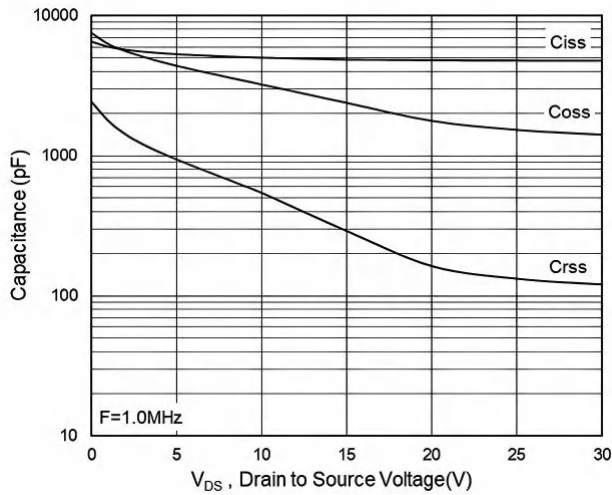


Fig.7 Capacitance

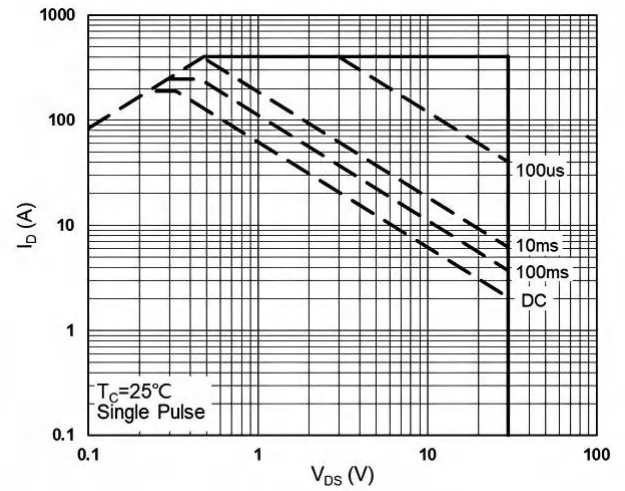


Fig.8 Safe Operating Area

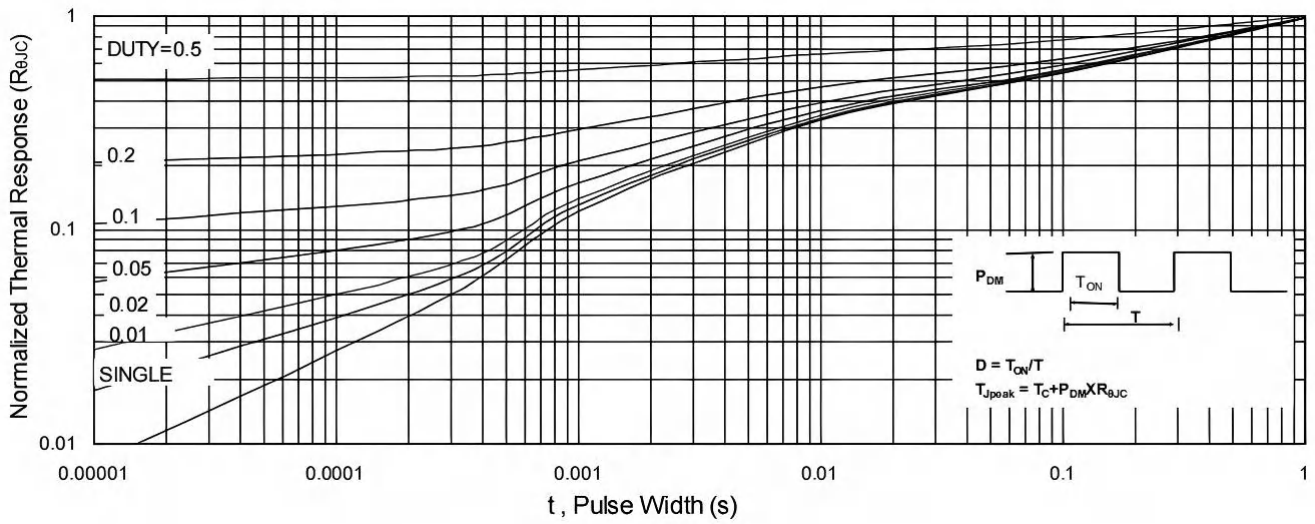


Fig.9 Normalized Maximum Transient Thermal Impedance

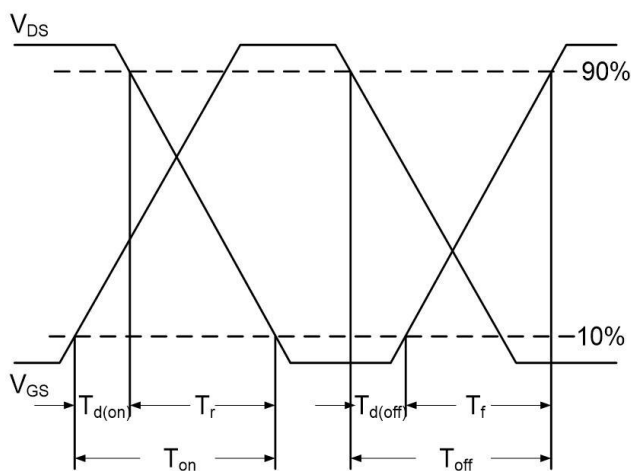


Fig.10 Switching Time Waveform

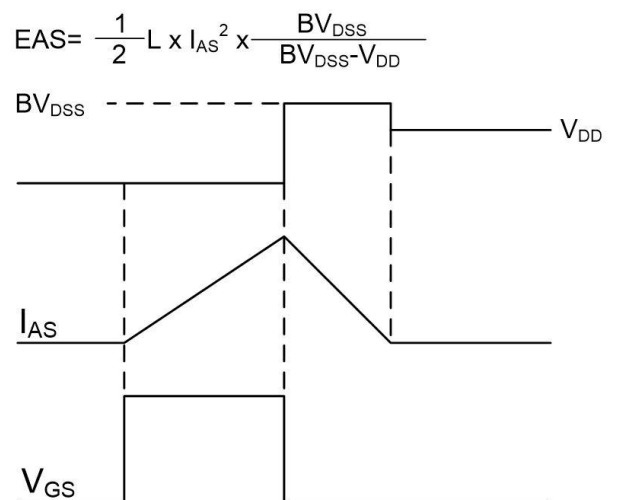


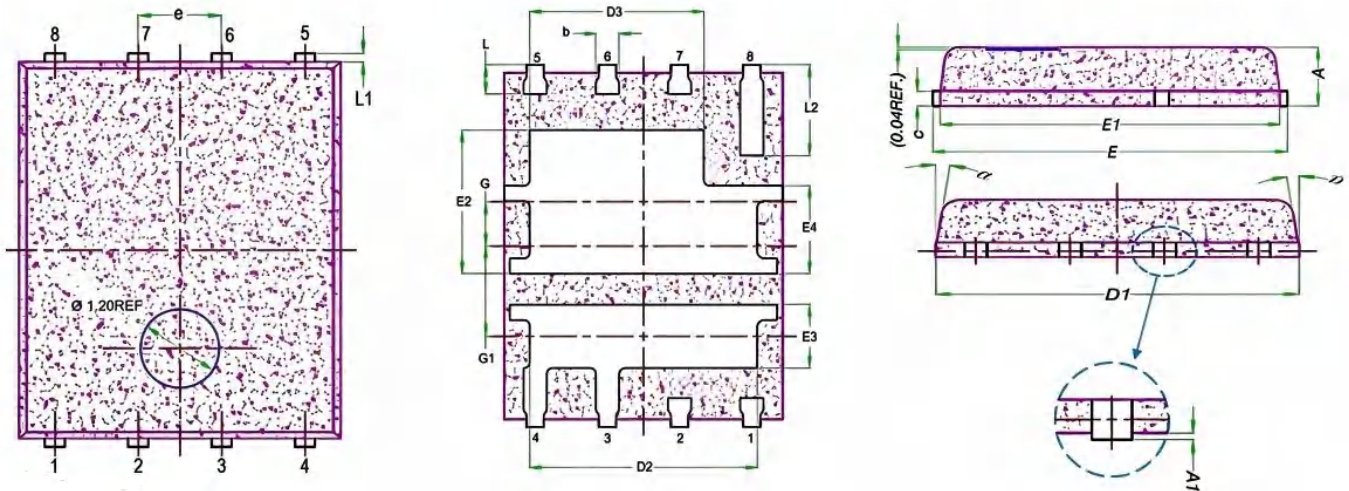
Fig.11 Unclamped Inductive Waveform



Ordering Information

Part Number	Package code	Packaging
HSBA3430	PRPAK5*6	3000/Tape&Reel

PRPAK5*6 Dual Asymmetric Package Outline



COMMON DIMENSIONS(UNIT OF MEASURE - MM)

SYMBOL	MIN	NOM	MAX
A	0.9	1	1.1
A1	0	----	0.05
b	0.33	0.4	0.51
c	0.2	0.25	0.3
D1	4.8	4.9	5
D2	3.9	4	4.1
D3	2.96	3.06	3.16
E	5.9	6	6.1
E1	5.7	5.75	5.8
E2	2.28	2.38	2.48
E3	0.95	1.05	1.15
E4	1.36	1.46	1.56
e	1.27 BSC		
G	0.73 BSC		
G1	1.50 BSC		
L	0.38	0.48	0.58
L1	0.06	0.13	0.2
L2	1.4	1.5	1.6
α	0	-	12

HSBA3430 Marking:

