

Applications

- High Frequency Switching and Synchronous Rectification.
- DC/DC Converter.
- Motor Drivers.

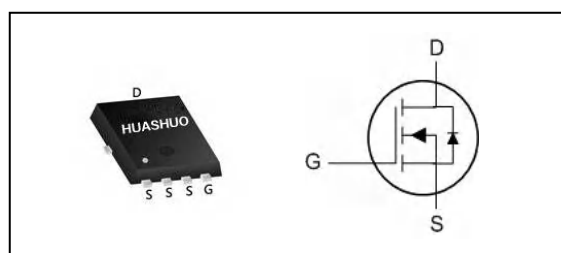
Product Summary

V_{DS}	80	V
$R_{DS(ON),MAX}$	8.7	m Ω
I_D	50	A

Features

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density SGT MOS Technology

PRPAK3X3 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	80	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	50	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	33	A
I_{DM}	Pulsed Drain Current ²	200	A
EAS	Single Pulse Avalanche Energy ³	45	mJ
I_{AS}	Avalanche Current	30	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	45	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	55	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.8	$^\circ C/W$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	80	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=10A$	---	---	8.7	$m\Omega$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V, I_D=10A$	---	---	13	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.8	2.3	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=64V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=64V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
gfs	Forward Transconductance	$V_{DS}=5V, I_D=20A$	---	70	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	1.5	---	Ω
Qg	Total Gate Charge (10V)	$V_{DS}=40V, V_{GS}=10V, I_D=10A$	---	29	---	nC
Qgs	Gate-Source Charge		---	7.7	---	
Qgd	Gate-Drain Charge		---	5.3	---	
Td(on)	Turn-On Delay Time	$V_{DD}=40V, V_{GS}=10V, R_G=3.3\Omega, I_D=10A$	---	6.3	---	ns
Tr	Rise Time		---	19	---	
Td(off)	Turn-Off Delay Time		---	9.4	---	
Tf	Fall Time		---	36	---	
Ciss	Input Capacitance	$V_{DS}=40V, V_{GS}=0V, f=1\text{MHz}$	---	1738	---	pF
Coss	Output Capacitance		---	317	---	
Crss	Reverse Transfer Capacitance		---	12	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	50	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10A, dI/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	35	---	nS
Q_{rr}	Reverse Recovery Charge		---	62	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=30A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.



Typical Characteristics

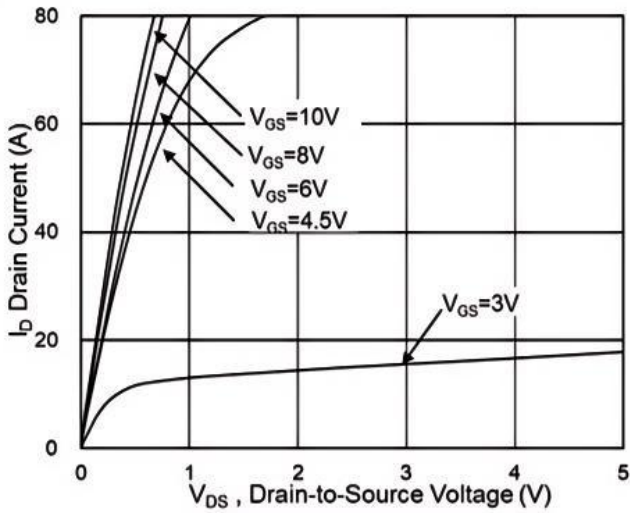


Fig.1 Typical Output Characteristics

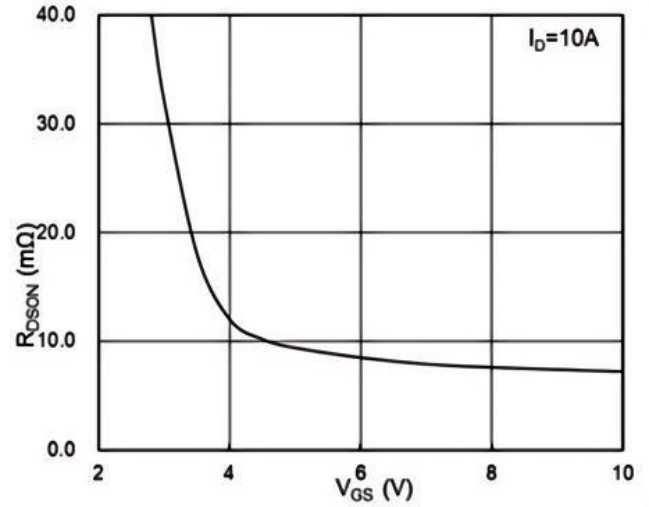


Fig.2 On-Resistance vs G-S Voltage

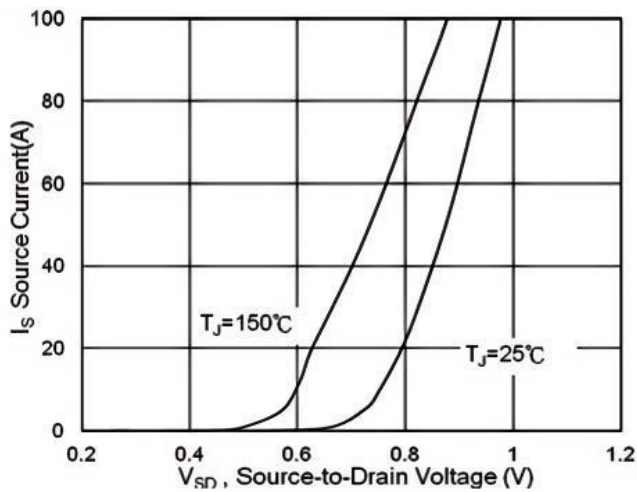


Fig.3 Source Drain Forward Characteristics

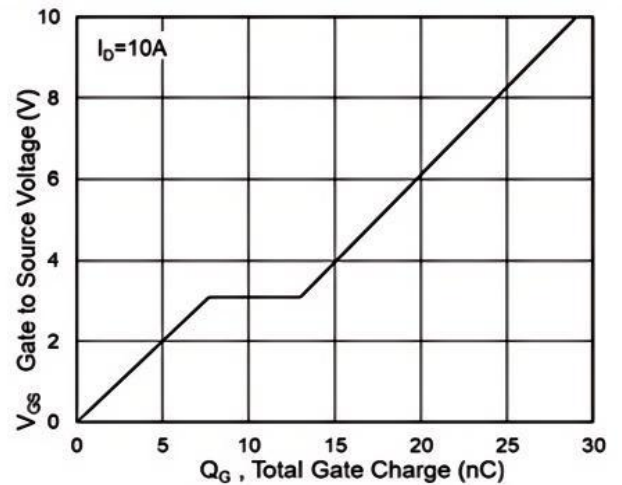


Fig.4 Gate-Charge Characteristics

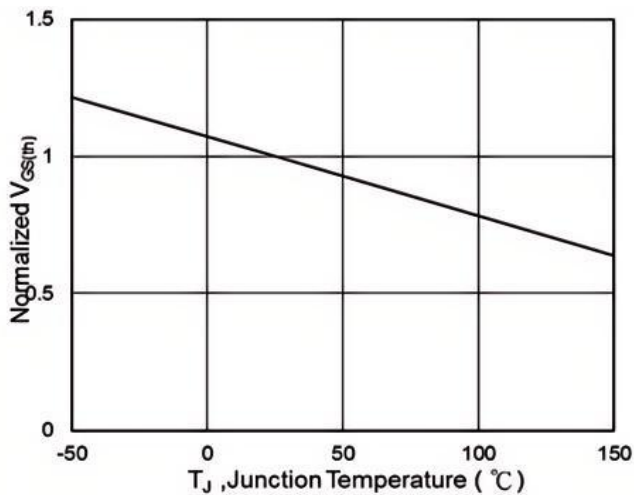


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

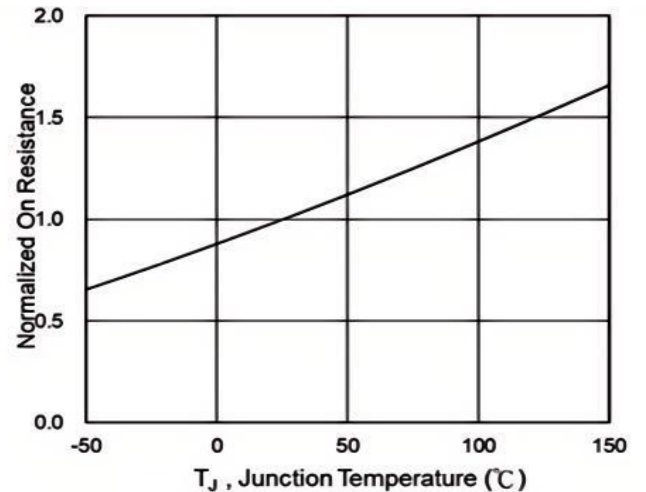


Fig.6 Normalized $R_{DS(on)}$ vs. T_J



N-Ch 80V Fast Switching MOSFETs

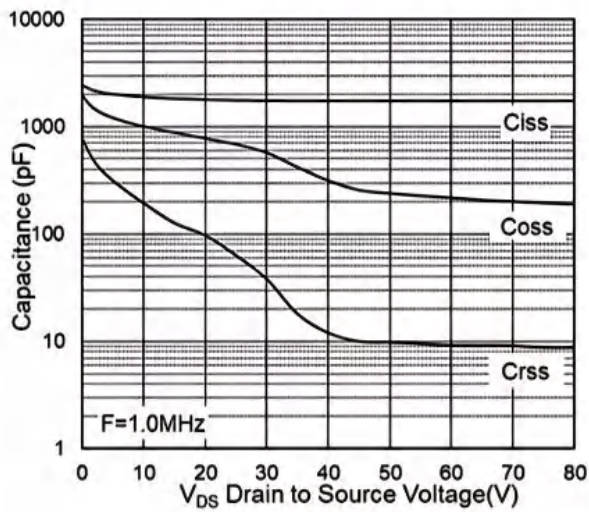


Fig.7 Capacitance

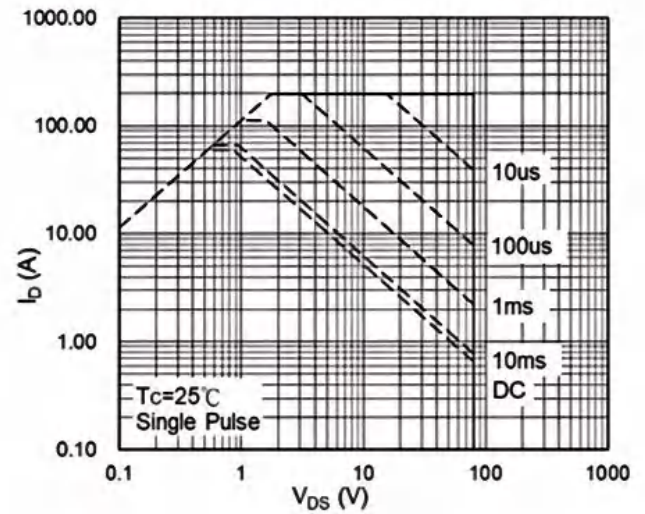


Fig.8 Safe Operating Area

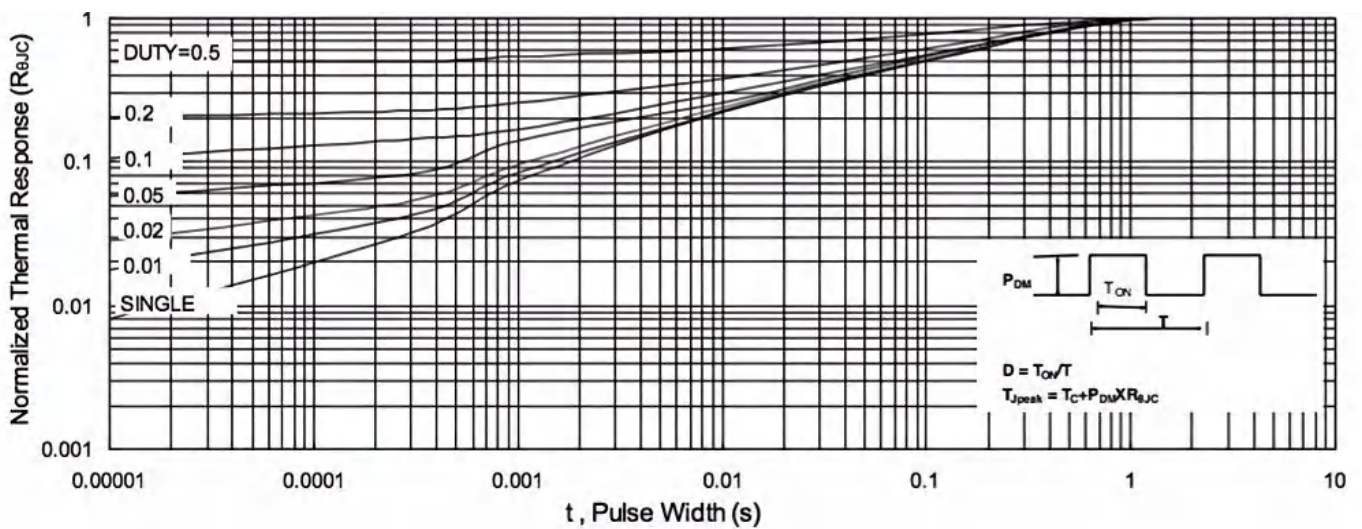


Fig.9 Normalized Maximum Transient Thermal Impedance

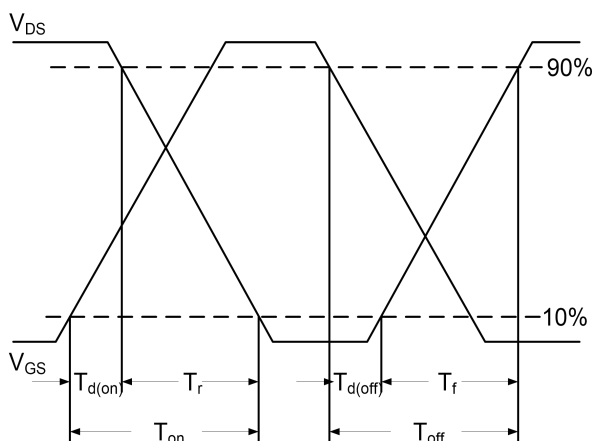


Fig.10 Switching Time Waveform

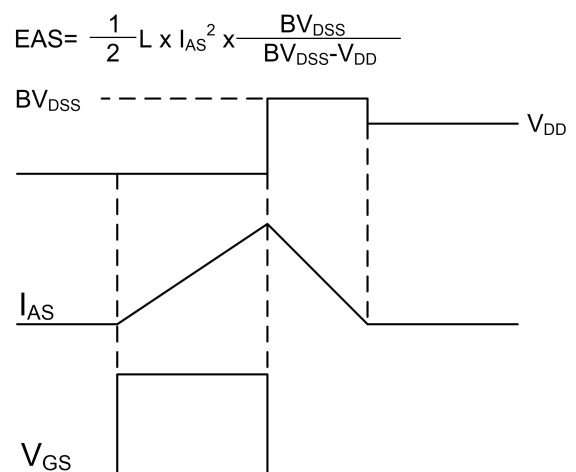


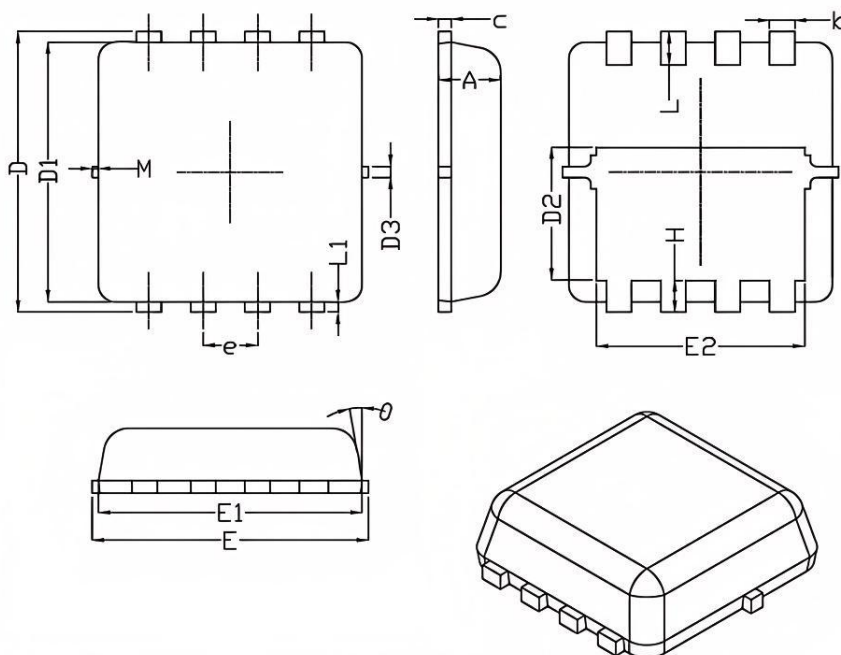
Fig.11 Unclamped Inductive Switching Waveform



Ordering Information

Part Number	Package code	Packaging
HSBB8066	PRPAK3*3	3000/Tape&Reel

PRPAK3*3(E) Single Package Outline



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
C	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.15	3.30	3.45
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°



HSBB8066 Marking:

