

Features

- 100% UIS Tested
- Advance SGT MOS Technology
- Low Gate Charge
- High Current Capability
- RoHS and Halogen-Free Compliant

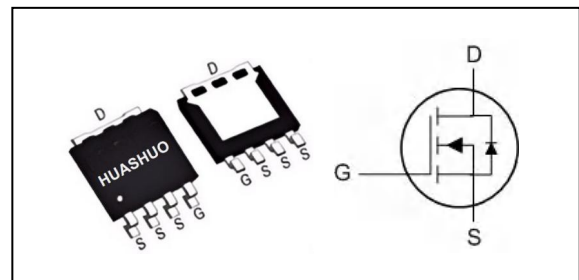
Product Summary

V_{DS}	60	V
$R_{DS(ON),typ}$	0.9	m Ω
I_D	255	A

Applications

- Motor Control
- DC/DC Converters
- Synchronous Rectifier Applications

LFPAK5X6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current ^{1,6}	255	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current ^{1,6}	153	A
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current ^{1,6}	33	A
$I_D@T_A=100^\circ\text{C}$	Continuous Drain Current ^{1,6}	21	A
I_{DM}	Pulsed Drain Current ²	750	A
EAS	Single Pulse Avalanche Energy ³	155	mJ
I_{AS}	Avalanche Current	55	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation ⁴	140	W
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	2.5	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	50	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	0.9	$^\circ\text{C/W}$

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

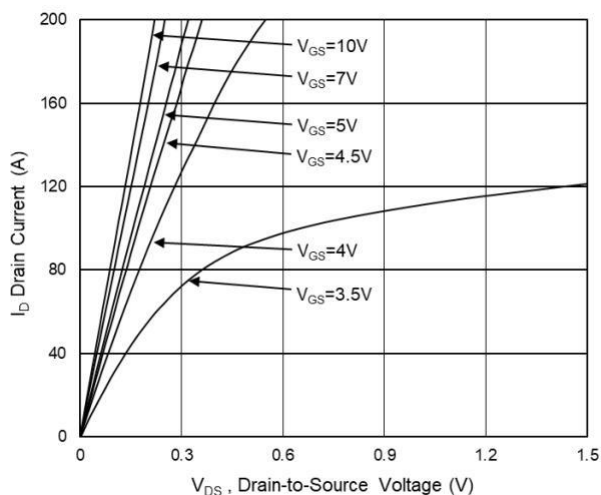
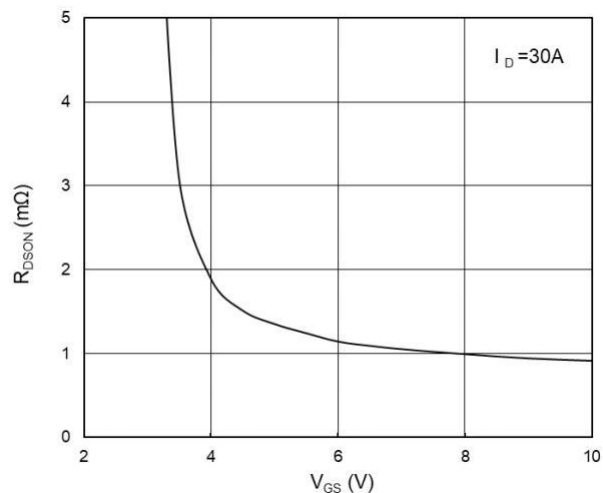
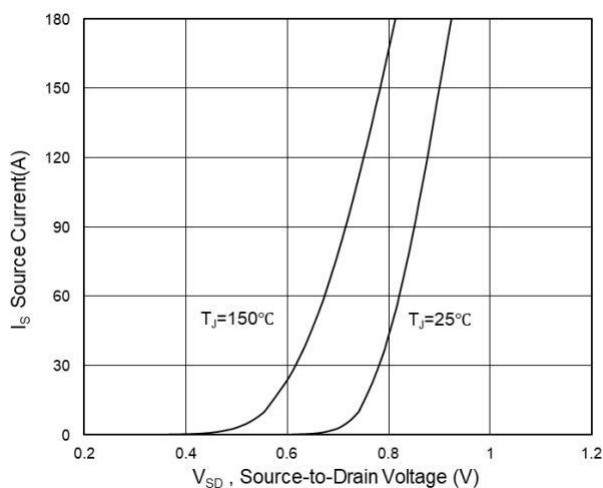
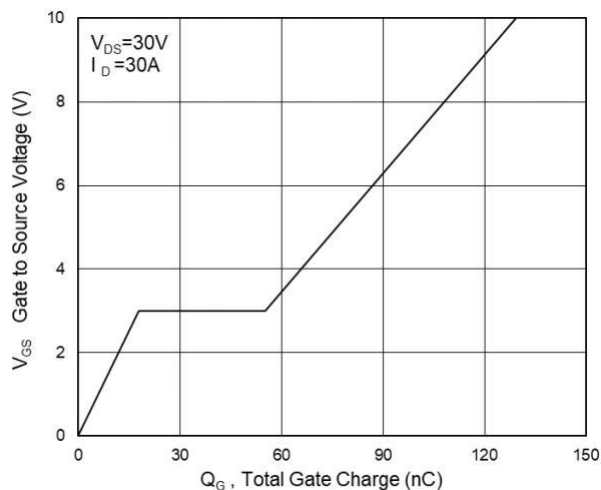
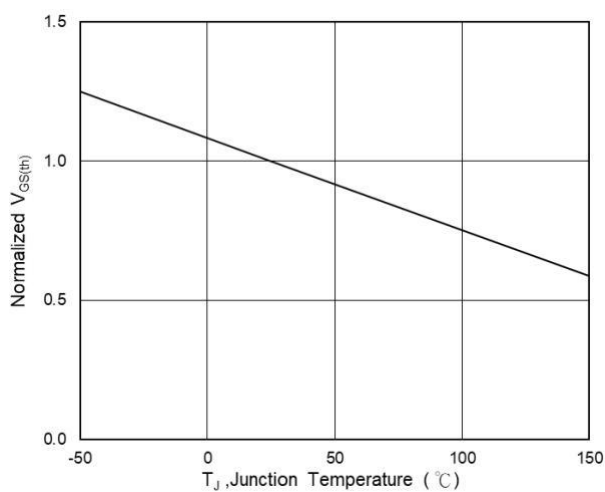
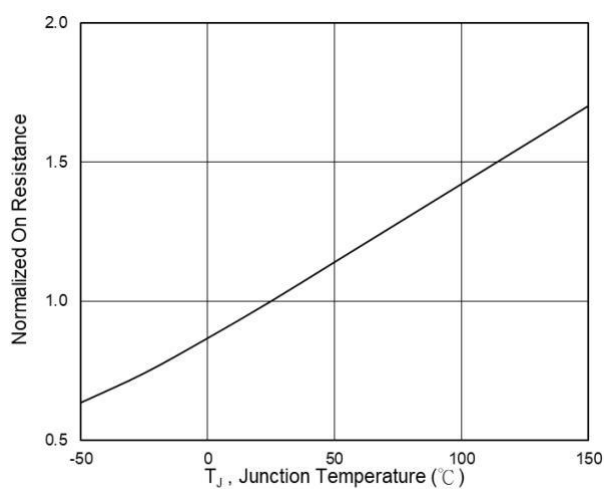
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=30A$	---	0.9	1.3	$m\Omega$
		$V_{GS}=4.5V, I_D=30A$	---	1.5	1.9	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.75	2.2	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=48V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	2.3	---	Ω
Q_g	Total Gate Charge (4.5V)	$V_{DS}=30V, V_{GS}=4.5V, I_D=30A$	---	72	---	nC
Q_{gs}	Gate-Source Charge		---	18	---	
Q_{gd}	Gate-Drain Charge		---	37	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=30V, V_{GS}=10V, R_G=3\Omega, I_D=30A$	---	15	---	ns
T_r	Rise Time		---	42	---	
$T_{d(off)}$	Turn-Off Delay Time		---	87	---	
T_f	Fall Time		---	81	---	
C_{iss}	Input Capacitance	$V_{DS}=30V, V_{GS}=0V, f=1\text{MHz}$	---	6155	---	pF
C_{oss}	Output Capacitance		---	2422	---	
C_{rss}	Reverse Transfer Capacitance		---	141	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	100	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=50A, dI/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	85	---	nS
Q_{rr}	Reverse Recovery Charge		---	205	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=55A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.

Typical Characteristics

Fig.1 Typical Output Characteristics

Fig.2 On-Resistance vs G-S Voltage

Fig.3 Diode Forward Voltage vs. Current

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs T_J

Fig.6 Normalized $R_{DS(on)}$ vs T_J

N-Ch 60V Fast Switching MOSFETs

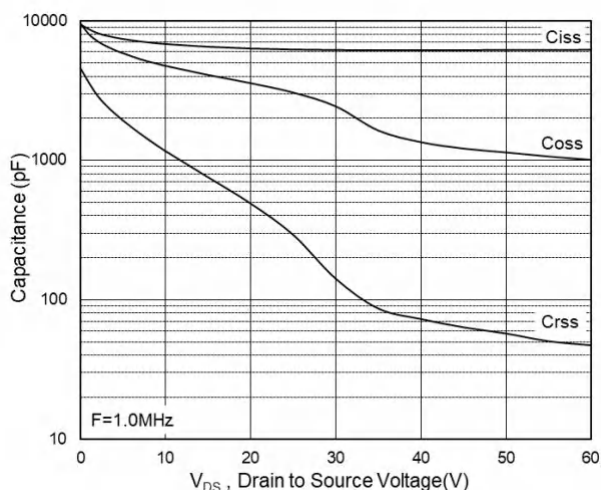


Fig.7 Capacitance

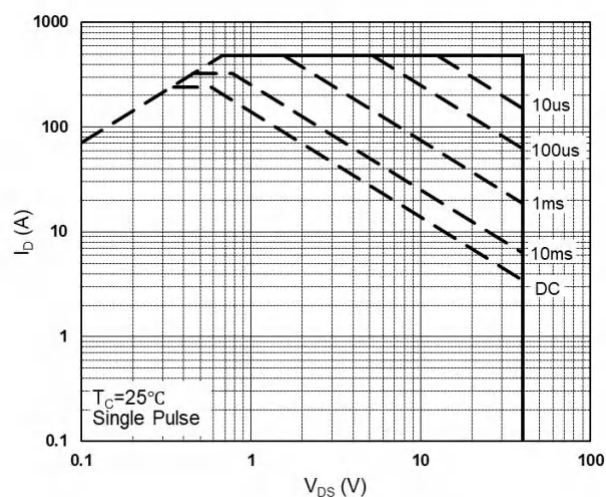


Fig.8 Safe Operating Area

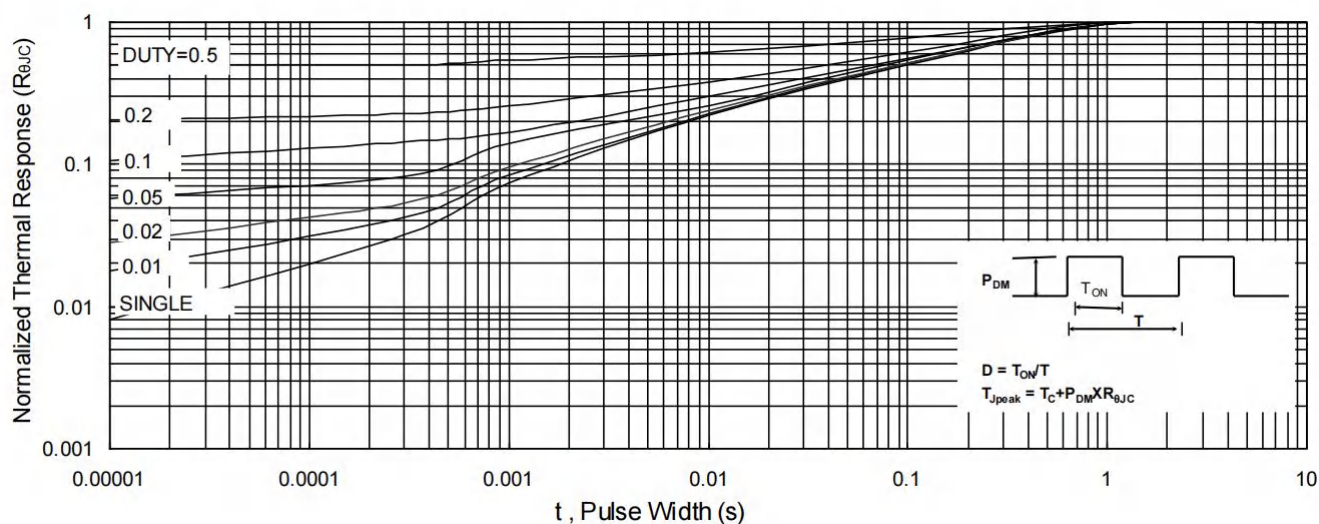


Fig.9 Normalized Maximum Transient Thermal Impedance

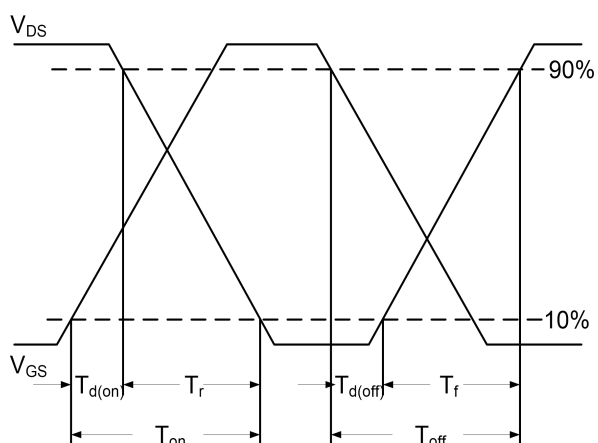


Fig.10 Switching Time Waveform

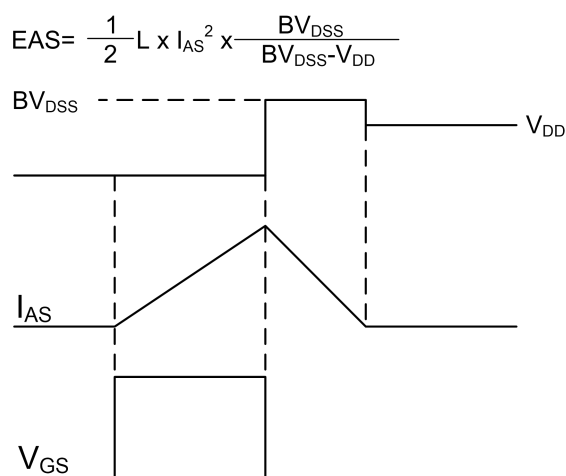
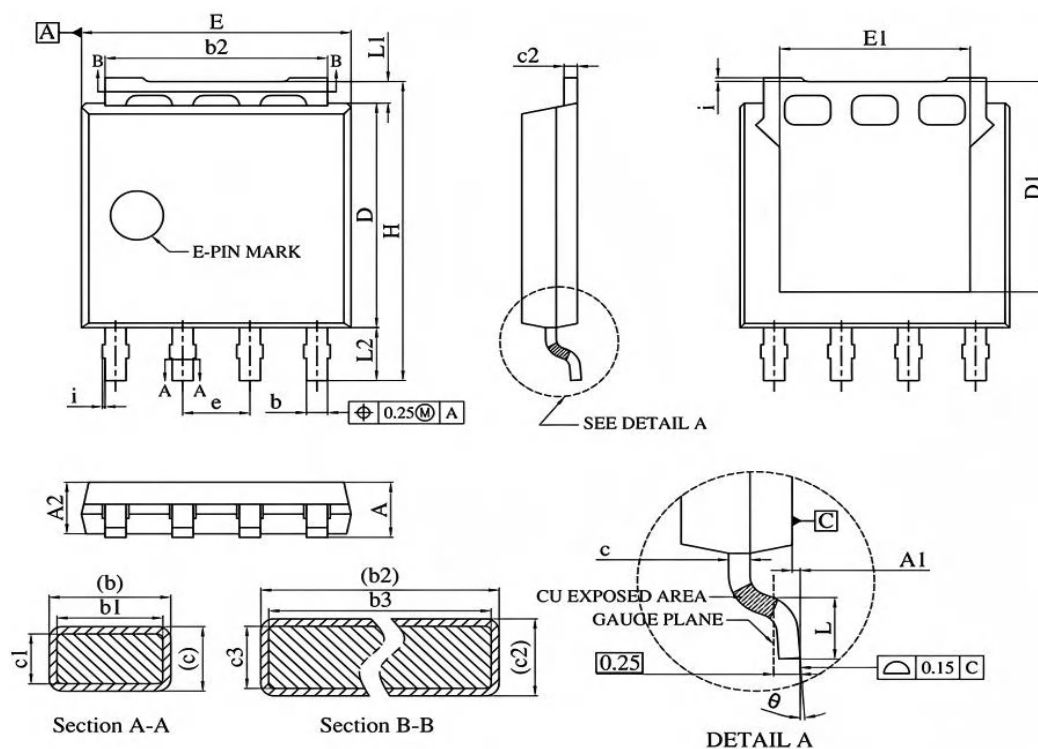


Fig.11 Unclamped Inductive Switching Waveform

Ordering Information

Part Number	Package code	Packaging
HSMA6078	LPAK5*6	2000/Tape&Reel

LPAK5*6 Package Outline



COMMON DIMENSIONS(MM)			
SYMBOLS	MIN	NOM	MAX
A	1.00	-	1.20
A1	0	-	0.15
A2	0.98	1.05	1.12
b	0.35	0.40	0.50
b1	0.32	0.43	0.54
b2	4.02	4.21	4.40
b3	4.00	4.19	4.37
c	0.18	0.20	0.25
c1	0.17	0.20	0.23
c2	0.23	0.25	0.30
c3	0.22	0.25	0.28
D	4.45	-	4.70
D1	4.20	-	4.45
E	4.95	-	5.40
E1	3.50	-	3.70
e	1.27 BSC.		
H	5.95	-	6.25
i	-	-	0.25
L	0.40	-	0.85
L1	0.270	-	0.57
L2	0.80	-	1.30
θ	0°	-	8°

HSMA6078 Marking:

