

General Description

The HSBB15N15S is the highest performance SGT N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(on)}$ and gate charge for most of the synchronous buck converter applications.

The HSBB15N15S meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

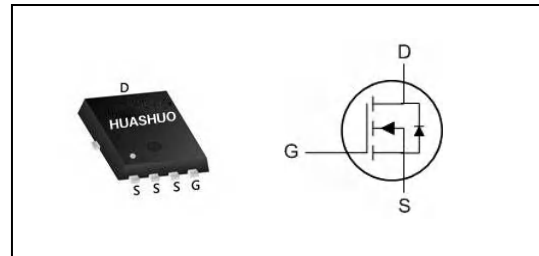
Product Summary

V_{DS}	150	V
$R_{DS(on),typ}$	35	m Ω
I_D	15	A

Features

- Super Low Gate Charge
- Green Device Available
- Excellent C_{dv}/dt effect decline
- Advanced high cell density SGT MOS Technology

PRPAK3*3 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	15	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	9	A
I_{DM}	Pulsed Drain Current ²	60	A
EAS	Single Pulse Avalanche Energy ³	53	mJ
I_{AS}	Avalanche Current	18	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ³	45	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	55	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.8	$^\circ C/W$



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	150	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =10A	---	35	56	mΩ
	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =10A	---	41	68	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.8	2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =120V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =120V, V _{GS} =0V, T _J =55°C	---	---	5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A	---	33	---	S
Q _g	Total Gate Charge	V _{DS} =75V, V _{GS} =4.5V, I _D =10A	---	19	---	nC
Q _{gs}	Gate-Source Charge		---	4.5	---	
Q _{gd}	Gate-Drain Charge		---	2.6	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =75V, V _{GS} =10V, R _G =3.3Ω I _D =10A	---	13	---	ns
T _r	Rise Time		---	8.2	---	
T _{d(off)}	Turn-Off Delay Time		---	25	---	
T _f	Fall Time		---	11	---	
C _{iss}	Input Capacitance	V _{DS} =75V, V _{GS} =0V, f=1MHz	---	1090	---	pF
C _{oss}	Output Capacitance		---	93	---	
C _{rss}	Reverse Transfer Capacitance		---	6	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	15	A
I _{SM}	Pulsed Source Current ^{2,5}		---	---	60	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =10A, dI/dt=100A/μs, T _J =25°C	---	37	---	nS
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	263	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V, V_{GS}=10V, L=0.3mH, I_{AS}=18A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

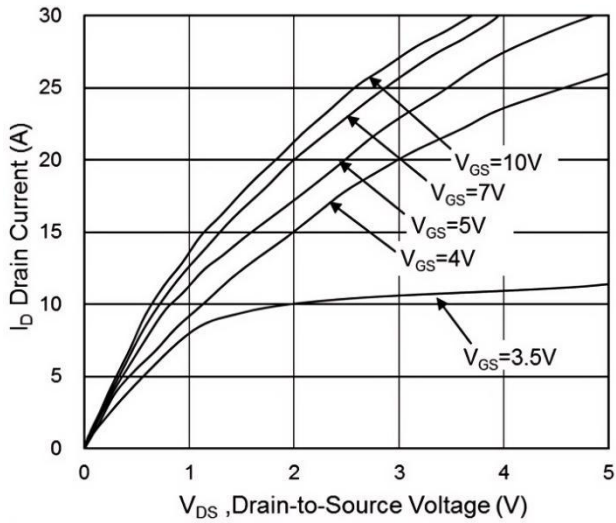


Fig.1 Typical Output Characteristics

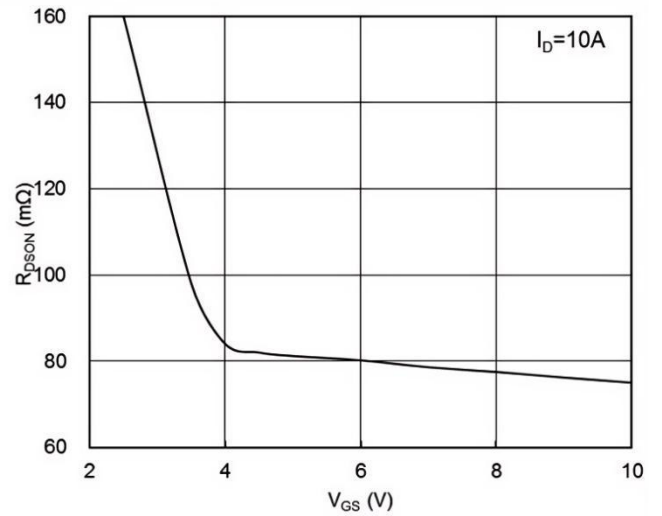


Fig.2 On-Resistance vs. Gate-Source Voltage

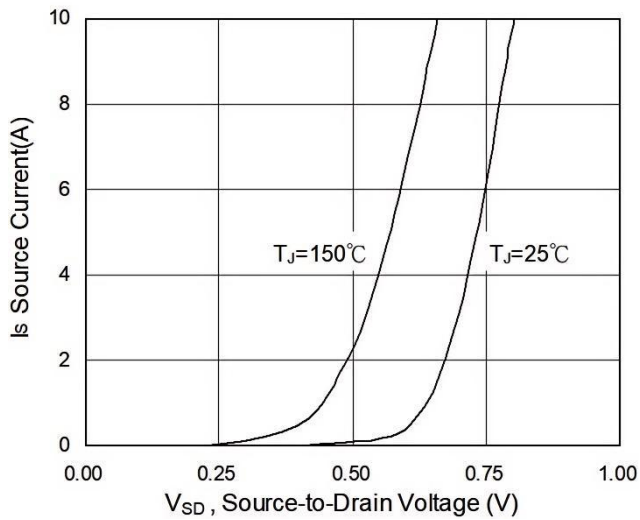


Fig.3 Forward Characteristics of Reverse

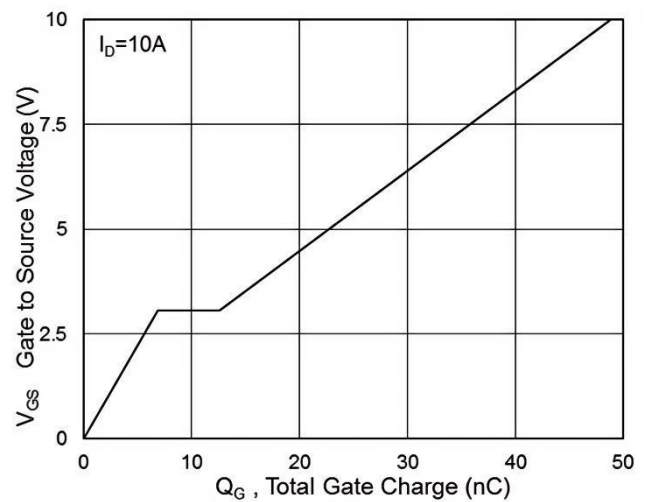


Fig.4 Gate-Charge Characteristics

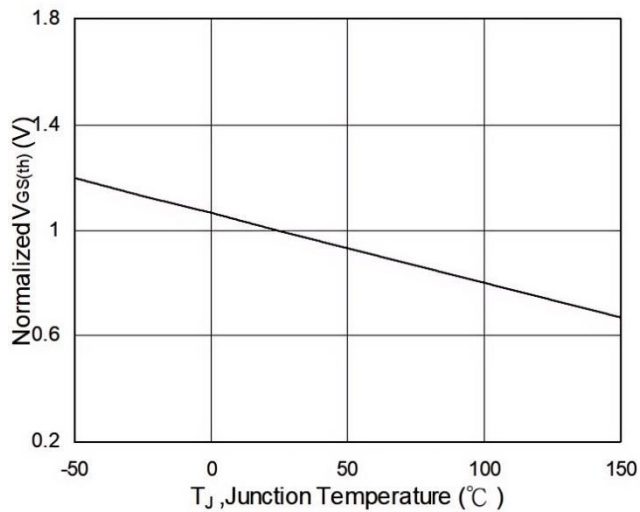


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

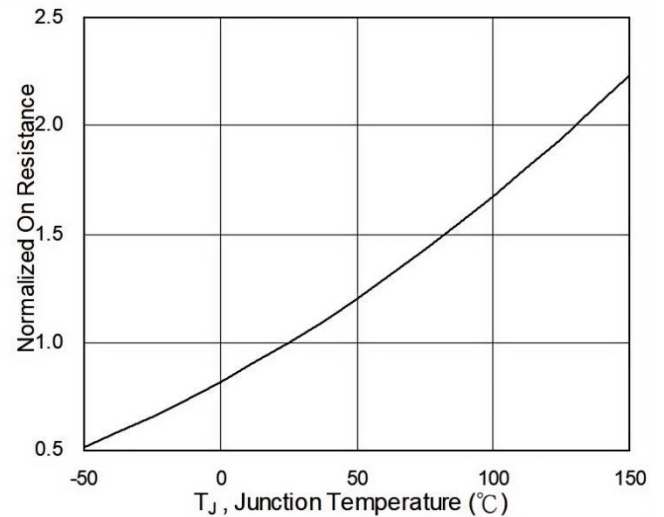


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

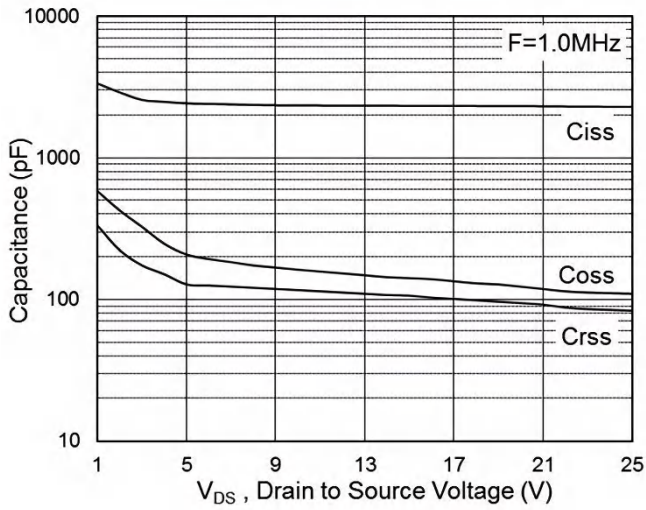


Fig.7 Capacitance

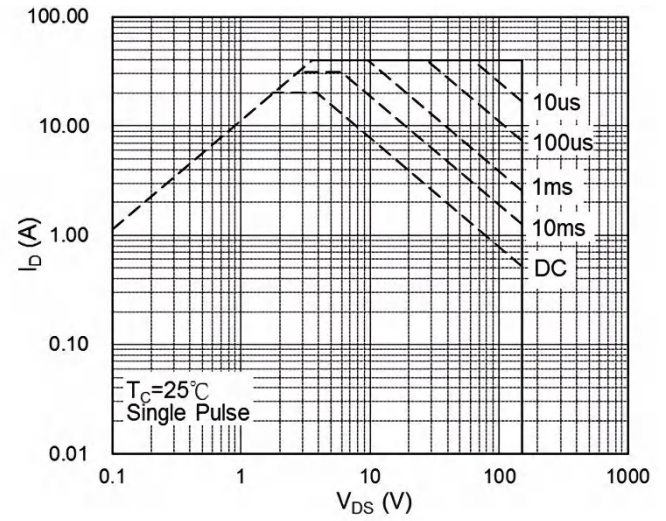


Fig.8 Safe Operating Area

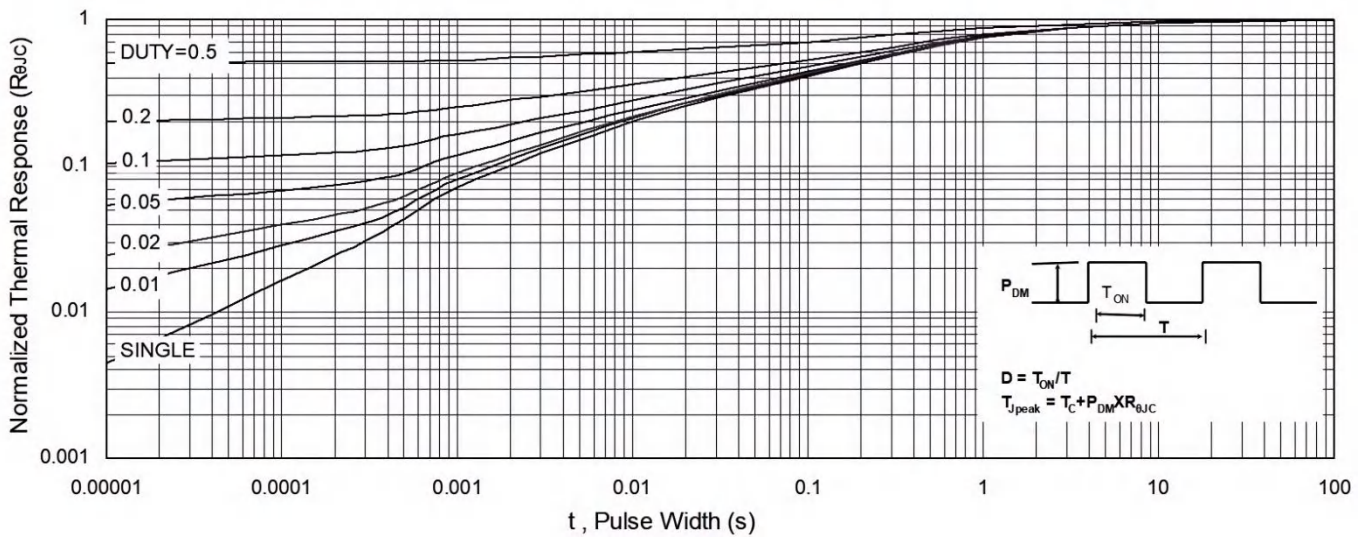


Fig.9 Normalized Maximum Transient Thermal Impedance

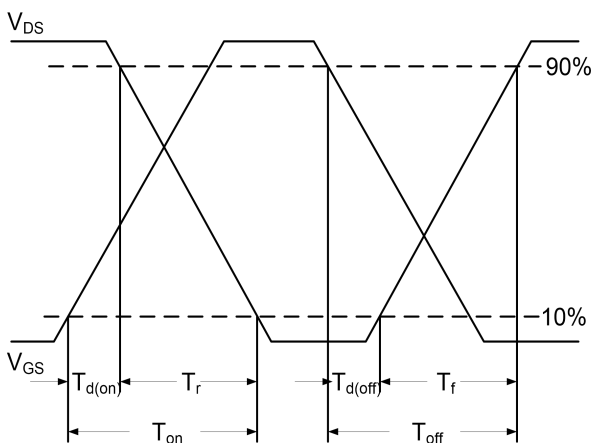


Fig.10 Switching Time Waveform

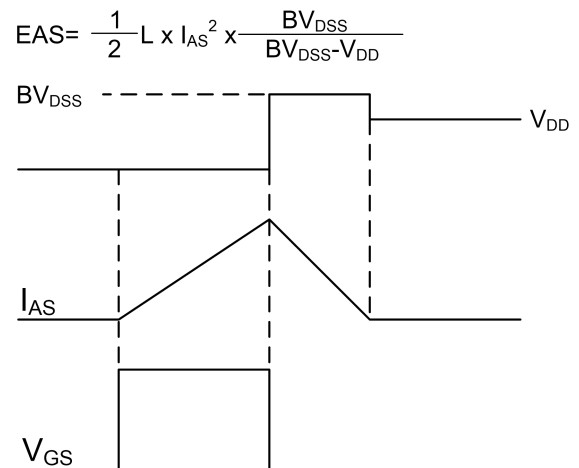


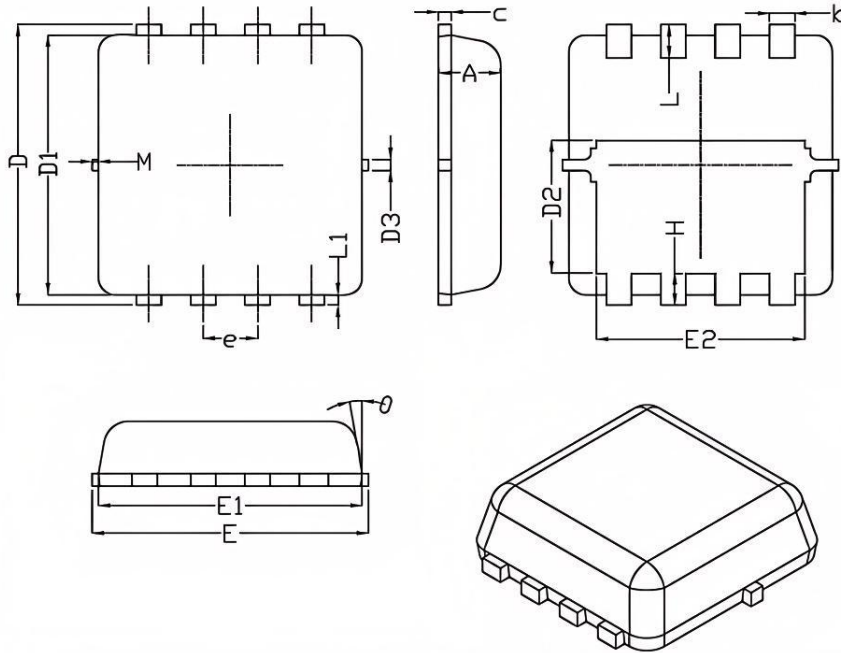
Fig.11 Unclamped Inductive Switching Waveform



Ordering Information

Part Number	Package code	Packaging
HSBB15N15S	PRPAK3*3	3000/Tape&Reel

PRPAK3*3(E) Single Package Outline



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
C	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.15	3.30	3.45
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°



HSBB15N15S Marking:

