



General Description

The HSBB35P02 is the high cell density trench P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The HSBB35P02 meet the RoHS and Green Product requirement with full function reliability approved.

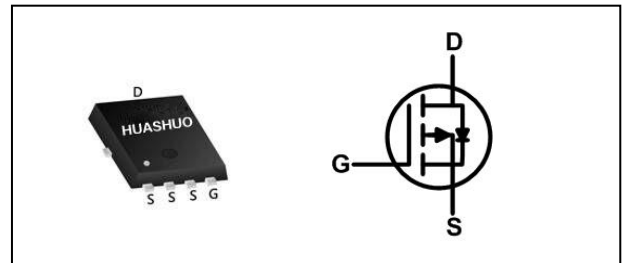
Features

- Super Low Gate Charge
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench Technology

Product Summary

V _{DS}	-20	V
R _{DS(ON),typ}	7.9	mΩ
I _D	-35	A

PRPAK3x3 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-Source Voltage	±12	V
I _{D@T_C=25°C}	Continuous Drain Current, V _{GS} @ -4.5V ¹	-35	A
I _{D@T_C=100°C}	Continuous Drain Current, V _{GS} @ -4.5V ¹	-28	A
I _{DM}	Pulsed Drain Current ²	-140	A
P _{D@T_C=25°C}	Total Power Dissipation ³	40	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	75	°C/W
R _{θJA}	Thermal Resistance Junction-Ambient ¹ (t ≤ 10s)	40	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	3.2	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-20	---	---	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-4.5V, I _D =-10A	---	7.9	10	mΩ
		V _{GS} =-2.5V, I _D =-5A	---	9	13	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-0.4	-0.62	-1.0	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-20V, V _{GS} =0V, T _J =25°C	---	---	1	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-10V, V _{GS} =-4.5V, I _D =-10A	---	42	---	nC
Q _{gs}	Gate-Source Charge		---	8.1	---	
Q _{gd}	Gate-Drain Charge		---	10	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-10V, V _{GS} =-4.5V, R _G =3.3Ω, I _D =-10A	---	15	---	ns
T _r	Rise Time		---	31	---	
T _{d(off)}	Turn-Off Delay Time		---	133	---	
T _f	Fall Time		---	55	---	
C _{iss}	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1MHz	---	3483	---	pF
C _{oss}	Output Capacitance		---	529	---	
C _{rss}	Reverse Transfer Capacitance		---	421	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	-35	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1.2	V

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The power dissipation is limited by 150°C junction temperature
4. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



Typical Characteristics

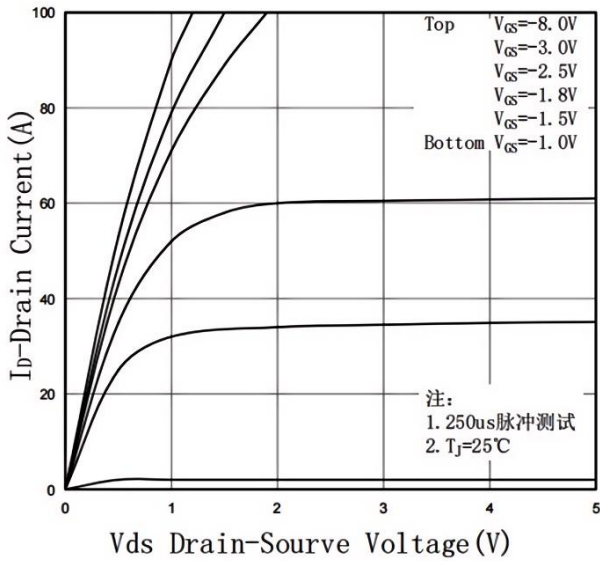


Fig.1 Output Characteristic

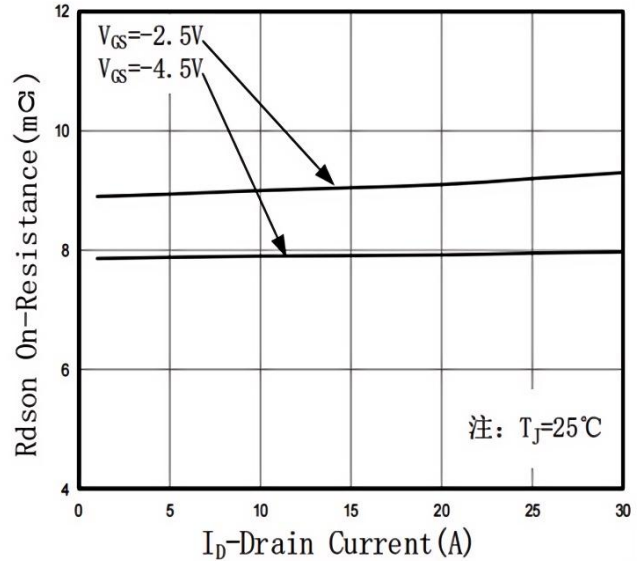


Fig.2 On-Resistance vs. Drain Current

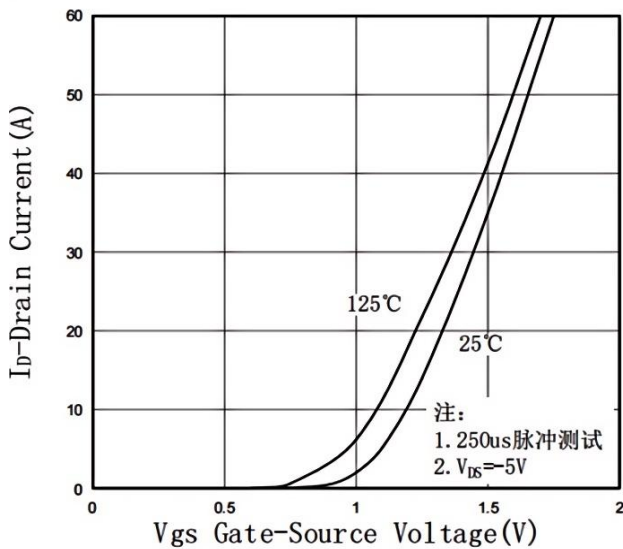


Fig.3 Transfer Characteristic

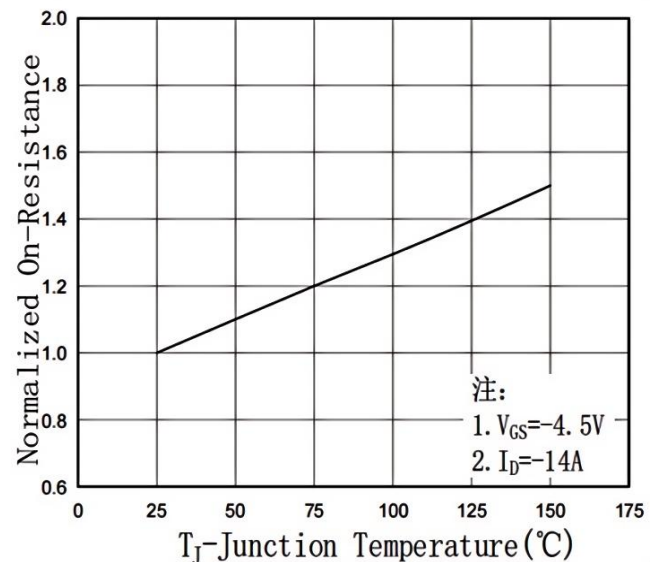


Fig.4 On-Resistance vs. Junction Temperature

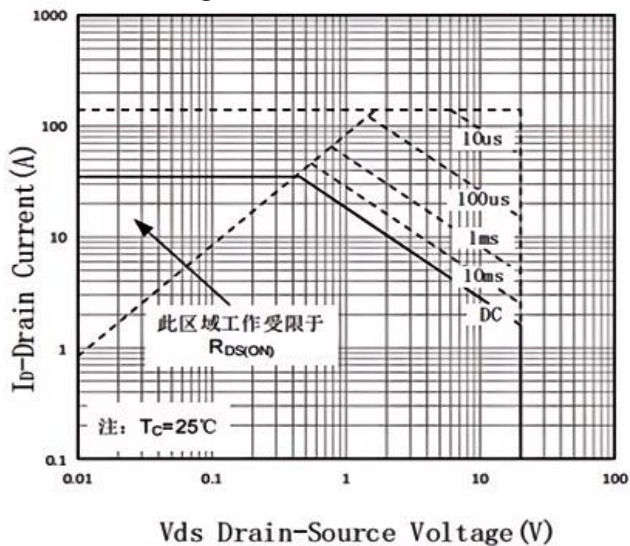


Fig.5 Safe Operation Area

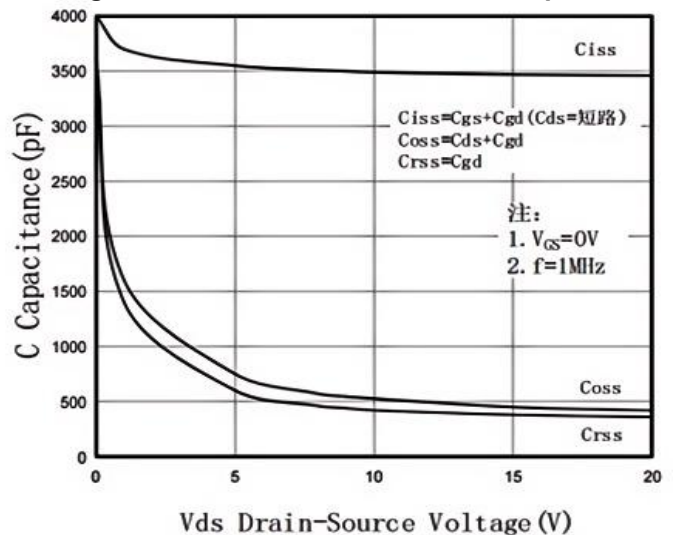


Fig.6 Capacitance Characteristic

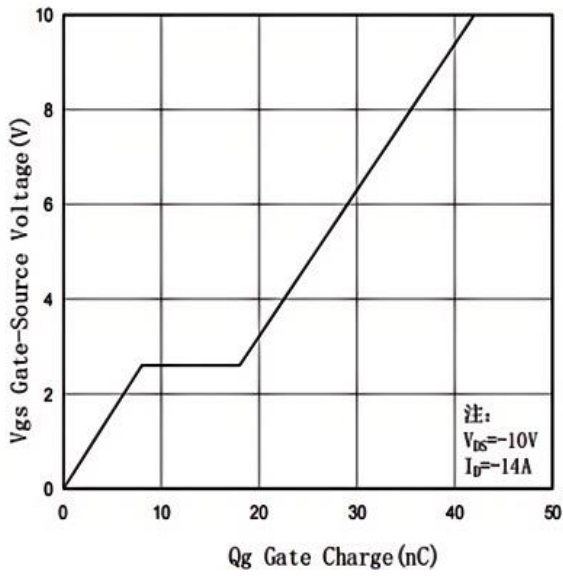


Fig.7 Gate-Charge Characteristic

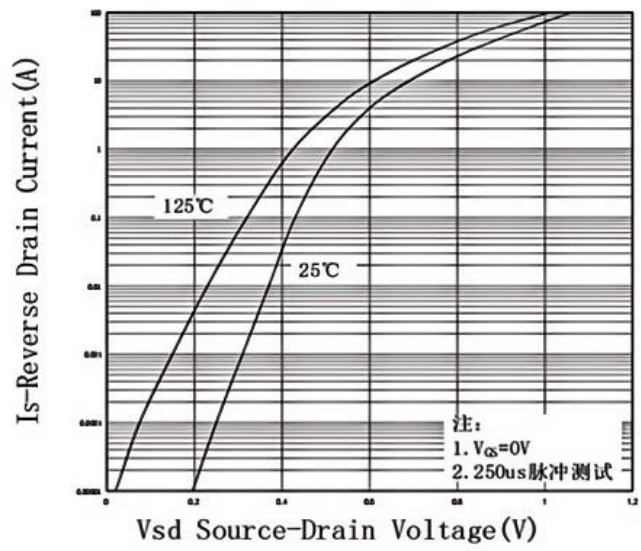


Fig.8 Body Diode Characteristic

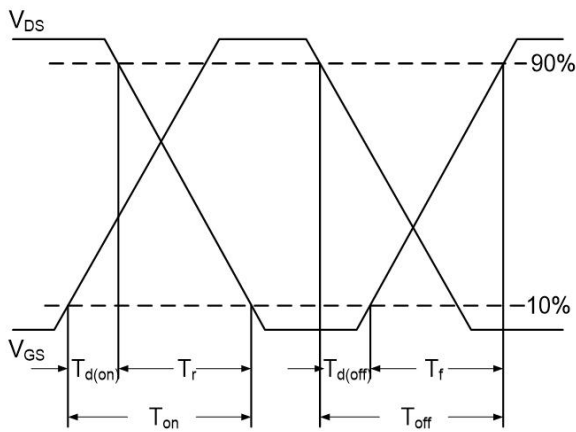


Fig.9 Switching Time Waveform

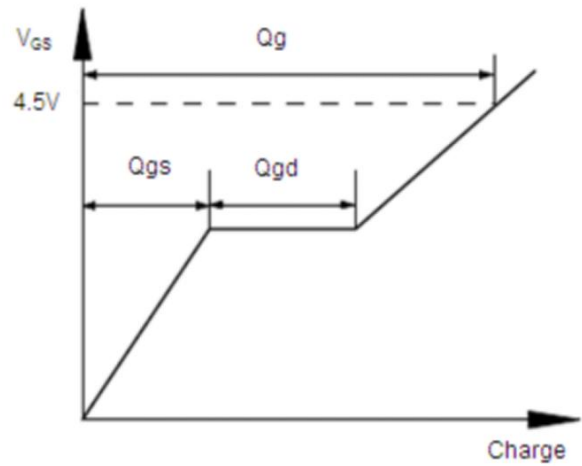


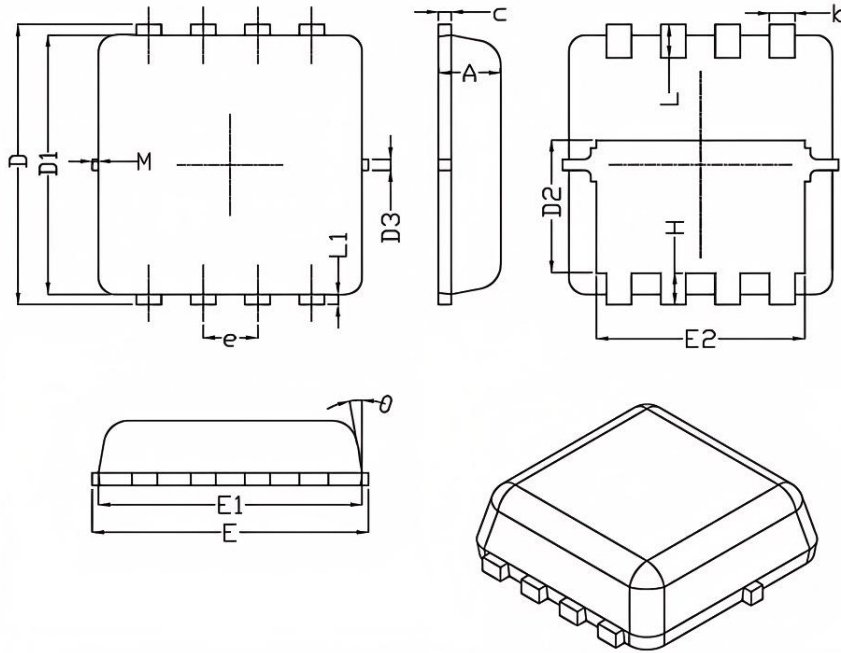
Fig.10 Gate Charge Waveform



Ordering Information

Part Number	Package code	Packaging
HSBB35P02	PRPAK3*3	3000/Tape&Reel

PRPAK3*3(E) Single Package Outline



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
C	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.15	3.30	3.45
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°



HSBB35P02 Marking:

