

# CMSC3050

30V, 4.4mΩ typ., 80A N-Channel MOSFET

## General Description

The CMSC3050 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ . This is suitable for DC/DC converter and general purpose applications.

## Features

- Low On-Resistance
- Simple Drive Requirements
- Surface Mount Package
- RoHS Compliant

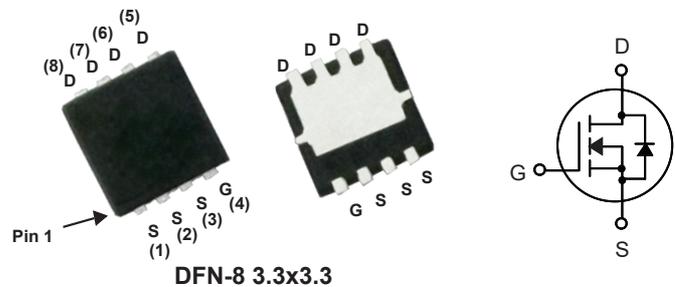
## Product Summary

BVDSS	$R_{DS(on)}$ max.	ID
30V	5mΩ	80A

## Applications

- DC-DC converters
- Load switching

## DFN-8 3.3x3.3 Pin Configuration



Type	Package	Marking
CMSC3050	DFN-8 3.3x3.3	3050

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	±20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	80	A
$I_D@T_C=100^\circ C$	Continuous Drain Current	56	A
$I_{DM}$	Pulsed Drain Current	320	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	113	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	45	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>2</sup>	---	56	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	2.78	°C/W

**Electrical Characteristics (T<sub>J</sub>=25°C , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30	---	---	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =20A	---	4.4	5	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A	---	7	9	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	---	2.0	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V	---	---	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V , V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =10A	---	14	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	---	6.7	---	Ω
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =20A	---	20	---	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> = 15V	---	4	---	
Q <sub>gd</sub>	Gate-Drain Charge	V <sub>GS</sub> =4.5V	---	8	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> = 15V	---	6	---	ns
T <sub>r</sub>	Rise Time	V <sub>GS</sub> = 10V	---	11	---	
T <sub>d(off)</sub>	Turn-Off Delay Time	R <sub>GEN</sub> = 3Ω	---	36	---	
T <sub>f</sub>	Fall Time	R <sub>L</sub> =0.75Ω	---	12	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz	---	1600	---	pF
C <sub>oss</sub>	Output Capacitance		---	150	---	
C <sub>riss</sub>	Reverse Transfer Capacitance		---	140	---	

**Diode Characteristics**

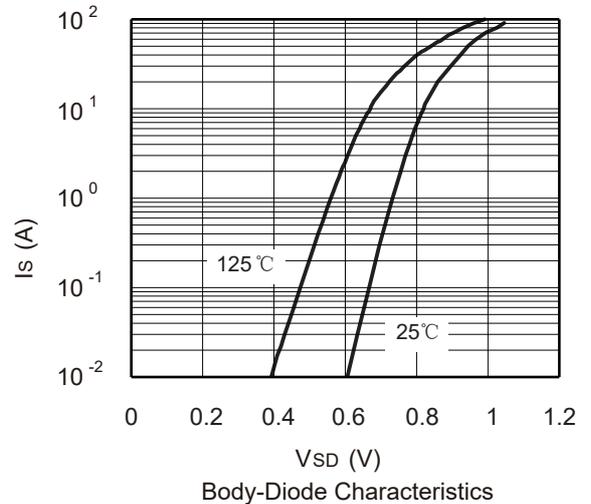
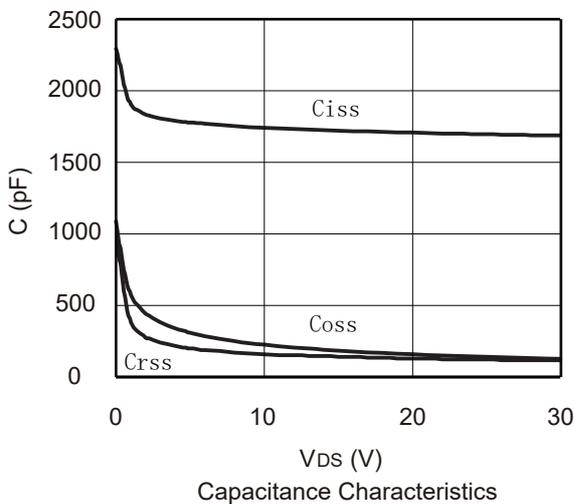
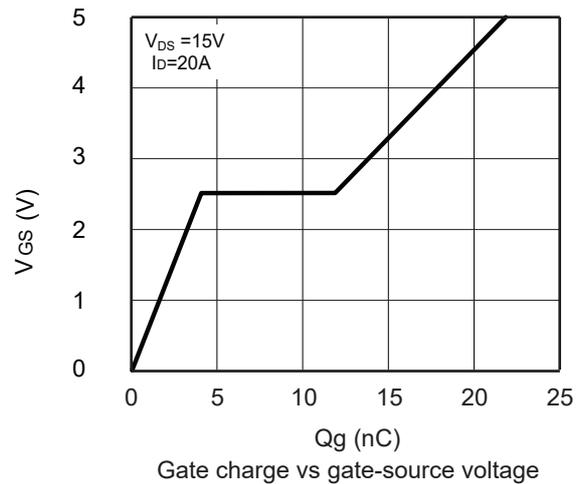
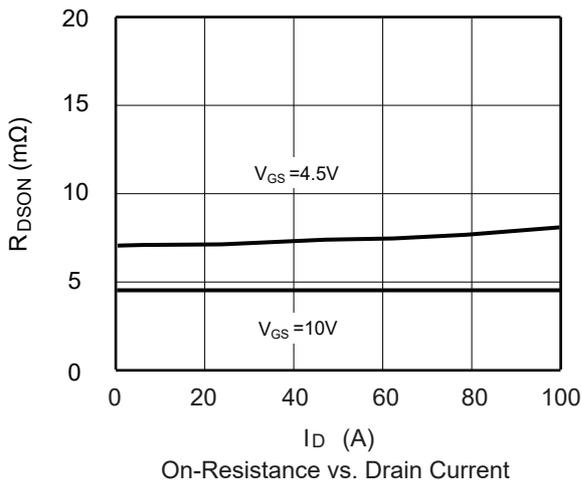
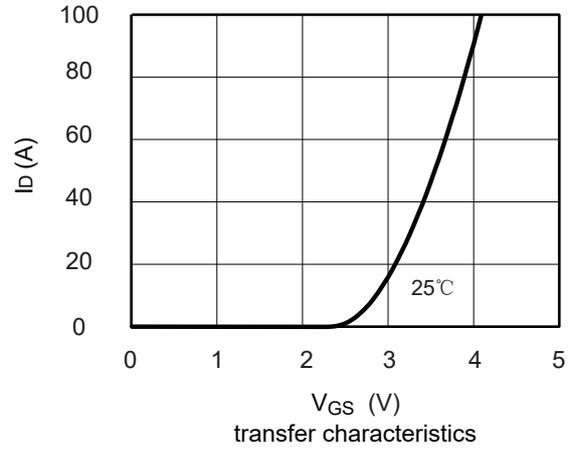
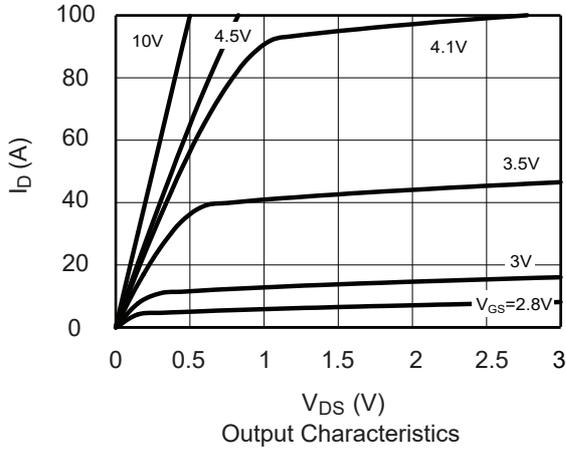
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	80	A
I <sub>SM</sub>	Pulsed Source Current		---	---	320	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =20A , T <sub>J</sub> =25°C	---	0.85	1.4	V

Note :

- 1.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V , V<sub>GS</sub>=10V , L=0.3mH , I<sub>AS</sub>=27.5A.
2. Surface-mounted on FR4 board using 1 sq-in pad, 1 oz Cu.

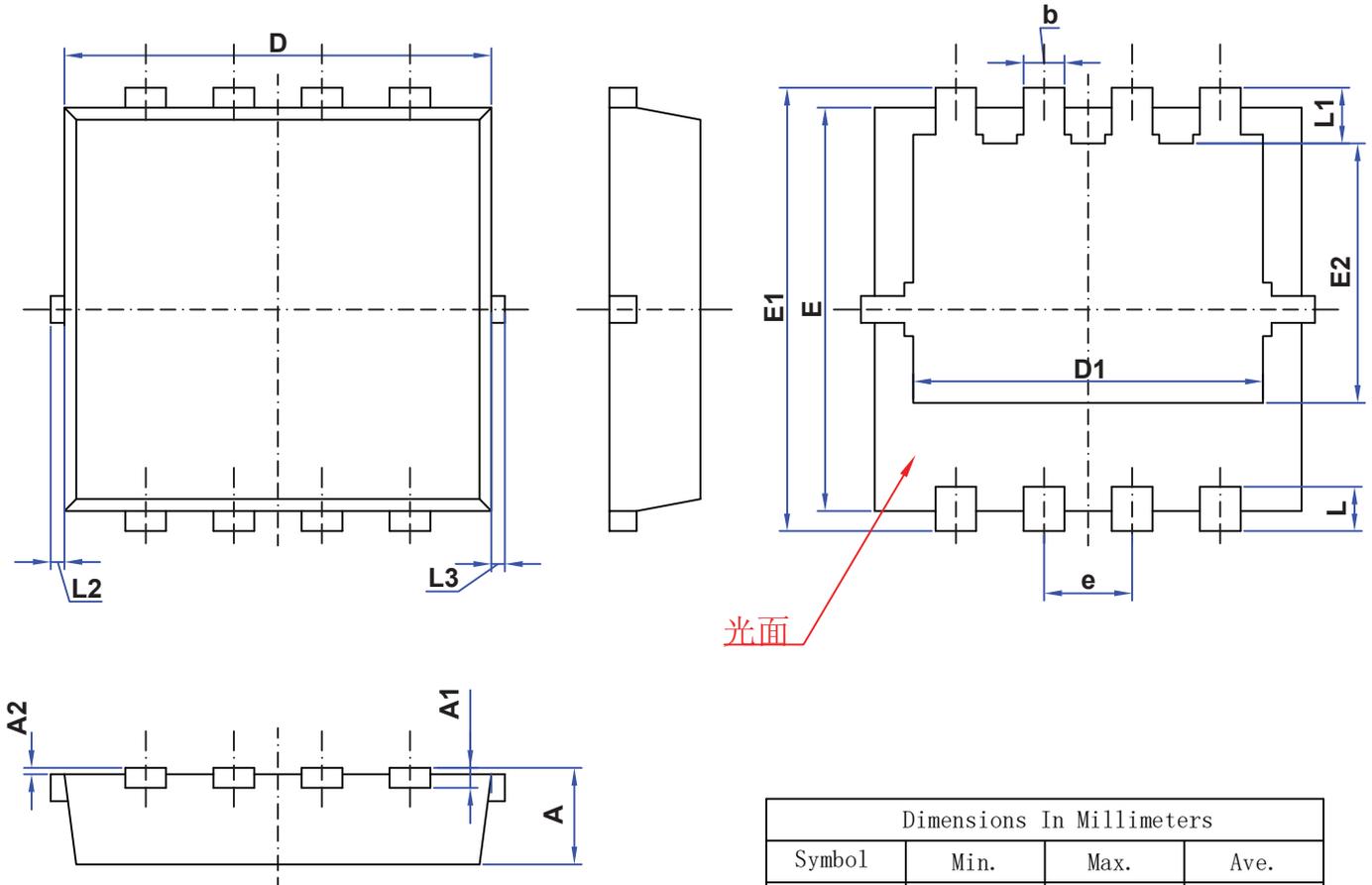
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 Cmos reserves the right to improve product design ,functions and reliability without notice.Please refer to the latest version of specification.

Typical Characteristics



Package Dimension

DFN-8 3.3x3.3 Unit :mm



注:

1. 未注公差±0.10,
2. 塑封体无缺损、缩孔、裂纹、气泡等不良缺陷
3. 标注单位mm

Dimensions In Millimeters			
Symbol	Min.	Max.	Ave.
A	0.700	0.900	0.800
A1	0.100	0.200	0.150
A2	-	0.050	-
D	3.000	3.200	3.100
D1	2.350	2.550	2.450
E	3.000	3.200	3.100
E1	3.200	3.600	3.400
E2	1.635	1.835	1.735
b	0.200	0.400	0.300
e	0.550	0.750	0.650
L	0.250	0.650	0.450
L1	0.345	0.745	0.545
L2	0~0.100		
L3	0~0.100		