

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
250V	8.0mΩ@10V	170A



合肥矽普半导体

Siliup Semiconductor Technology Co., Ltd

技术 品质 服务

www.siliup.com

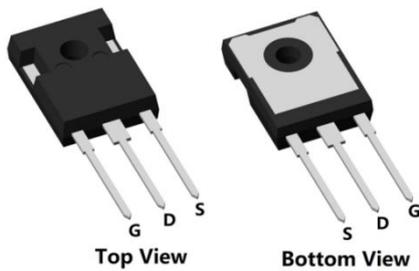
### Feature

- Fast Switching
- Low Gate Charge and R<sub>ds(on)</sub>
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

### Applications

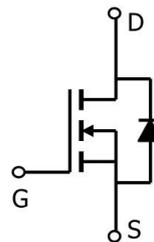
- PWM Application
- Hard switched and high frequency circuits
- Power Management

### Package

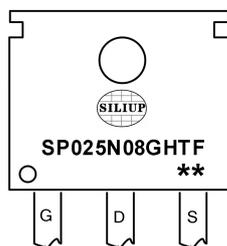


TO-247(1:G 2:D 3:S)

### Circuit diagram



### Marking



SP025N08GHTF : Device Code  
 \*\* : Week Code

### Order Information

Device	Package	Unit/Tube
SP025N08GHTF	TO-247	30

**Absolute maximum ratings (Ta=25°C, unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	250	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current (Tc=25°C)	$I_D$	170	A
Continuous Drain Current (Tc=100°C)	$I_D$	113	A
Pulsed Drain Current	$I_{DM}$	680	A
Single Pulse Avalanche Energy <sup>1</sup>	$E_{AS}$	900	mJ
Power Dissipation (Tc=25°C)	$P_D$	540	W
Power Dissipation (Tc=100°C)	$P_D$	216	W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	0.23	°C/W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

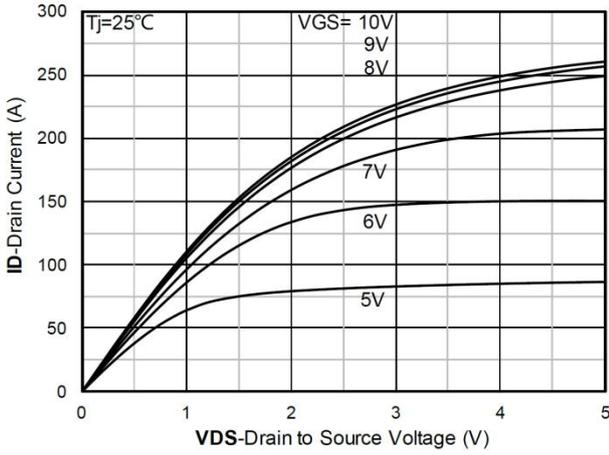
**Electrical characteristics (Ta=25°C, unless otherwise noted)**

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	250	-	-	V
Drain Cut-Off Current	$I_{DSS}$	$V_{DS}=200V, V_{GS}=0V, T_J=25^\circ C$	-	-	1	$\mu A$
Gate Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	2.6	3.0	3.4	V
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$	-	8.0	10.0	m $\Omega$
Gate Resistance	$R_G$	$V_{DS}=125V, V_{GS}=0V, f=1MHz$	-	7	-	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=125V, V_{GS}=0V, f=1MHz$	-	8560	-	pF
Output Capacitance	$C_{oss}$		-	600	-	
Reverse Transfer Capacitance	$C_{rss}$		-	24	-	
Total Gate Charge	$Q_g$	$V_{DS}=125V, V_{GS}=10V, I_D=20A$	-	120	-	nC
Gate-Source Charge	$Q_{gs}$		-	41	-	
Gate-Drain Charge	$Q_{gd}$		-	19	-	
Gate Plateau Voltage	$V_{plateau}$		-	4.95	-	
<b>Switching Characteristics</b>						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=125V, V_{GS}=10V, R_G=10\Omega, I_D=20A$	-	33	-	nS
Rise Time	$t_r$		-	47	-	
Turn-Off Delay Time	$t_{d(off)}$		-	64	-	
Fall Time	$t_f$		-	34	-	
<b>Drain-Source Body Diode Characteristics</b>						
Source-Drain Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	-	-	1.2	V
Maximum Body-Diode Continuous Current	$I_S$		-	-	170	A
Reverse Recovery Time	$T_{rr}$	$I_S=20A, di/dt=200A/\mu s, T_J=25^\circ C$	-	336	-	nS
Reverse Recovery Charge	$Q_{rr}$		-	1590	-	nC

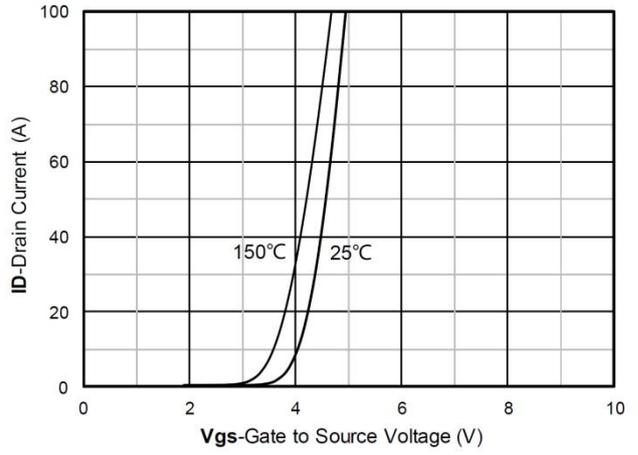
**Note :**

- The test condition is  $V_{DD}=50V, V_{GS}=10V, L=0.5mH, R_G=25\Omega$

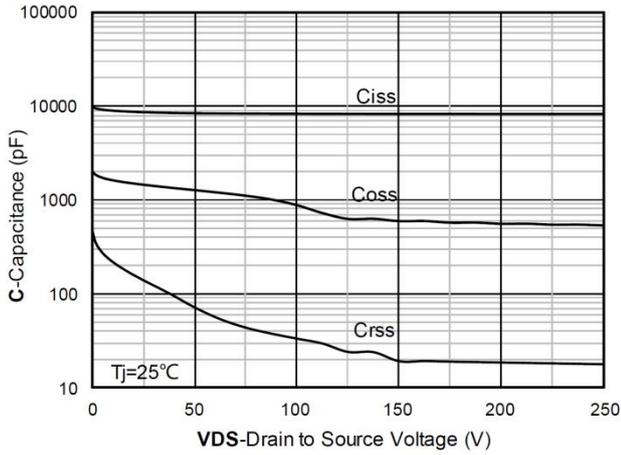
**Typical Characteristics**



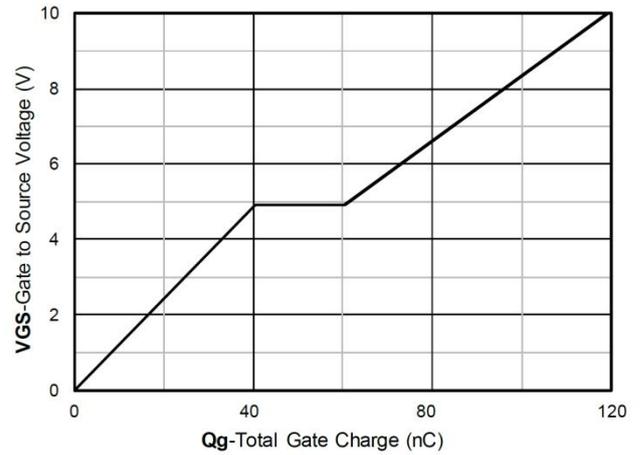
Output Characteristics



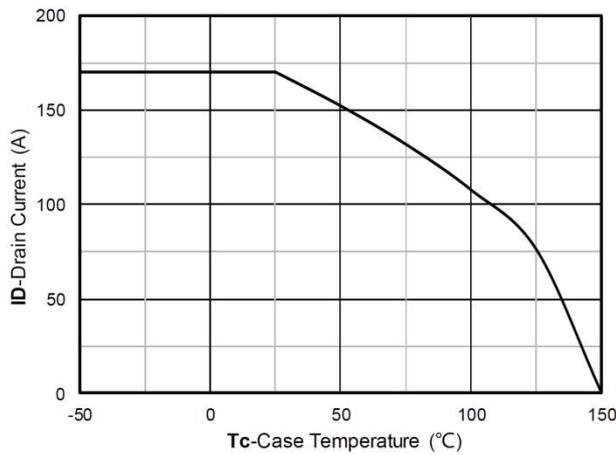
Transfer Characteristics



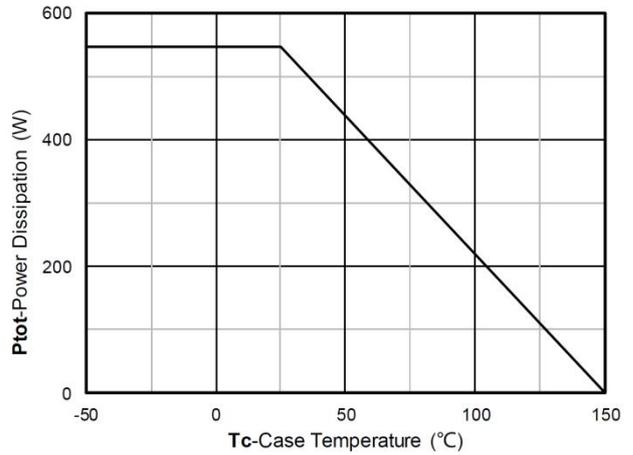
Capacitance Characteristics



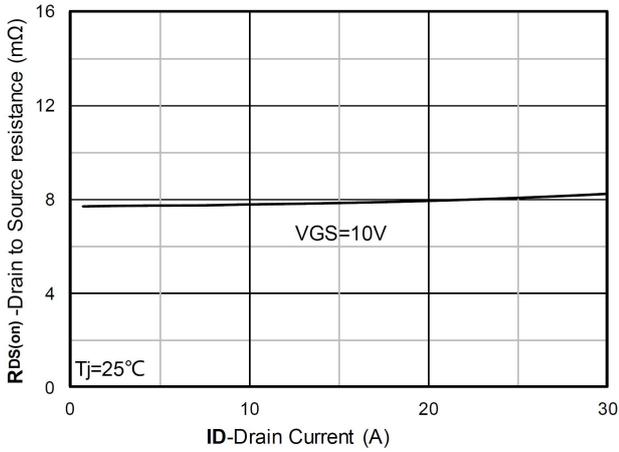
Gate Charge



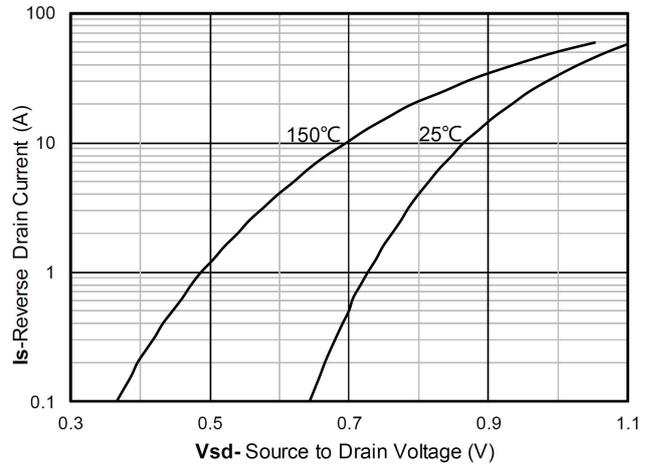
Current dissipation



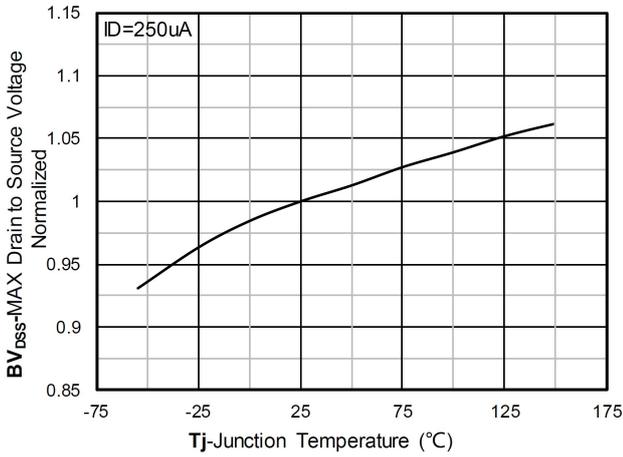
Power dissipation



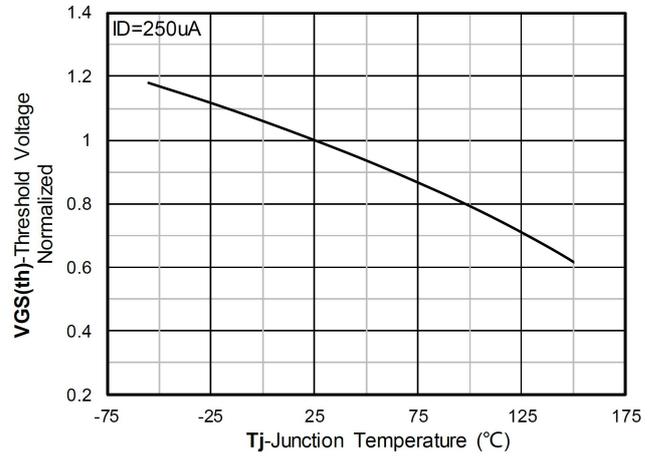
RDS(on) VS Drain Current



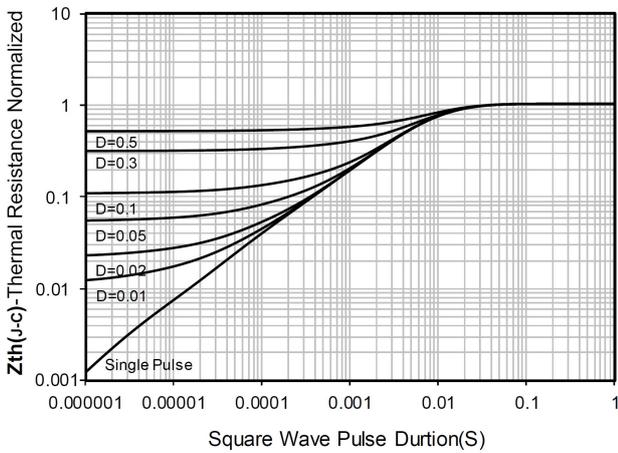
Forward characteristics of reverse diode



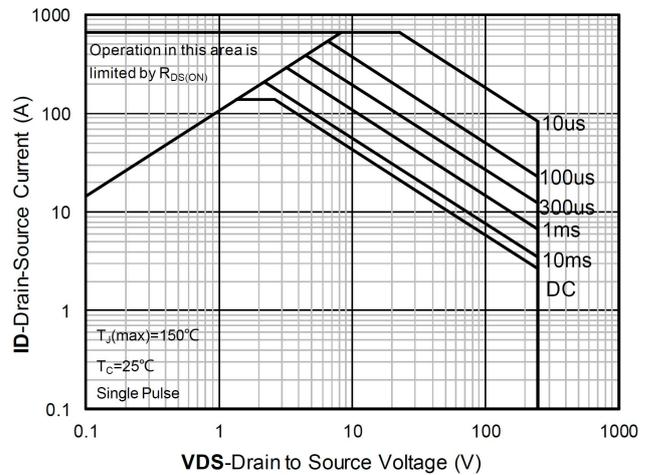
Normalized breakdown voltage



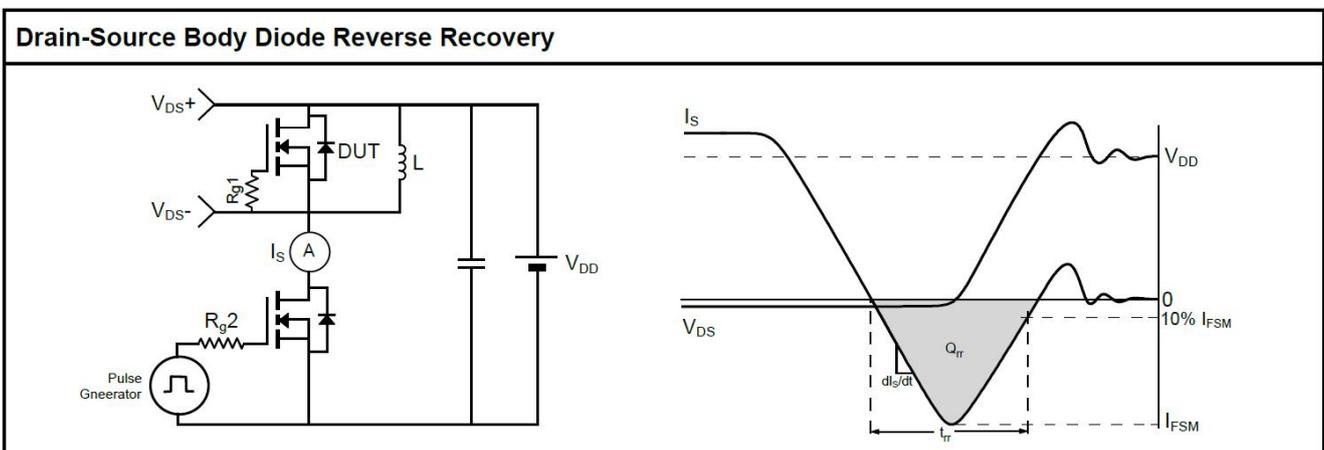
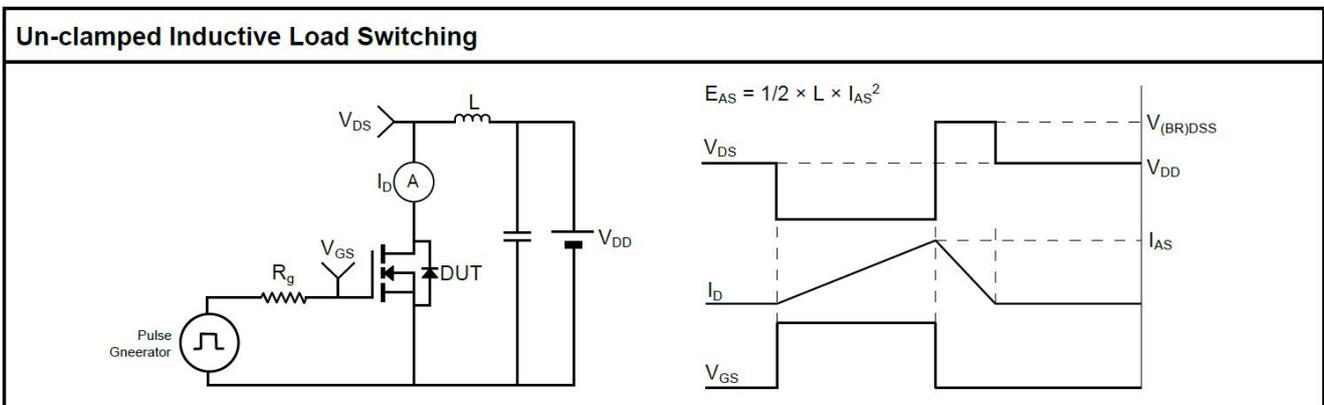
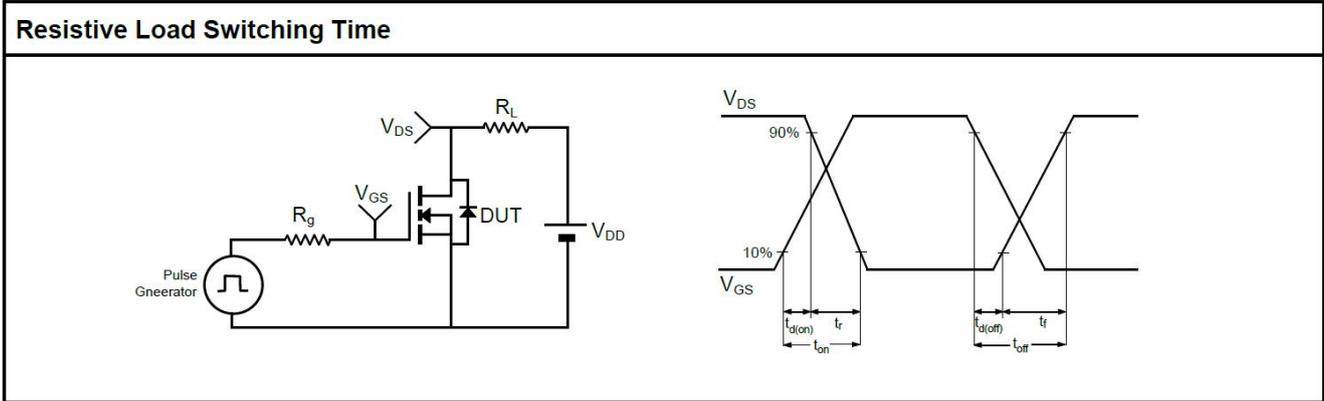
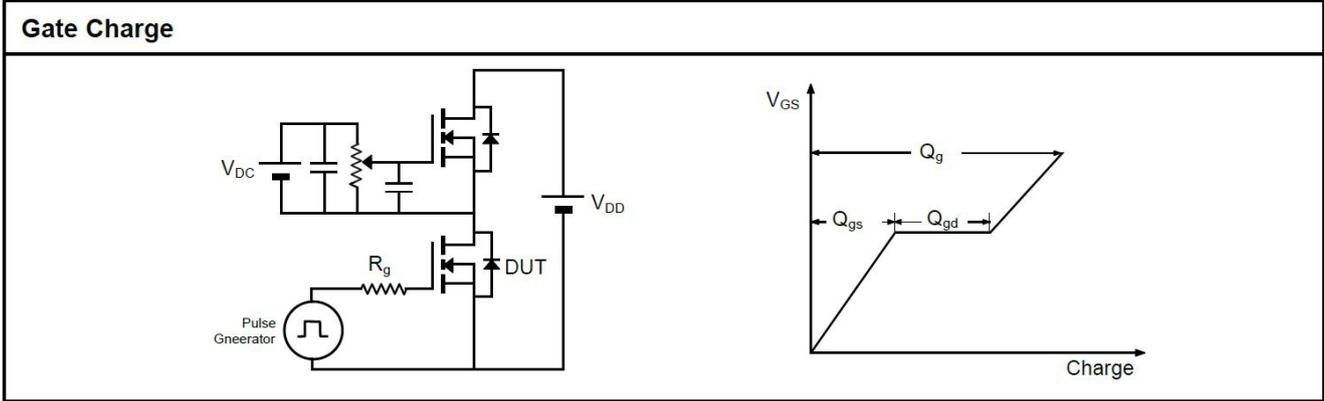
Normalized Threshold voltage

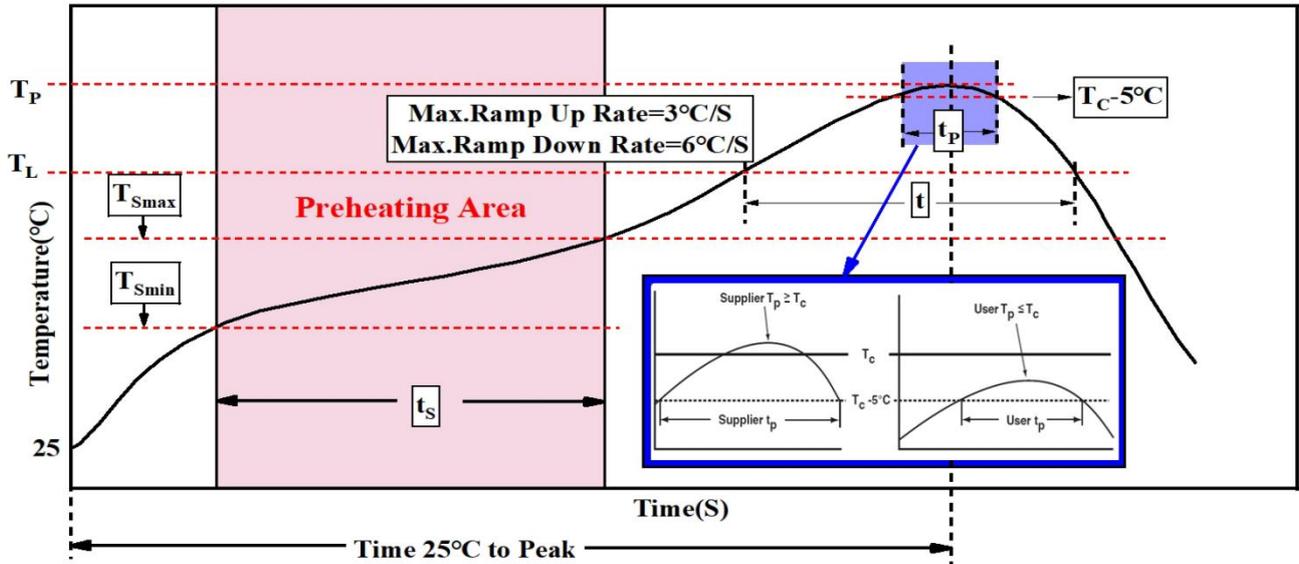


Maximum Transient Thermal Impedance



Safe Operation Area

**Test Circuit**


**Temperature Profile for IR Reflow Soldering(Pb-Free)**


Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{smin}$ )	100°C	150°C
Temperature max ( $T_{smax}$ )	150°C	200°C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217°C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature e ( $T_p$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_p$ )** within 5°C of the specified classification temperature ( $T_c$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum		

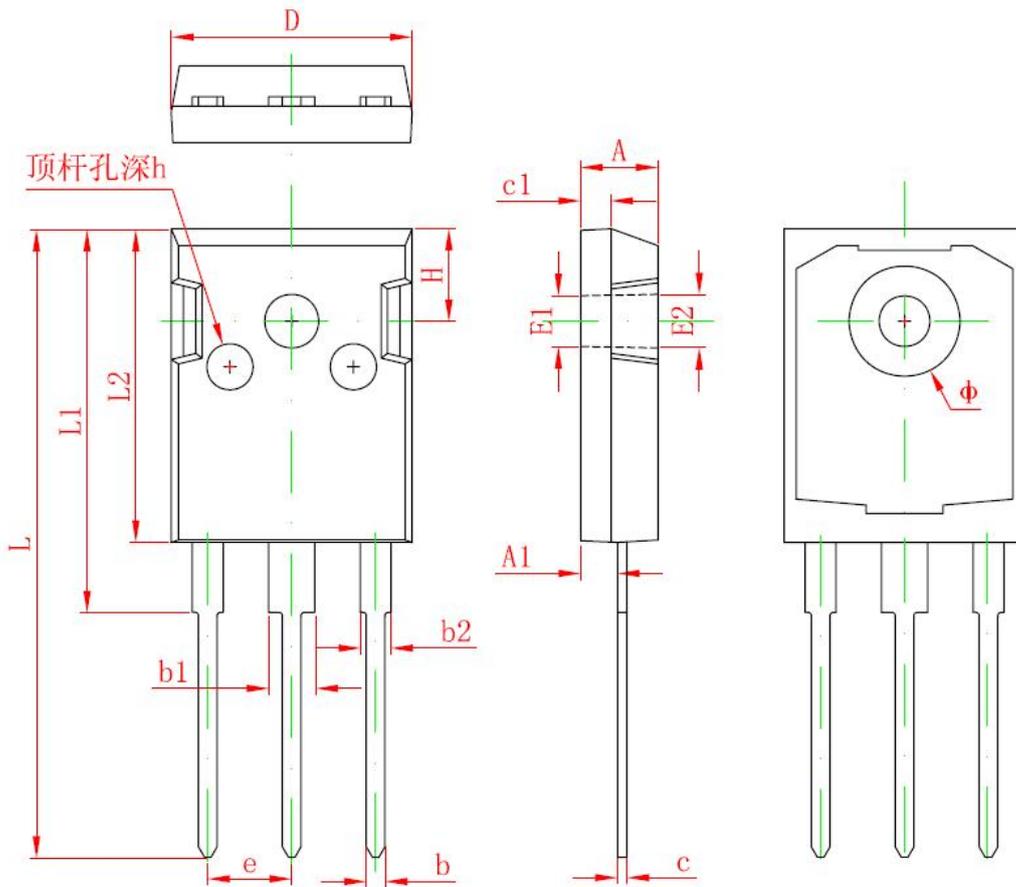
 Table 1. SnPb Eutectic Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

 Table 2. Pb-free Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

**TO-247 Package Information**



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.700	5.300
A1	2.100	2.600
b	1.000	1.400
b1	2.800	3.200
b2	1.800	2.200
c	0.500	0.700
c1	1.900	2.500
D	15.450	16.200
E1	3.500 REF.	
E2	3.600 REF.	
L	40.400	41.600
L1	24.750	25.750
L2	20.300	21.300
Φ	7.100	7.300
e	5.450 TYP.	
H	5.980 REF.	
h	0.000	0.300