

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
100V	0.9mΩ@10V	450A



合肥矽普半导体

Siliup Semiconductor Technology Co., Ltd

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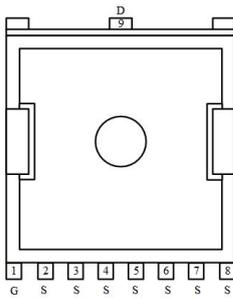
## Feature

- Fast Switching
- Low Gate Charge and R<sub>ds(on)</sub>
- 175°C Junction Temperature
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

## Applications

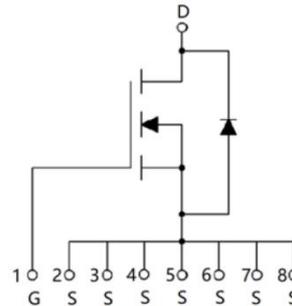
- DC-DC Converters
- Power management functions
- Synchronous-rectification applications

## Package

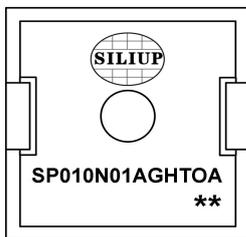


TOLL

## Circuit diagram



## Marking



SP010N01AGHTOA

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=Device Code

=Week Code

## Order Information

Device	Package	Unit/Tape
SP010N01AGHTOA	TOLL	2000

**Absolute maximum ratings (Ta=25°C, unless otherwise noted)**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	100	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Tc=25°C)	I <sub>D</sub>	450	A
Continuous Drain Current (Tc=100°C)	I <sub>D</sub>	295	A
Pulse Drain Current Tested	I <sub>DM</sub>	1800	A
Single pulsed avalanche energy <sup>1</sup>	E <sub>AS</sub>	2450	mJ
Power Dissipation (Tc=25°C)	P <sub>D</sub>	446	W
Power Dissipation (Tc=100°C)	P <sub>D</sub>	223	W
Thermal Resistance Junction-to-Case	R <sub>θJC</sub>	0.34	°C/W
Thermal Resistance Junction-to-Ambient	R <sub>θJA</sub>	40	°C/W
Storage Temperature Range	T <sub>STG</sub>	-55 to 175	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to 175	°C

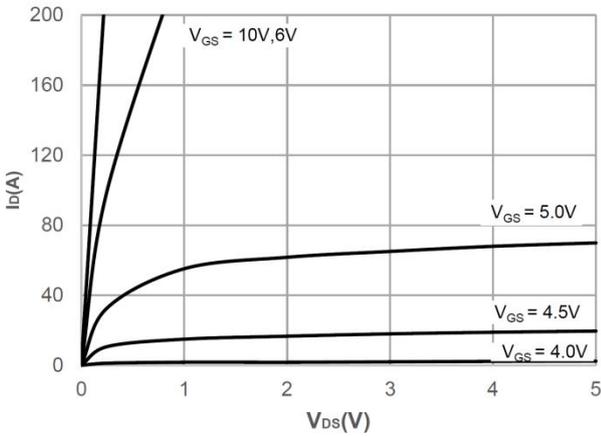
**Electrical characteristics (Ta=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	105	110	-	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	-	-	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.5	3.0	3.5	V
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =50A	-	0.9	1.1	mΩ
Gate Resistance	R <sub>G</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	-	4.1	-	Ω
<b>Dynamic characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	-	12142	-	pF
Output Capacitance	C <sub>oss</sub>		-	2231	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	47	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =125A	-	218	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	66	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	57	-	
Gate Plateau Voltage	V <sub>plateau</sub>		-	4.8	-	
<b>Switching Characteristics</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, V <sub>GS</sub> =10V, R <sub>G</sub> =1.6Ω, I <sub>D</sub> =125A	-	43	-	nS
Rise Time	t <sub>r</sub>		-	71	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	149	-	
Fall Time	t <sub>f</sub>		-	89	-	
<b>Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	-	-	1.2	V
Diode Continuous Current	I <sub>S</sub>		-	-	450	A
Reverse recover time	T <sub>rr</sub>	I <sub>S</sub> =20A, di/dt=100A/us, T <sub>J</sub> =25°C	-	136	-	nS
Reverse recovery charge	Q <sub>rr</sub>		-	380	-	nC

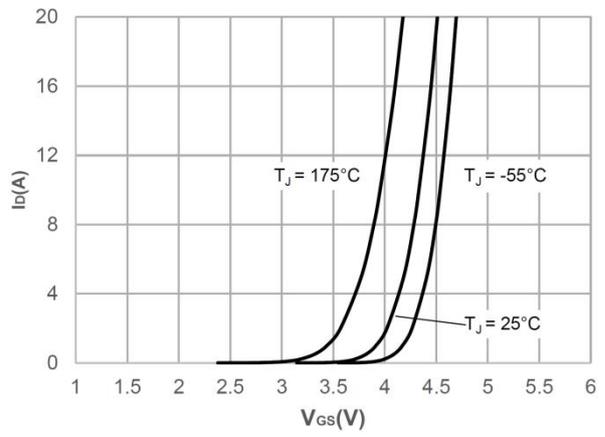
**Note :**

- The test condition is V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω

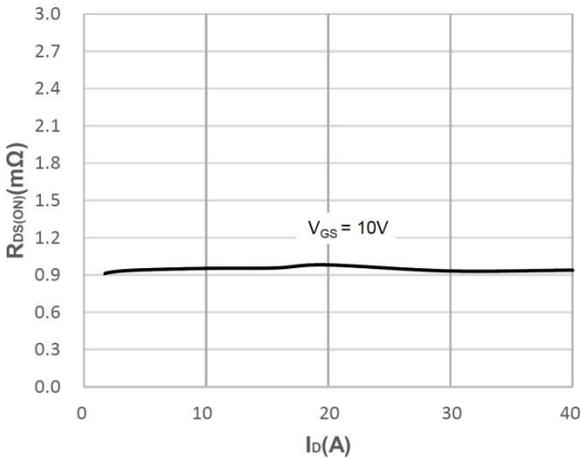
**Typical Characteristics**



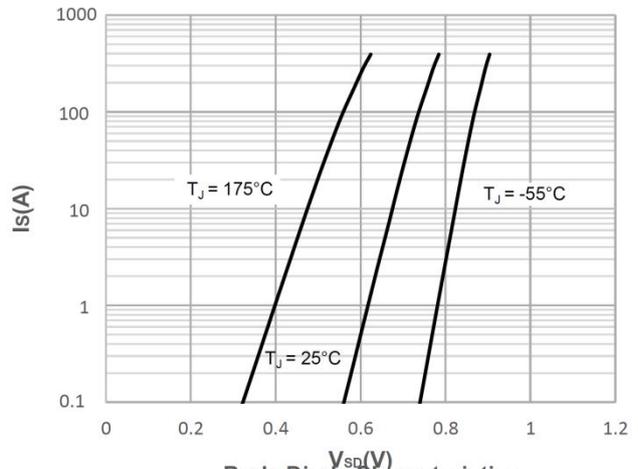
Output Characteristics



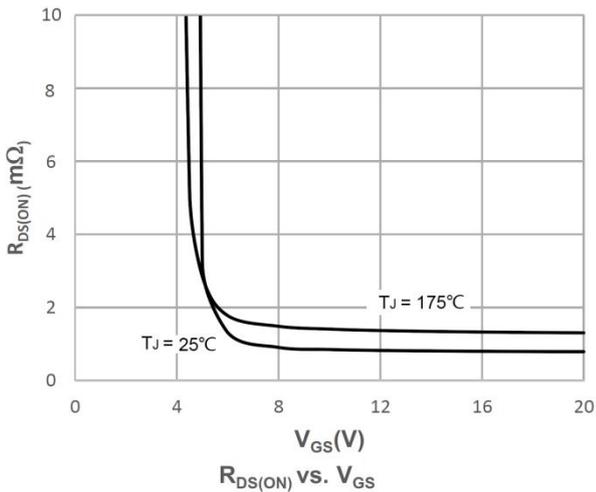
Typical Transfer Characteristics



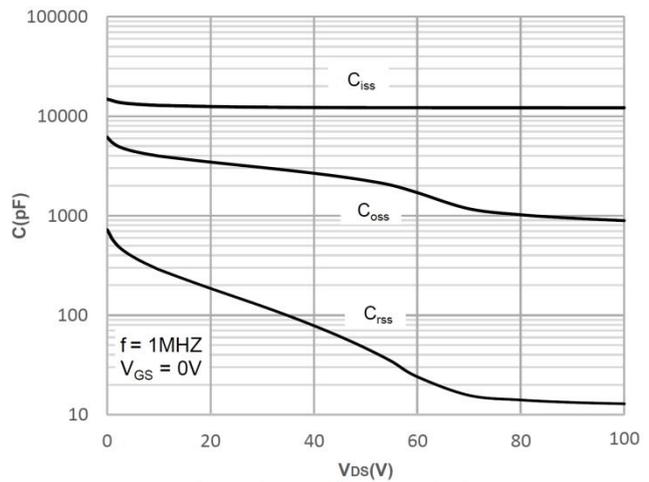
On-resistance vs. Drain Current



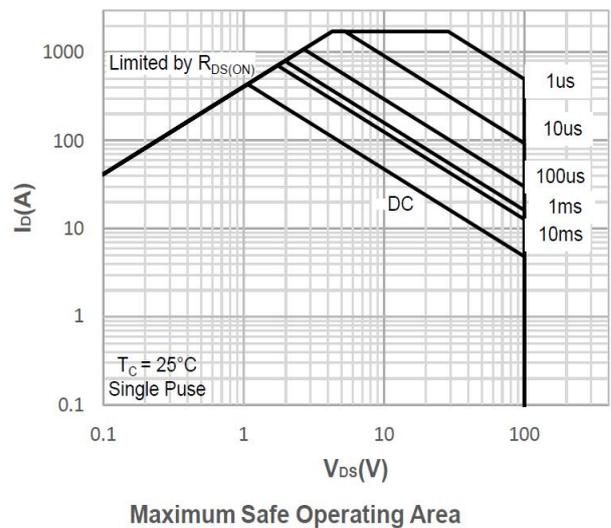
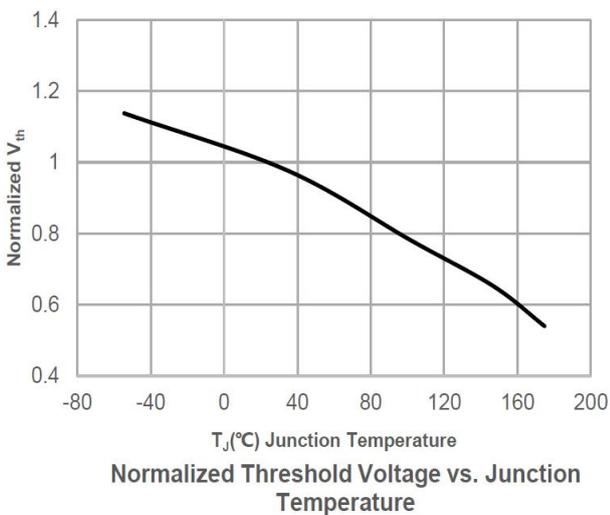
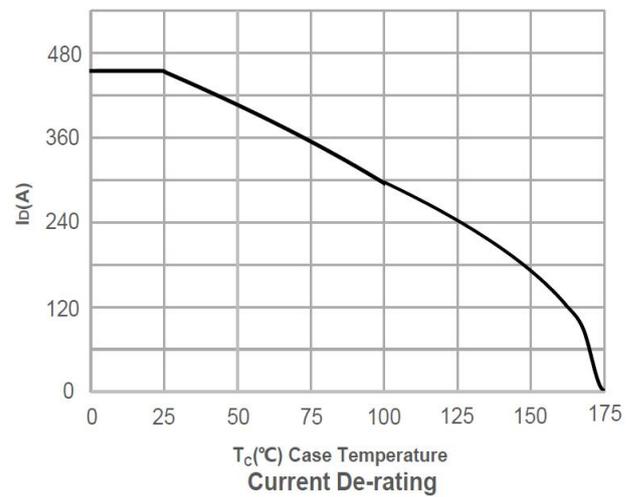
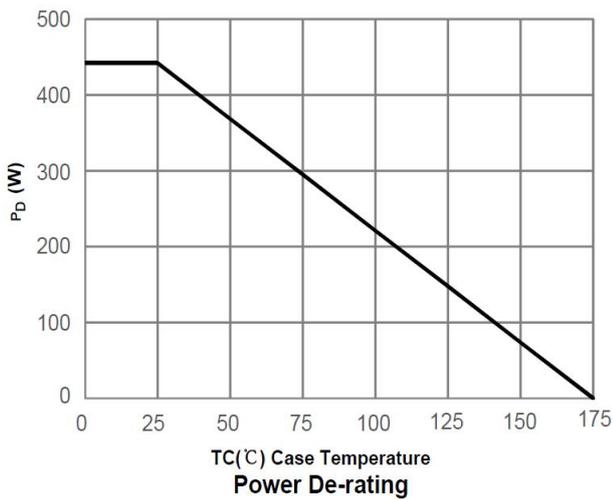
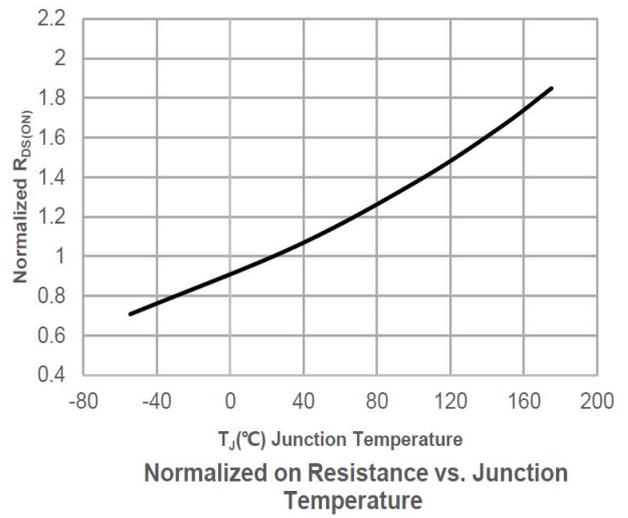
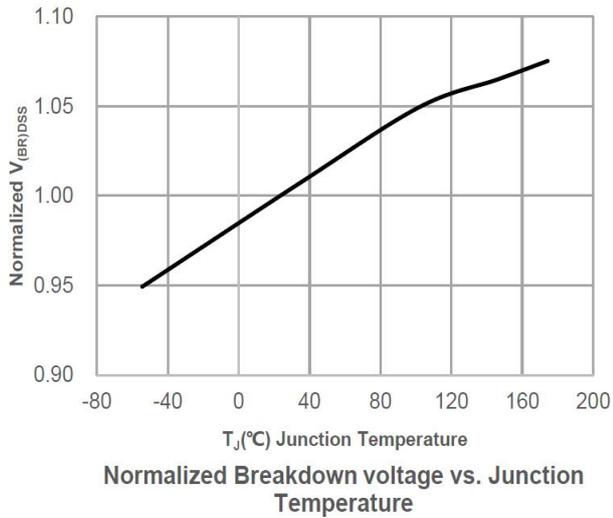
Body Diode Characteristics

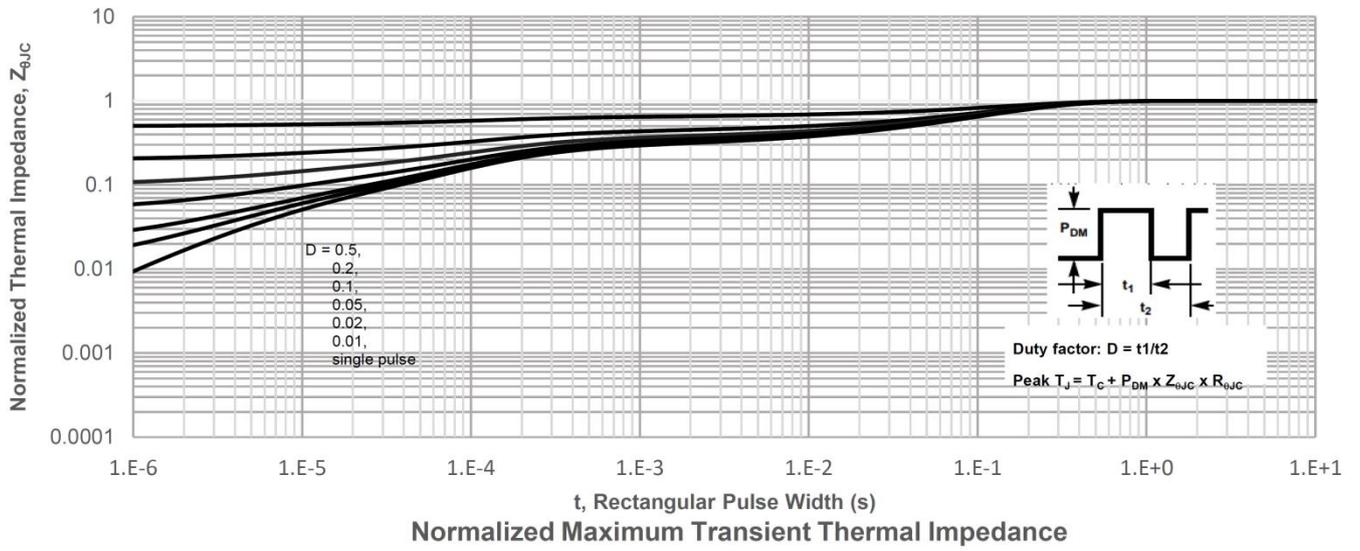


$R_{DS(ON)}$  vs.  $V_{GS}$

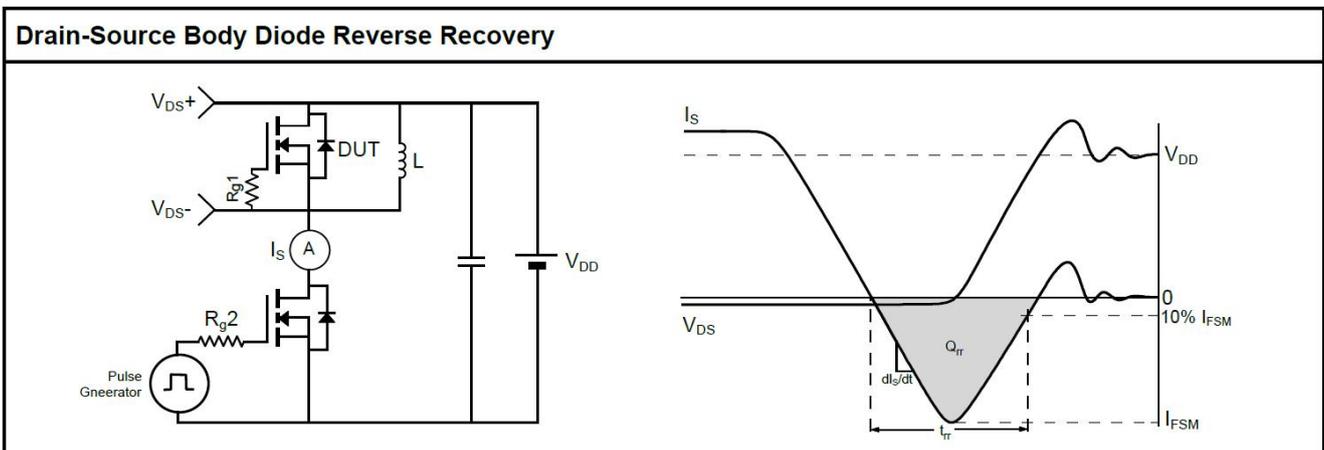
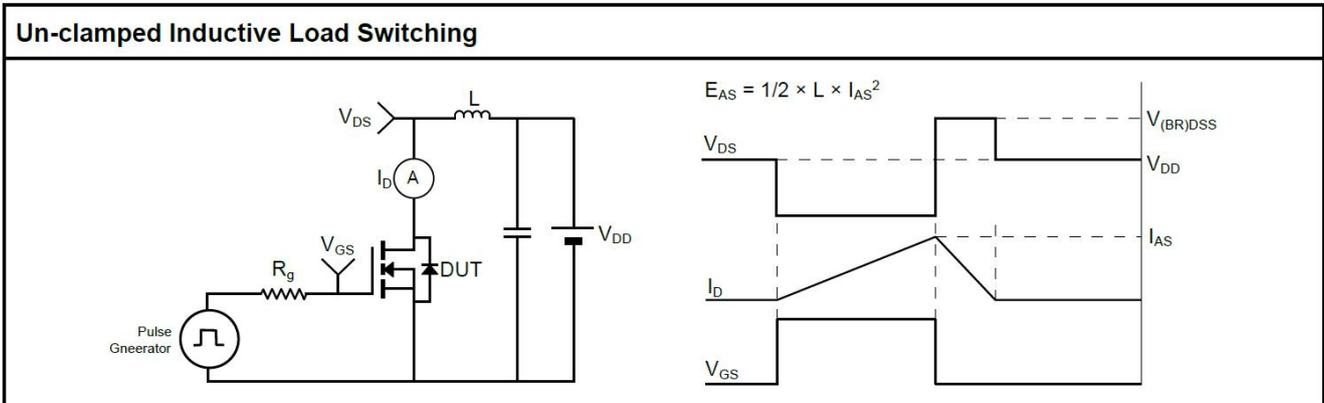
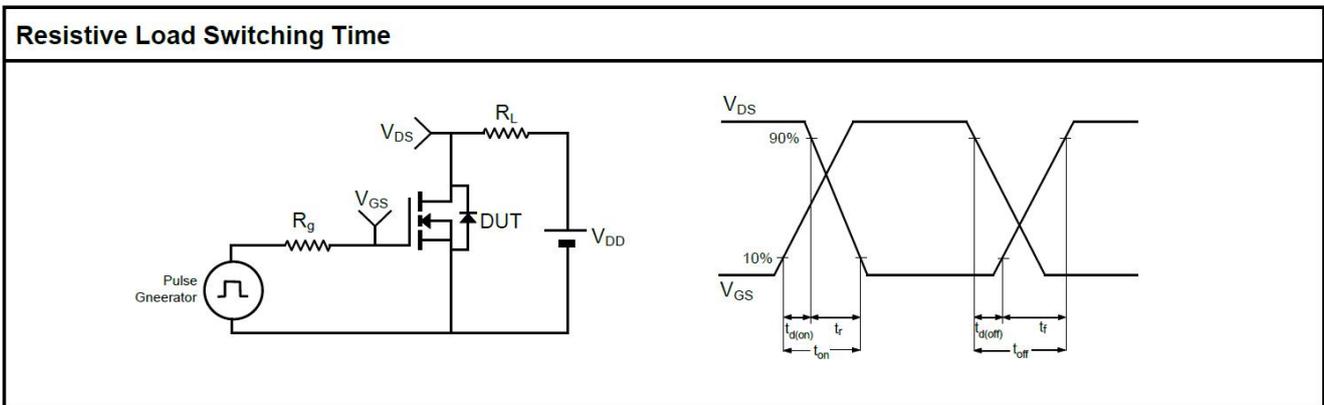
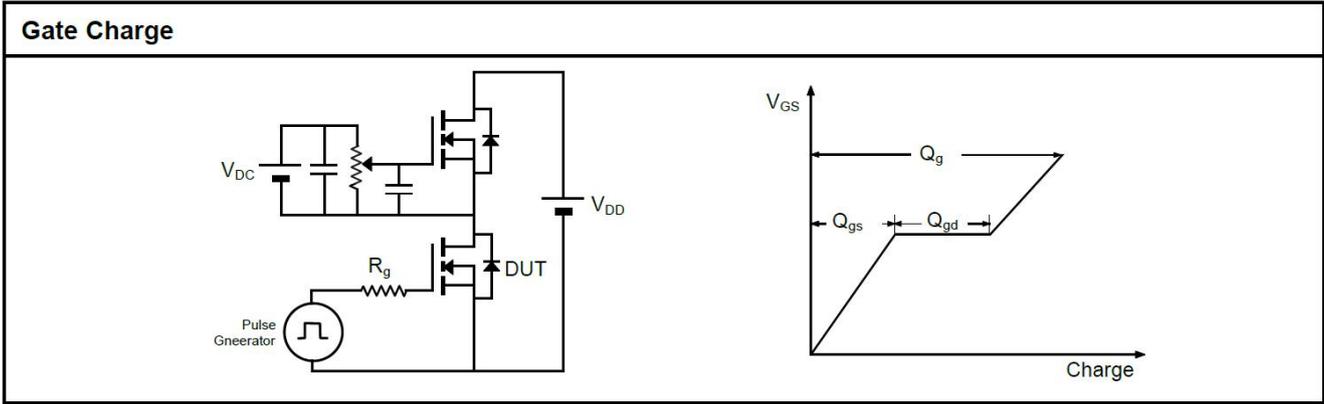


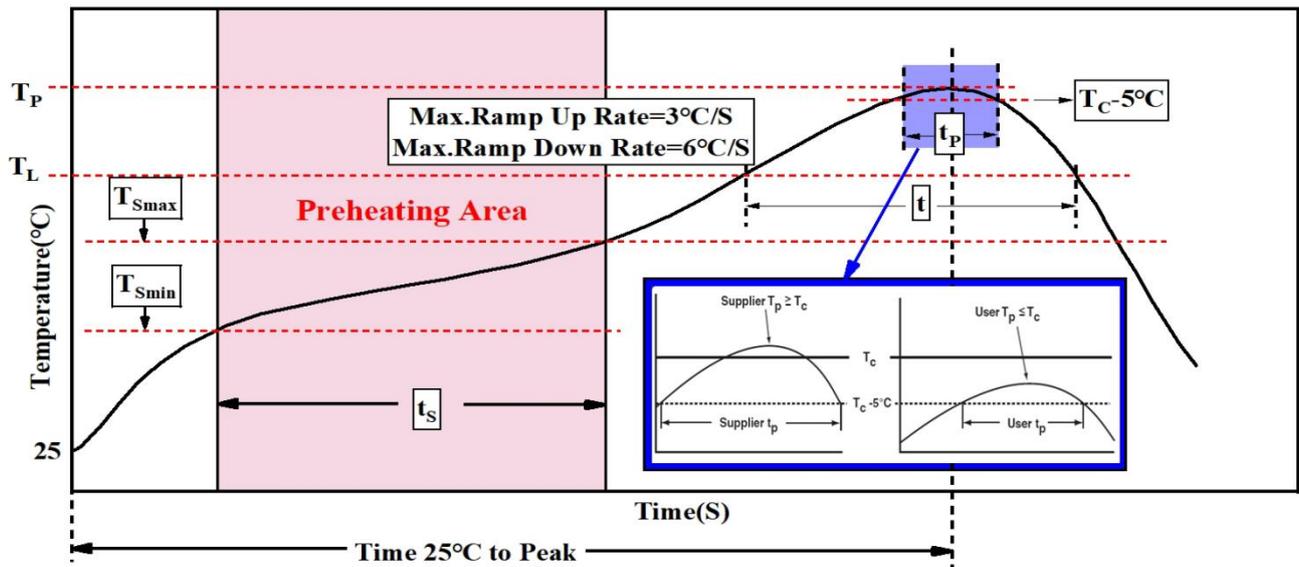
Capacitance Characteristics





## Test Circuit



**Temperature Profile for IR Reflow Soldering**


Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{smin}$ )	100°C	150°C
Temperature max ( $T_{smax}$ )	150°C	200°C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217°C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature e ( $T_p$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_p$ )** within 5°C of the specified classification temperature ( $T_c$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum		

 Table 1. SnPb Eutectic Process – Classification Temperatures ( $T_c$ )

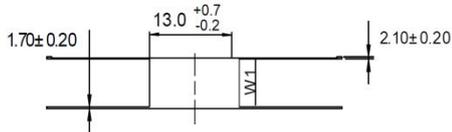
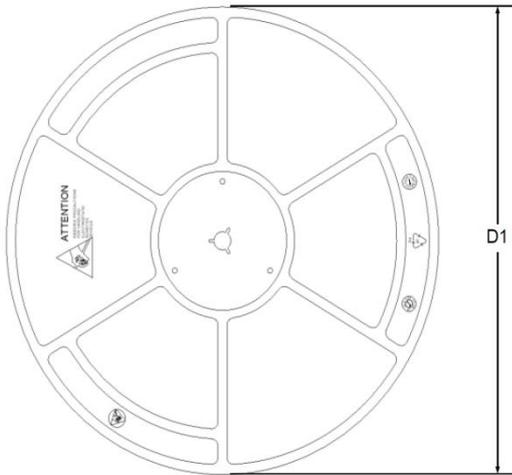
Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

 Table 2. Pb-free Process – Classification Temperatures ( $T_c$ )

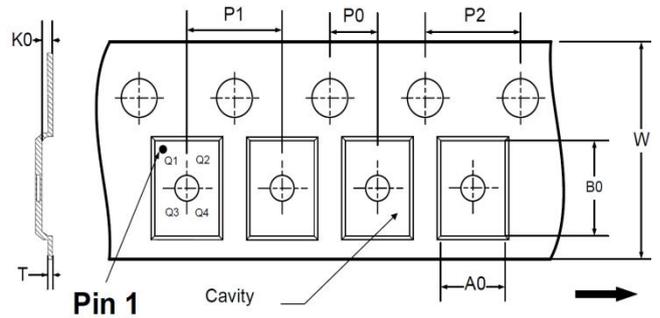
Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

### TOLL Reel Information

REEL DIMENSIONS



TAPE DIMENSIONS

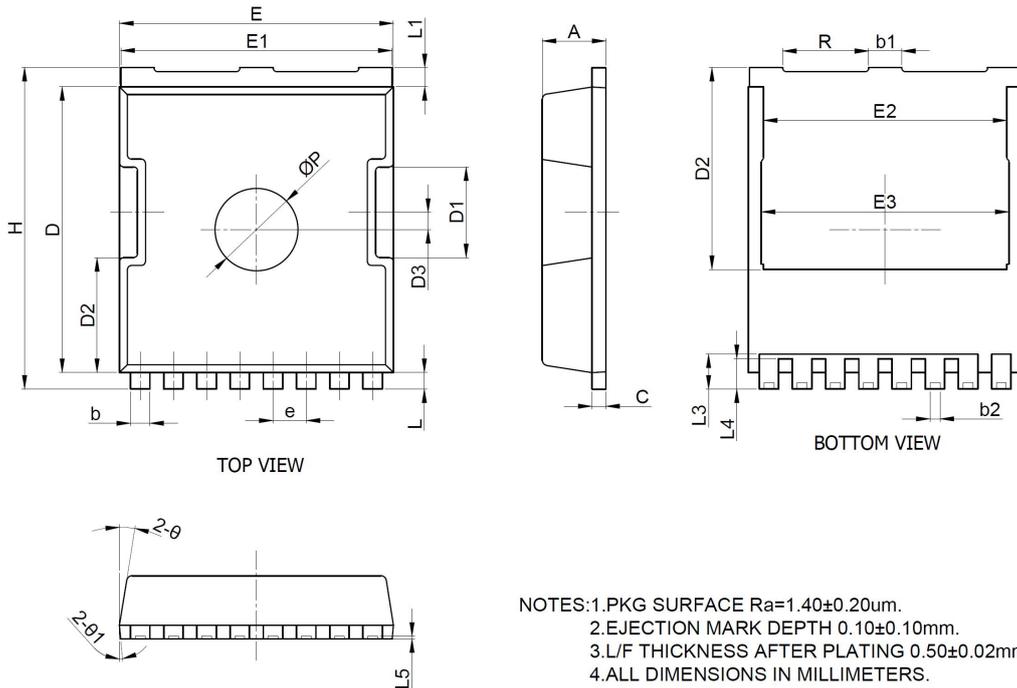


- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- K0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P0: Pitch between successive cavity centers and sprocket hole
- P1: Pitch between successive cavity centers
- P2: Pitch between sprocket hole
- T: Tape material thickness
- D1: Reel Diameter
- W1: Reel Width

		DIMENSIONS								(Unit: mm)	
Reel	D1	W1								Material	
	330	24.4								Hips	
Tape	P0	P1	P2	W	A0	B0	K0	T	Pin 1 Quadrant	Material	
	2	4	12	24	10.3	12.1	2.6	0.35	Q1	PC	

All dimensions are nominal

## TOLL Package Outline Dimensions



- NOTES: 1.PKG SURFACE Ra=1.40±0.20um.  
 2.EJECTION MARK DEPTH 0.10±0.10mm.  
 3.L/F THICKNESS AFTER PLATING 0.50±0.02mm.  
 4.ALL DIMENSIONS IN MILLIMETERS.

Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	2.20	2.30	2.40
b	0.60	0.70	0.80
b1	1.10	1.20	1.30
b2	0.36 REF.		
C	0.40	0.50	0.60
D	10.30	10.40	10.50
D1	3.20	3.30	3.40
D2	4.08	4.18	4.28
D3	0.53	0.63	0.73
D4	7.35 REF.		
E	9.80	9.9	10.00
E1	9.70	9.80	9.90
E2	8.80 REF.		
E3	8.95 REF.		
e	1.20 BSC.		
H	11.5	11.7	11.90
L	0.5	0.6	0.7
L1	0.60	0.7	0.80
L2	0.10 REF.		
L3	1.27 REF.		
L4	1.10 REF.		
P	2.00	3.00	4.00
R	3.00	3.10	3.20
θ	7°	9°	11°
θ1	3°	5°	7°