

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
85V	0.9mΩ@10V	440A



合肥矽普半导体

Siliup Semiconductor Technology Co.Ltd

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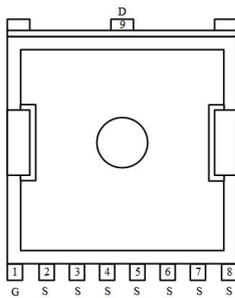
Feature

- Fast Switching
- Low Gate Charge and R_{DS(on)}
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

Applications

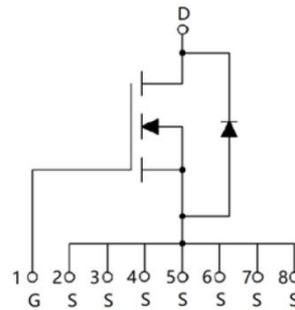
- Power switching application
- DC-DC Converter
- Power Management

Package

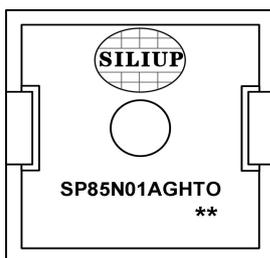


TOLL

Circuit diagram



Marking



SP85N01AGHTO : Product code
 ** : Week code

Order Information

Device	Package	Unit/Tape
SP85N01AGHTO	TOLL	2000

Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	85	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current1 (Tc=25°C)	I _D	440	A
Continuous Drain Current1 (Tc=100°C)	I _D	293	A
Pulsed Drain Current	I _{DM}	1760	A
Single Pulse Avalanche Energy ¹	E _{AS}	3136	mJ
Power Dissipation (Tc=25°C)	P _D	550	W
Power Dissipation (Tc=100°C)	P _D	220	W
Thermal Resistance Junction-to-Case	R _{θJC}	0.23	°C/W
Storage Temperature Range	T _{STG}	-55 to 150	°C
Operating Junction Temperature Range	T _J	-55 to 150	°C

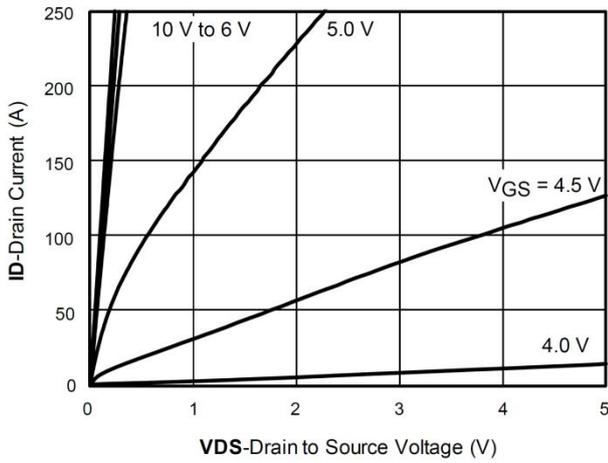
Electrical characteristics (Ta=25°C, unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA , V _{GS} =0V	85	90	-	V
Drain Cut-Off Current	I _{DSS}	V _{DS} =68V , V _{GS} =0V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{GS} =±20V , V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.6	3.0	3.4	V
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} =10V , I _D =20A	-	0.9	1.15	mΩ
Gate Resistance	R _G	V _{DS} =40V , V _{GS} =0V , f=1.0MHz	-	1.9	-	Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =40V , V _{GS} =0V , f=1.0MHz	-	14500	-	pF
Output Capacitance	C _{oss}		-	2680	-	
Reverse Transfer Capacitance	C _{rss}		-	171	-	
Total Gate Charge	Q _g	V _{DS} =40V , V _{GS} =10V , I _D =50A	-	240	-	nC
Gate-Source Charge	Q _{gs}		-	55	-	
Gate-Drain Charge	Q _{gd}		-	61	-	
Gate Plateau Voltage	V _{plateau}		-	4.5	-	V
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	V _{GS} =10V , V _{DS} =40V , I _D =50A , R _G =4.5Ω	-	42	-	nS
Rise Time	t _r		-	125	-	
Turn-Off Delay Time	t _{d(off)}		-	118	-	
Fall Time	t _f		-	134	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S =1A , V _{GS} =0V , T _J =25°C	-	-	1.2	V
Maximum Body-Diode Continuous Current	I _S		-	-	440	A
Reverse Recovery Time	T _{rr}	I _S =50A , di/dt=100A/us , T _J =25°C	-	125	-	nS
Reverse Recovery Charge	Q _{rr}		-	366	-	nC

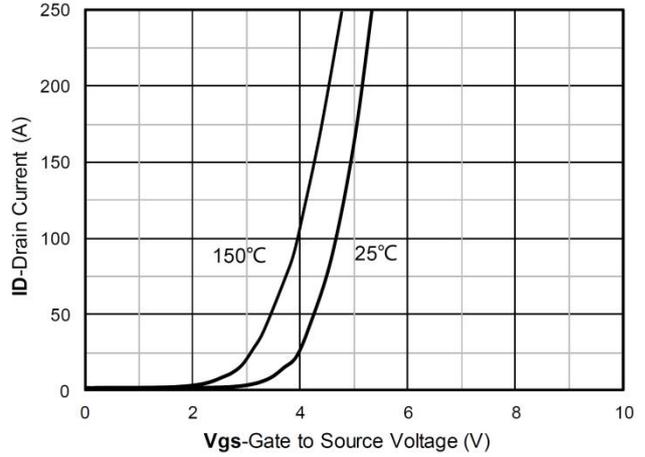
Note :

 1. The test condition is V_{DD}=40V, V_{GS}=10V, L=0.5mH, R_G=25Ω

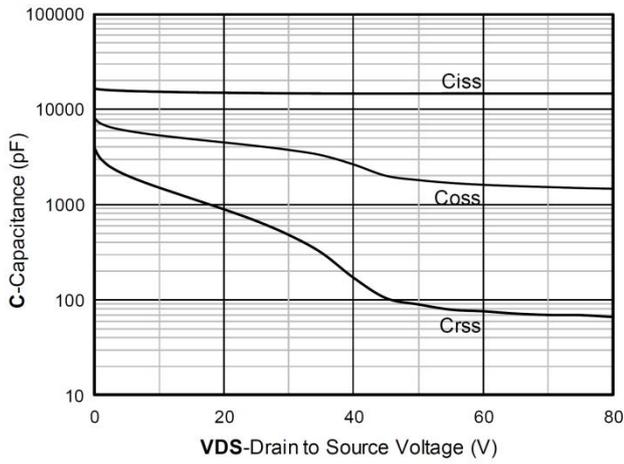
Typical Characteristics



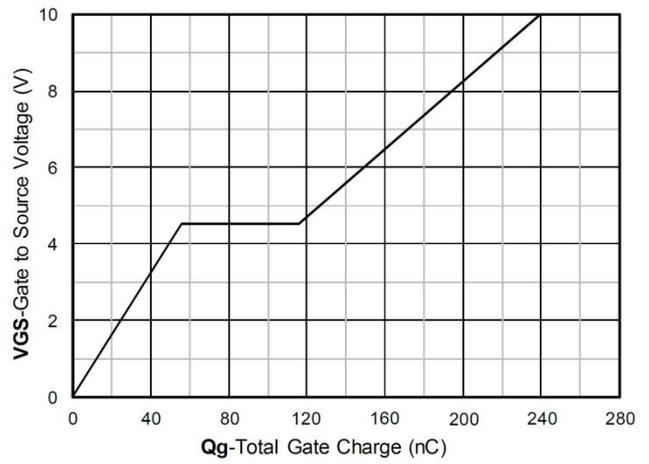
Output Characteristics



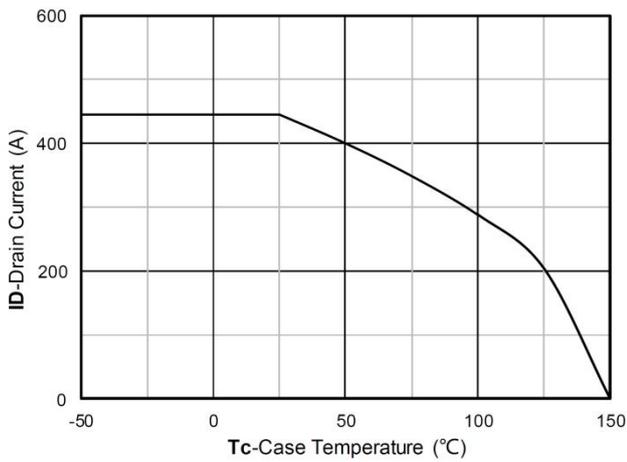
Transfer Characteristics



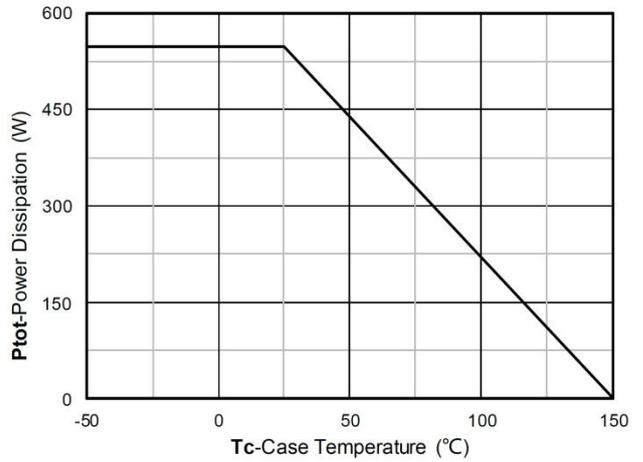
Capacitance Characteristics



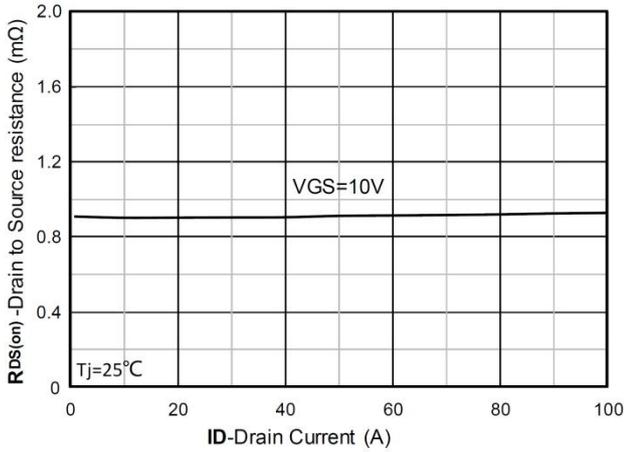
Gate Charge



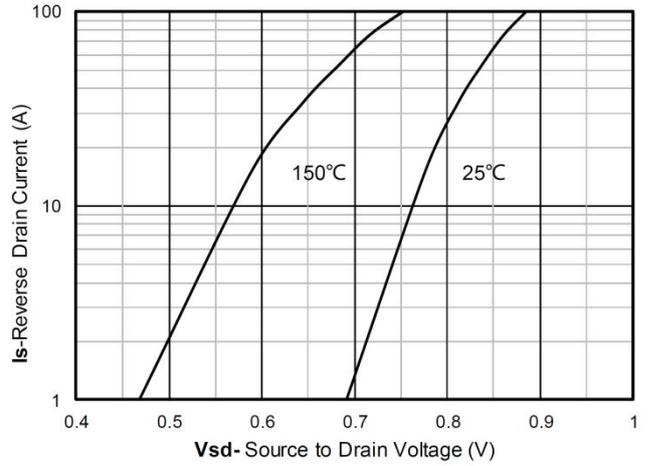
Current dissipation



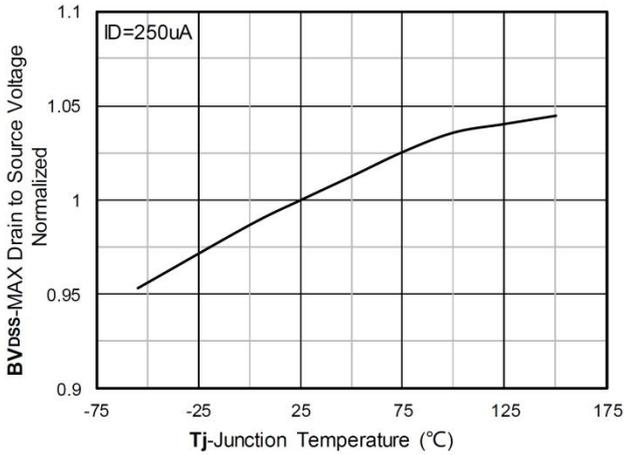
Power dissipation



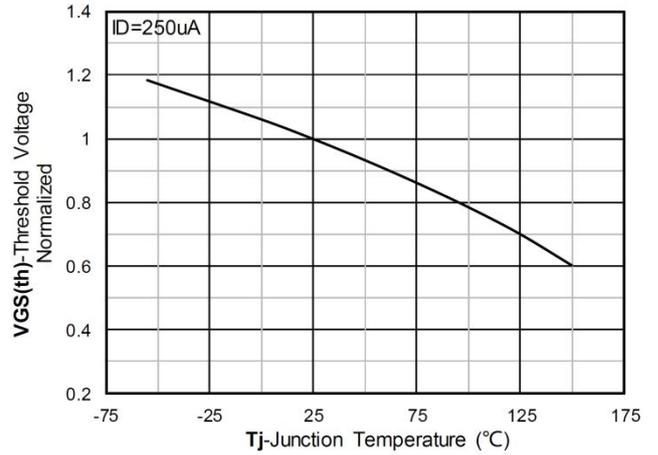
RDS(on) VS Drain Current



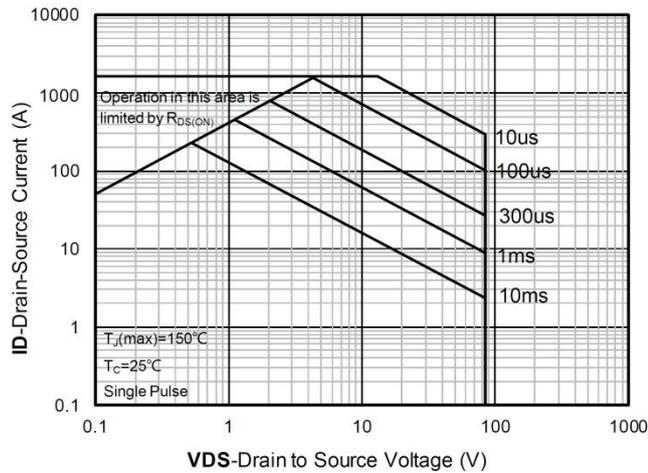
Forward characteristics of reverse diode



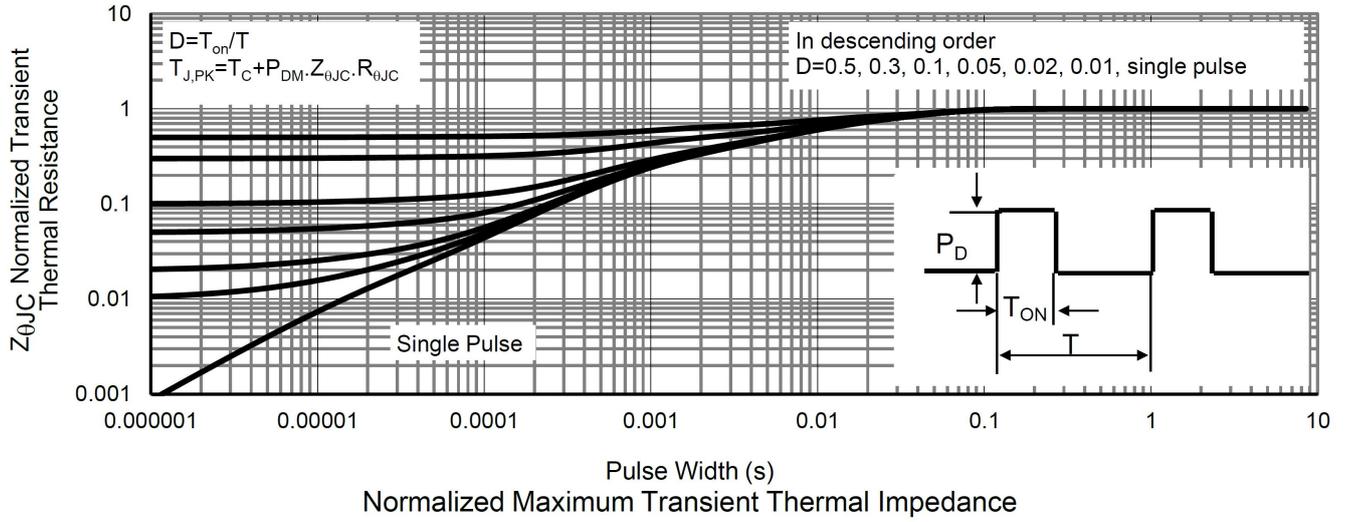
Normalized breakdown voltage



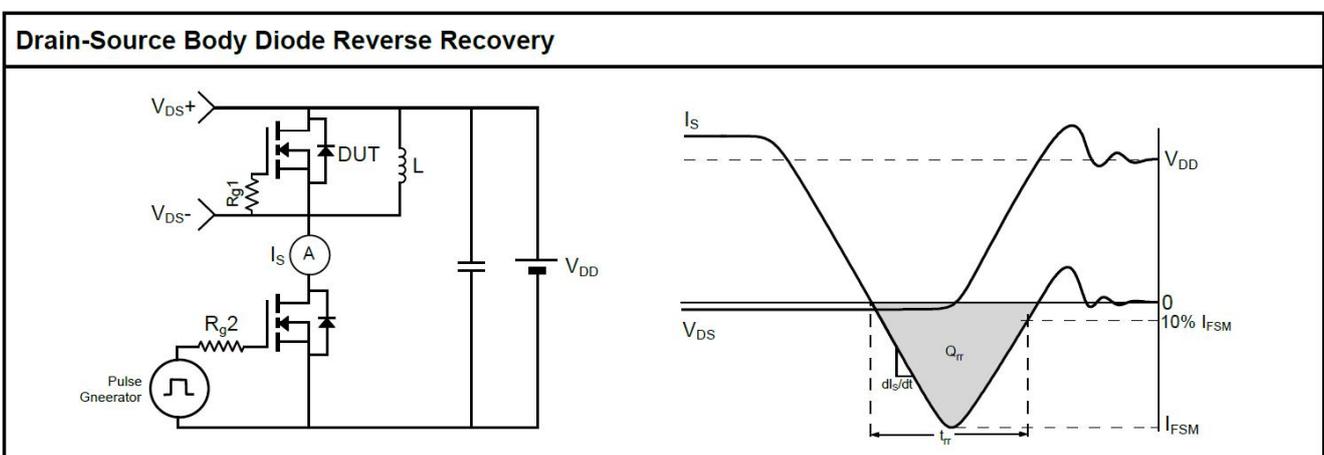
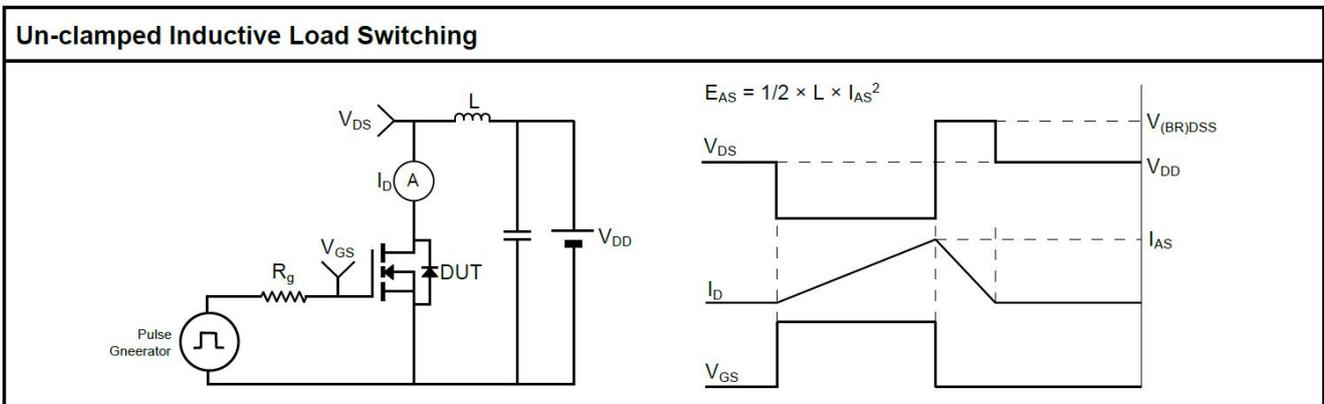
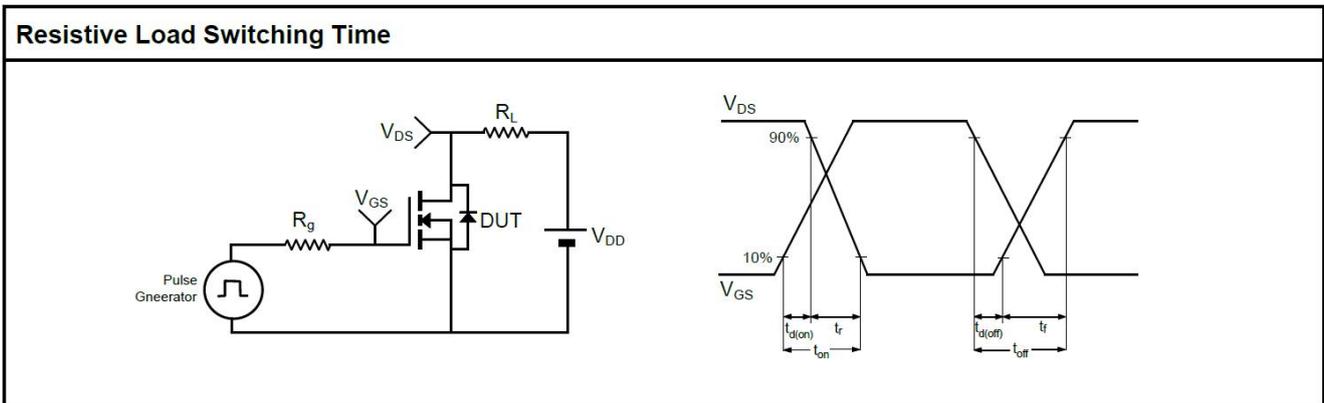
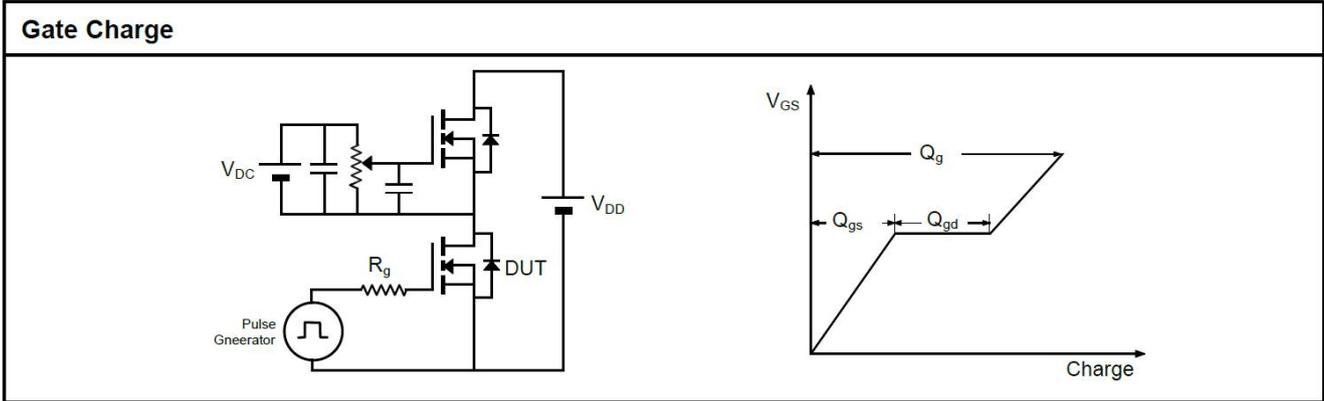
Normalized Threshold voltage



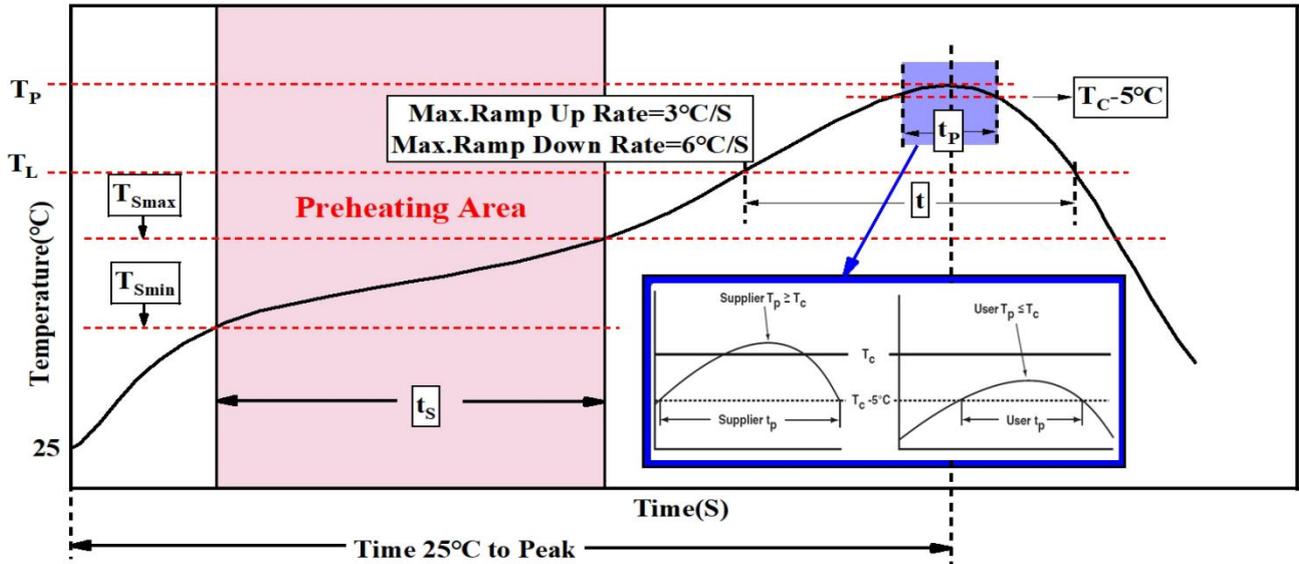
Safe Operation Area



Test Circuit



Temperature Profile for IR Reflow Soldering



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T _{smin})	100°C	150°C
Temperature max (T _{smax})	150°C	200°C
Time (T _{smin} to T _{smax}) (t _s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T _{smax} to T _p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T _L)	183 °C	217°C
Time at liquidous (t _L)	60-150 seconds	60-150 seconds
Peak package body Temperature e (T _p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t _p)** within 5°C of the specified classification temperature (T _c)	20** seconds	30** seconds
Average ramp-down rate (T _p to T _{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T _p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t _p) is defined as a supplier minimum and a user maximum		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

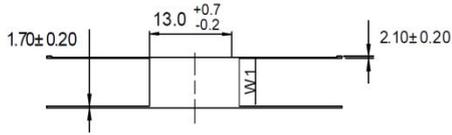
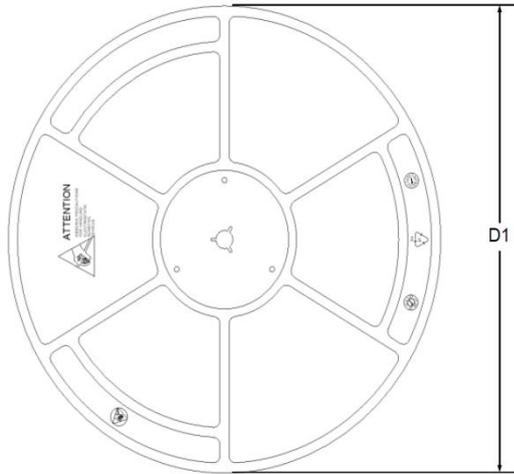
Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

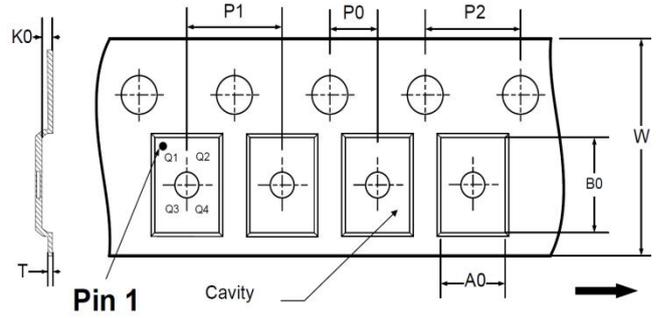
Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

TOLL Reel Information

REEL DIMENSIONS



TAPE DIMENSIONS

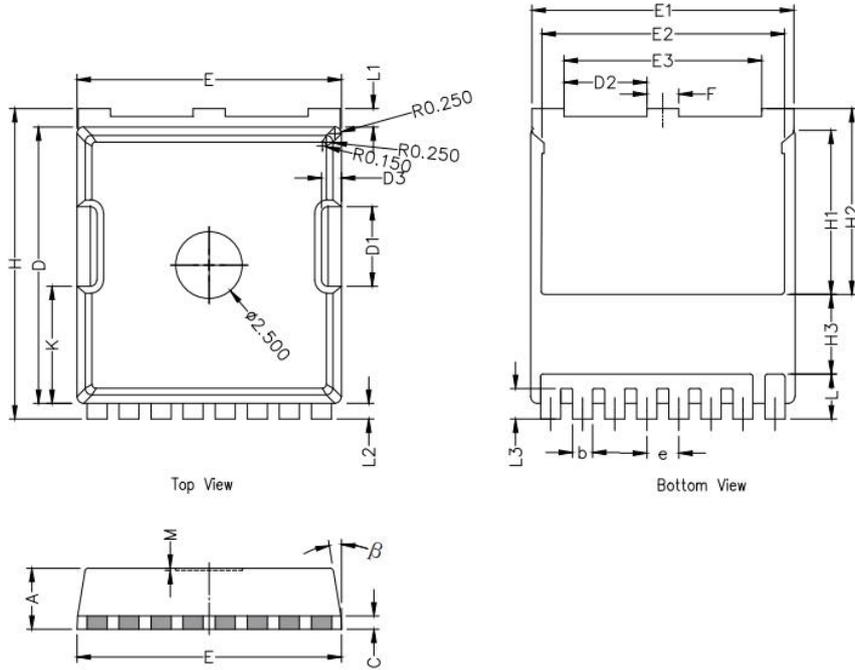


- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- K0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P0: Pitch between successive cavity centers and sprocket hole
- P1: Pitch between successive cavity centers
- P2: Pitch between sprocket hole
- T: Tape material thickness
- D1: Reel Diameter
- W1: Reel Width

DIMENSIONS										(Unit: mm)
Reel	D1	W1								Material
	330	24.4								Hips
Tape	P0	P1	P2	W	A0	B0	K0	T	Pin 1 Quadrant	Material
	2	4	12	24	10.3	12.1	2.6	0.35	Q1	PC

All dimensions are nominal

TOLL Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	2.15	2.30	2.45
b	0.60	0.75	0.90
C	0.40	0.50	0.60
D	10.18	10.38	10.58
D1	3.15	3.3	3.45
E	9.70	9.90	10.10
E1	9.65	9.80	9.95
E2	7.90	--	9.15
E3	7.20	--	8.40
e	1.20 BSC		
F	1.05	1.20	1.35
H	11.45	11.70	11.95
H2	6.75	7.00	7.3
H3	3.00 BSC		
L	1.20	--	2.10
L1	0.50	0.75	0.90
L2	0.45	0.6	0.75
M	0.08 REF.		
β	8°	10°	12°