

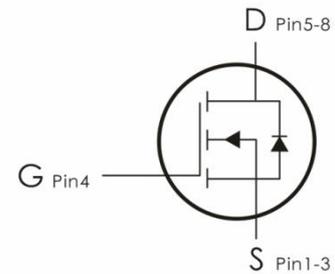
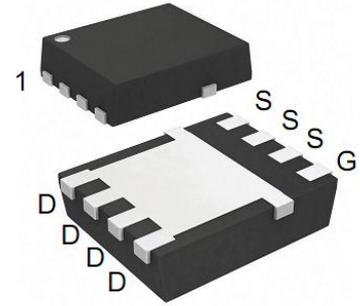
## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge.

It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=30V, I_D=140A, R_{DS(on)} < 2m\ \Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low  $R_{DS(on)}$ .
- 5) Excellent package for good heat dissipation.



## Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
NC002NG	C002N	DFN5*6-8	5000 pcs/Reel

## Absolute Maximum Ratings: ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	140	A
	Continuous Drain Current- $T_C=100^\circ\text{C}$	88	
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	515	
$P_D$	Power Dissipation	82	W
$E_{AS}$	Single pulse avalanche energy <sup>2</sup>	289	mJ
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55--+150	$^\circ\text{C}$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.52	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>3</sup>	36	$^\circ\text{C}/\text{W}$

**Electrical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	30	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=30V$	---	---	1	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	$\pm 100$	nA
<b>On Characteristics</b> <small>(Note3)</small>						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	1	1.6	2.5	V
$R_{DS(on)}$	Drain-Source On Resistance <sup>4</sup>	$V_{GS}=10V, I_D=30A$	---	1.5	2	m $\Omega$
		$V_{GS}=4.5V, I_D=20A$	---	2.5	3.2	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	5064	---	pF
$C_{oss}$	Output Capacitance		---	573	--	
$C_{rss}$	Reverse Transfer Capacitance		---	471	---	
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{DS}=15V, I_D=30A,$ $R_G=3\ \Omega, V_{GS}=10V$	---	15	---	ns
$t_r$	Rise Time		---	29	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	53	---	ns
$t_f$	Fall Time		---	18	---	ns
$Q_g$	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V,$ $I_D=30A$	---	96	---	nc
$Q_{gs}$	Gate-Source Charge		---	19	---	nc
$Q_{gd}$	Gate-Drain "Miller" Charge		---	22	---	nc
<b>Drain-Source Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_{SD}=30A$	---	---	1.2	V
$I_S$	Continuous Drain Current	$V_D=V_G=0V$	---	---	140	A
$I_{SM}$	Pulsed Drain Current		---	---	515	A
$T_{rr}$	Reverse Recovery Time	$I_F=30A, T_J=25^\circ\text{C}$	---	23	---	ns
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu\text{s}$	---	14	---	nc

### Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. EAS condition: Starting  $T_J=25^\circ\text{C}$ ,  $V_{DD}=15\text{V}$ ,  $V_G=10\text{V}$ ,  $R_G=25\text{ohm}$ ,  $L=0.5\text{mH}$ ,  $I_{AS}=34\text{A}$
3.  $R_{\theta JA}$  is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
4. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$ .

### Typical Characteristics: ( $T_C=25^\circ\text{C}$ unless otherwise noted)

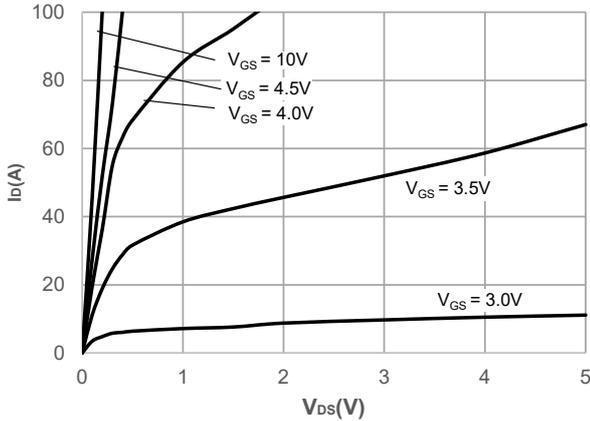


Figure 1: Output Characteristics

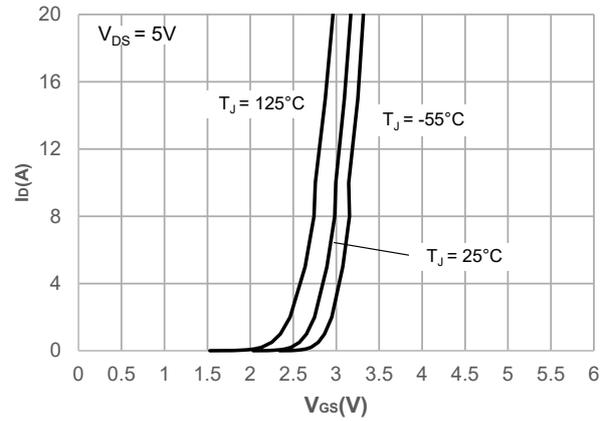


Figure 2: Typical Transfer Characteristics

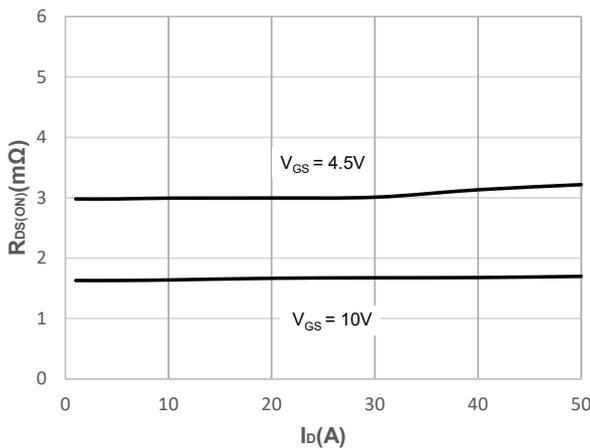


Figure 3: On-resistance vs. Drain Current

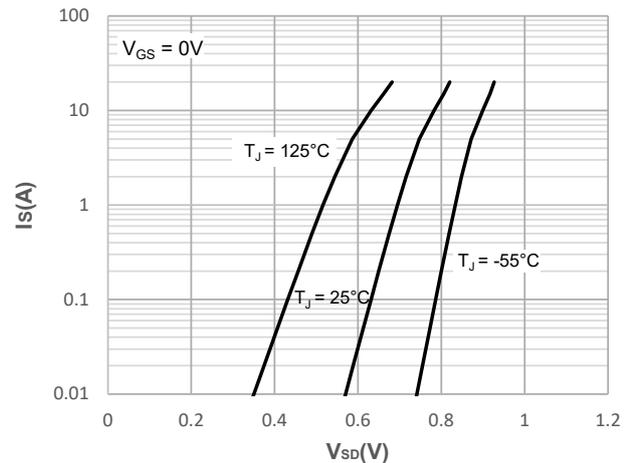


Figure 4: Body Diode Characteristics

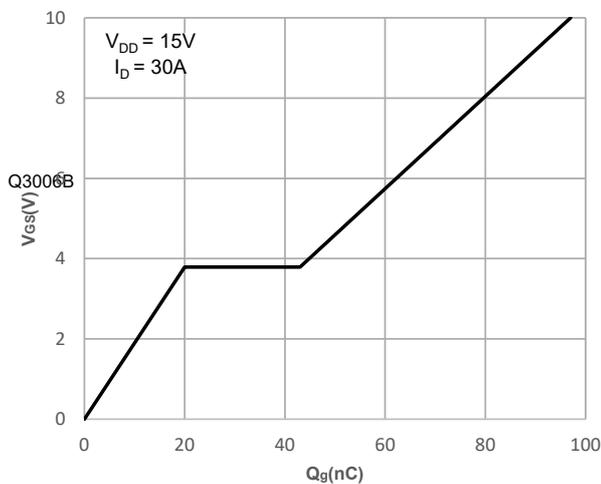


Figure 5: Gate Charge Characteristics

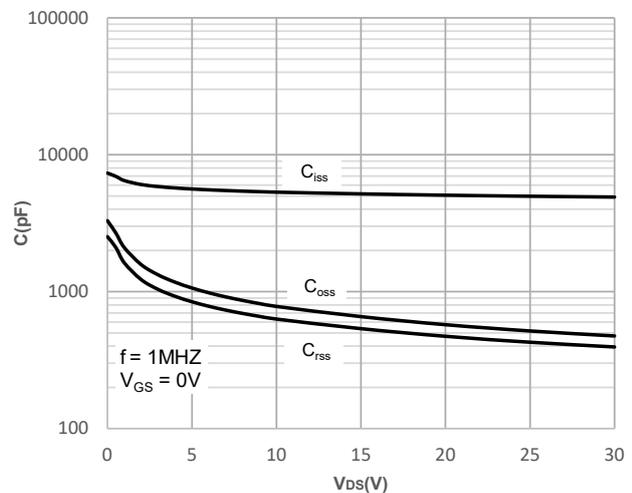
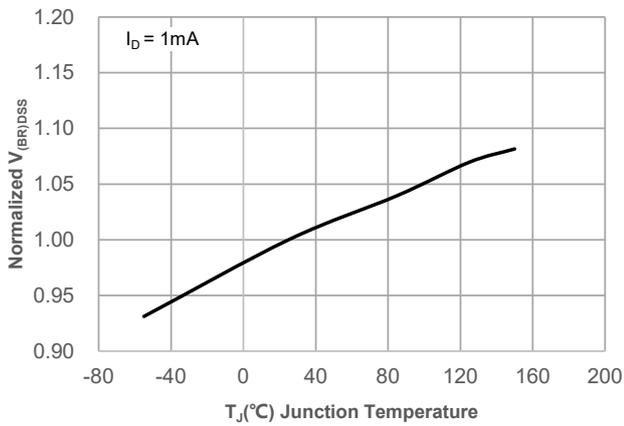
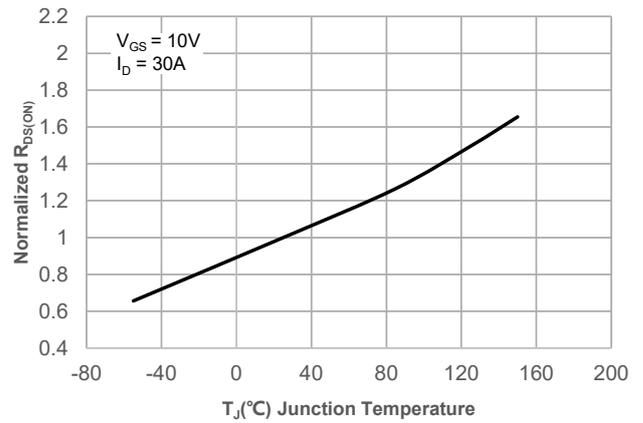


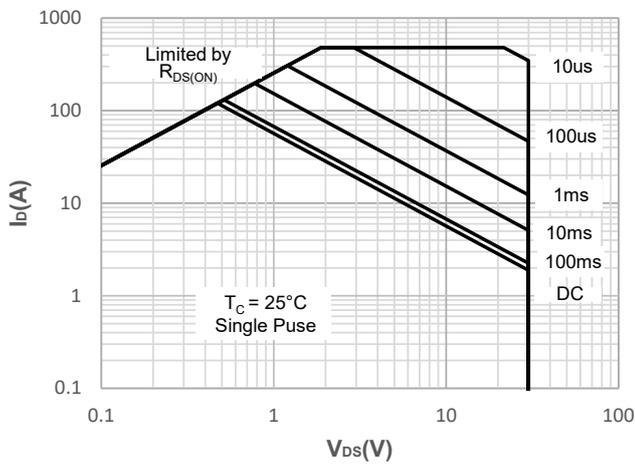
Figure 6: Capacitance Characteristics



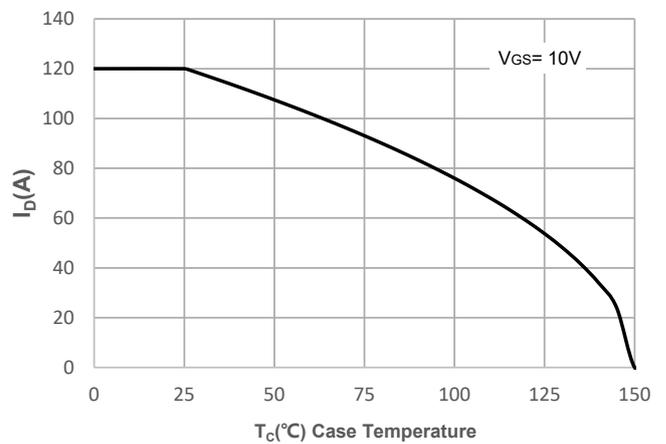
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



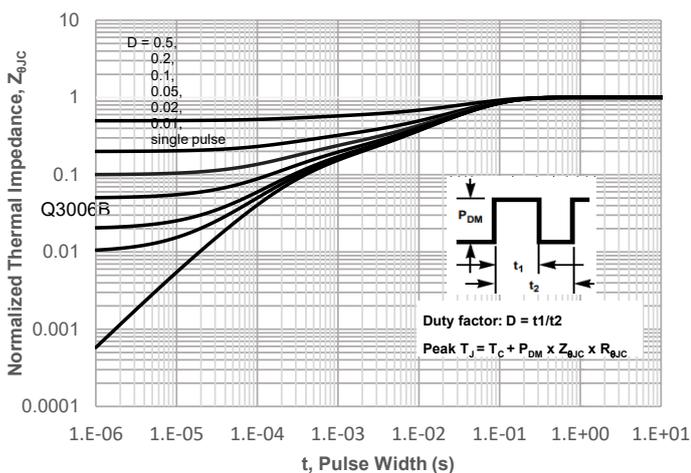
**Figure 8: Normalized on Resistance vs. Junction Temperature**



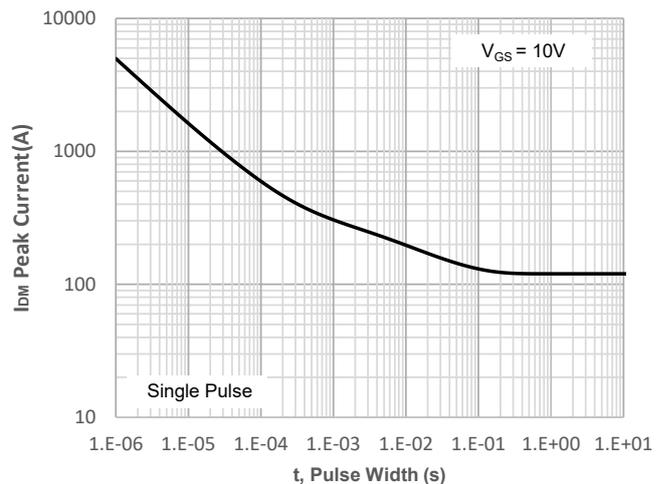
**Figure 9: Maximum Safe Operating Area**



**Figure 10: Maximum Continuous Driant Current vs. Case Temperature**

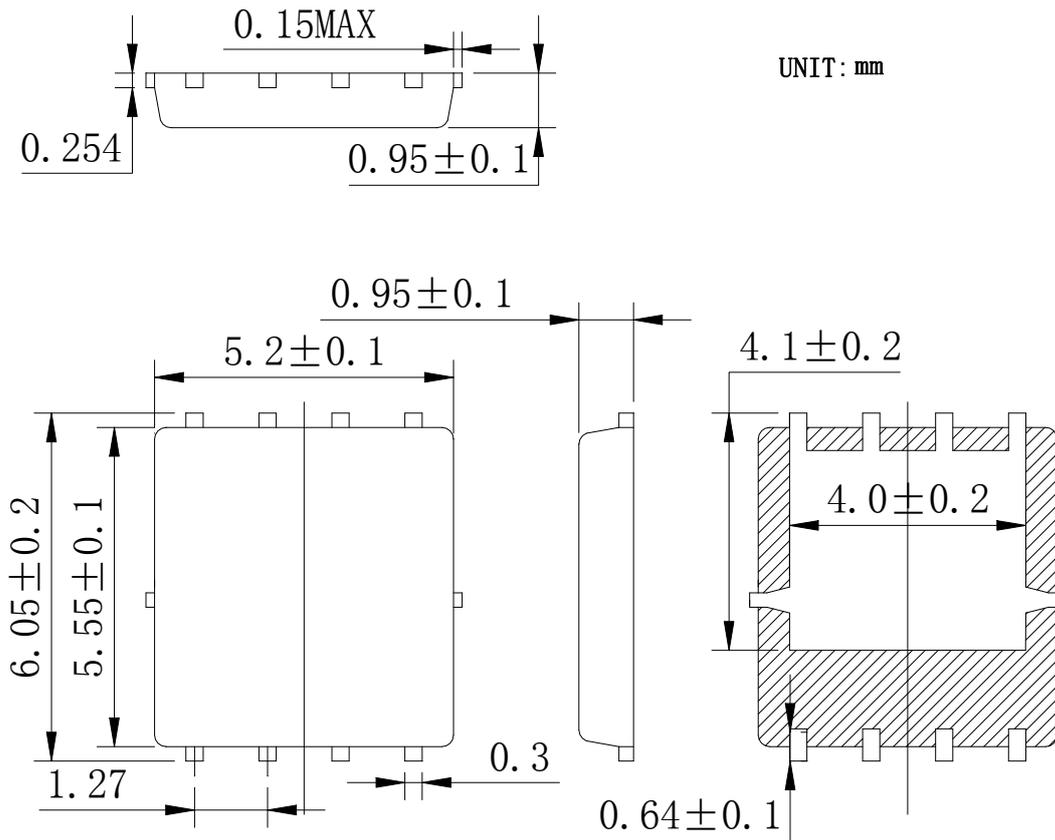


**Figure 11: Normalized Maximum Transient Thermal Impedance**



**Figure 12: Peak Current Capacity**

## DFN5x6-8 Package Information:



## Marking Information:

①. Doingter LOGO

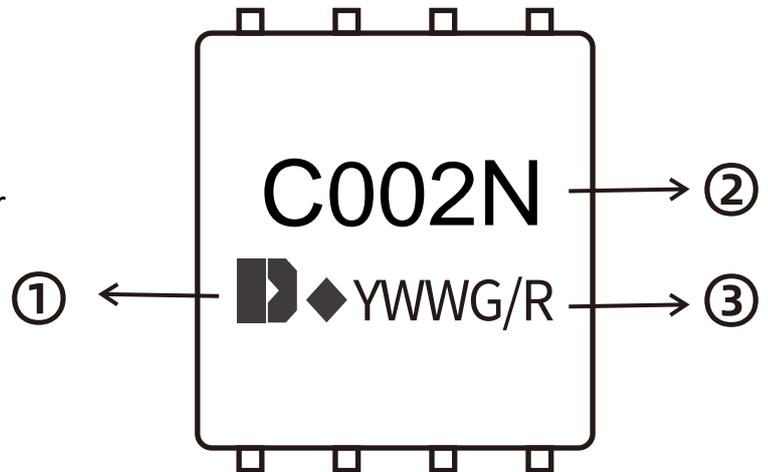
②. Part NO.

③. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



## Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2024-06-012	Release of final version

## Attention :

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