

MDD08N10A

100V N-Channel Enhancement Mode MOSFET

1. Description

This N-Channel MOSFET is produced using MDD Semiconductor's advanced Power Trench process technology. This process has been optimized to minimize on-state resistance and yet maintain superior switching performance with best in class soft body diode.

2. Features

- Max $R_{DS(on)}$ = 85 m Ω at $V_{GS} = 10$ V, $I_D = 5$ A
- Low Reverse Recovery Charge, Q_{rr}
- Pb-free plating

3. Application

- Load Switch
- PWM Application
- DC-DC converters
- Power Management

4. Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current (Note 1)	I_D	8	A
Pulsed Drain Current (Note 2)	I_{DM}	32	A
Thermal Resistance, steady-state	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	-55~+150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55~+150	$^\circ\text{C}$

Note: 1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating, pulse width limited by max. junction temperature.

5. Pinning information

Pin	Symbol	Description	Simplified outline	Equivalent Circuit	Marking	Package
1	G	Gate			8N10 XXY: Date code	SOT-23
2	S	Source				
3	D	Drain				

6. $T_A=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Condition	Min	Typ	Max	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	—	—	V
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$	—	—	± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V, V_{GS}=0V$	—	—	1	μA
$V_{GS(TH)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.3	—	2.4	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=10V, I_D=5A$	—	67	85	$m\Omega$
		$V_{GS}=4.5V, I_D=5A$	—	80	105	$m\Omega$

7. Dynamic Electrical Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS}=0V$ $V_{DS}=50V$ $f=1.0MHz$	—	171	—	pF
C_{oss}	Output Capacitance		—	58	—	pF
C_{rss}	Reverse Transfer Capacitance		—	1.9	—	pF
Q_g	Total Gate Charge	$V_{GS}=0-10V$ $V_{DS}=50V$ $I_D=2A$	—	3.7	—	nC
Q_{gs}	Gate Source Charge		—	0.8	—	nC
Q_{gd}	Gate Drain Charge		—	1	—	nC

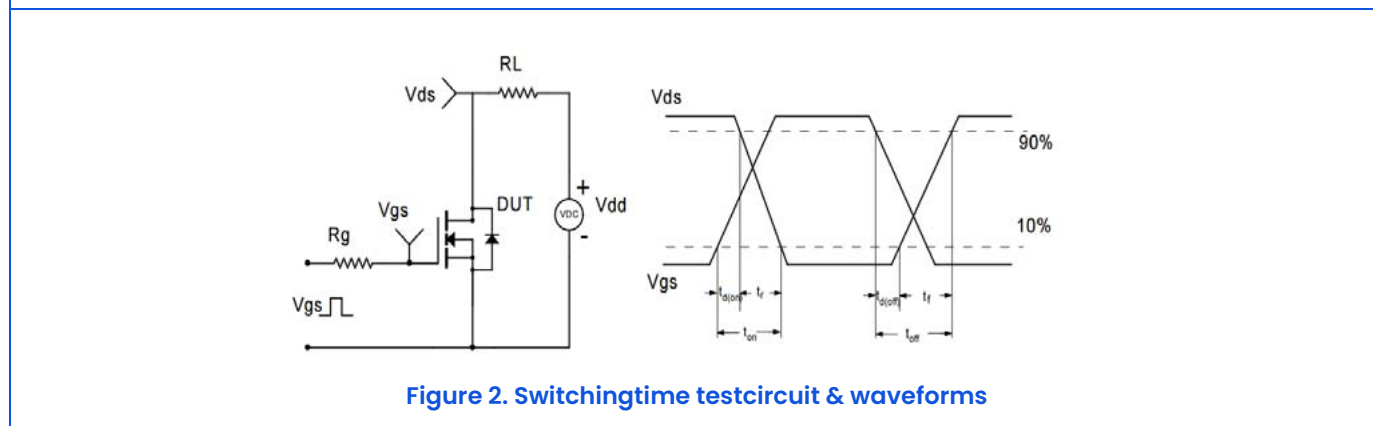
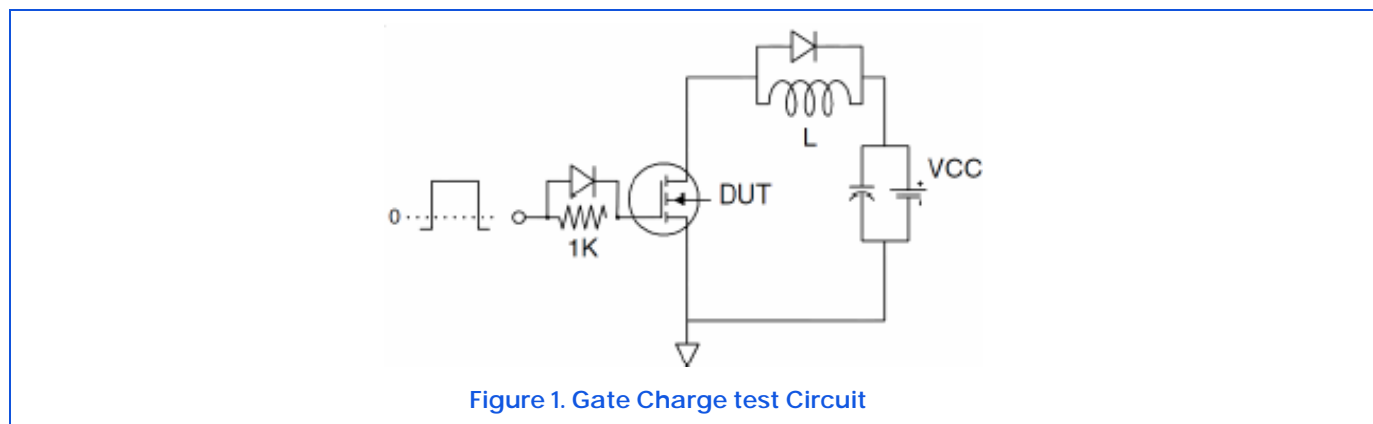
8. Switching Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
$t_{d(on)}$	Turn on Delay Time	$V_{GS}=10V$ $V_{DD}=50V$ $I_D=2A$ $R_\theta=5\Omega$	—	8	—	ns
t_r	Turn on Rise Time		—	16	—	ns
$t_{d(off)}$	Turn Off Delay Time		—	17	—	ns
t_f	Turn Off Fall Time		—	14	—	ns

9. Source Drain Diode Characteristics

Symbol	Parameter	Condition	Min	Typ	Max	Unit
V_{SD}	Drain-Source Diode Forward Voltage	$I_S=2A, V_{GS}=0V$	—	0.8	1.2	V

10. Test Circuits And Waveforms



The curve above is for reference only.

II. Electrical Characteristics Diagrams

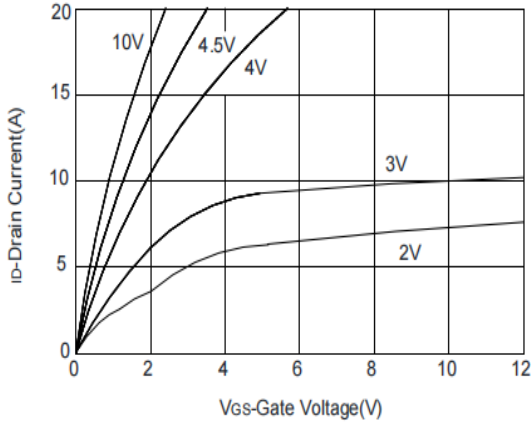


Figure 1. Typ. output characteristics

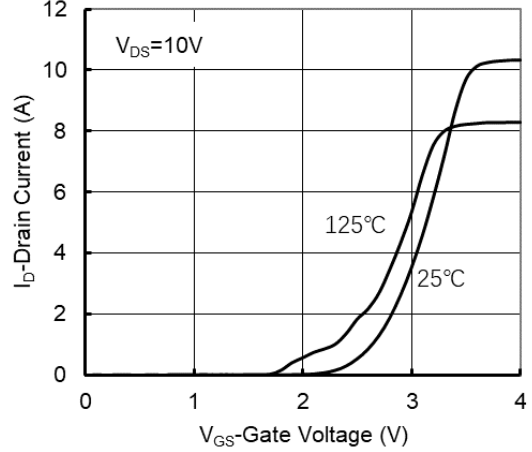


Fig 2. Transfer Charhacteristics

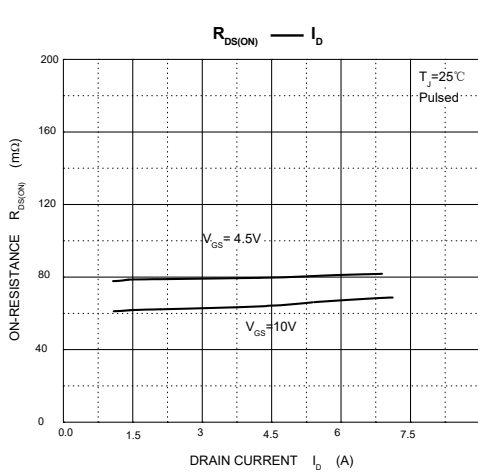


Fig 3. RDson vs I_D Characteristics

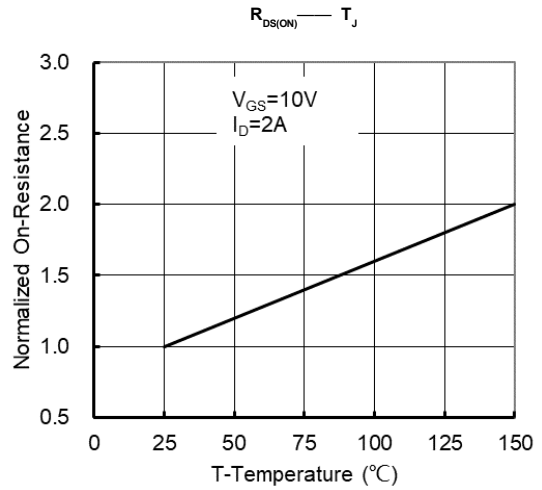


Fig 4. RDson vs T_J Characteristics

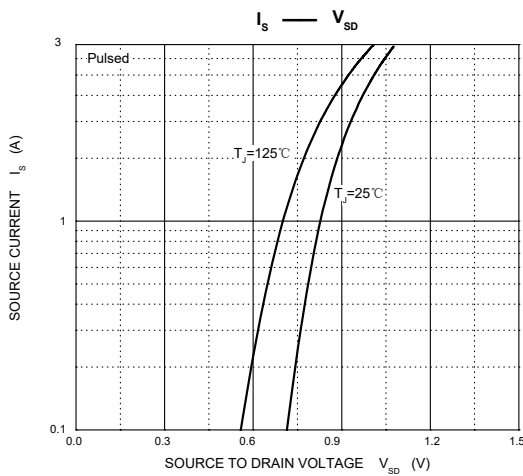


Fig 5. Source current Characteristics

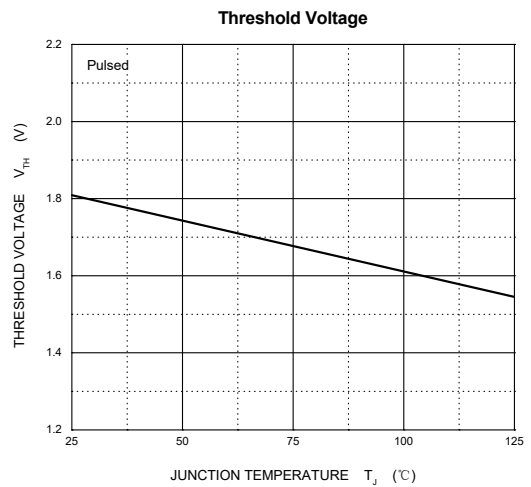
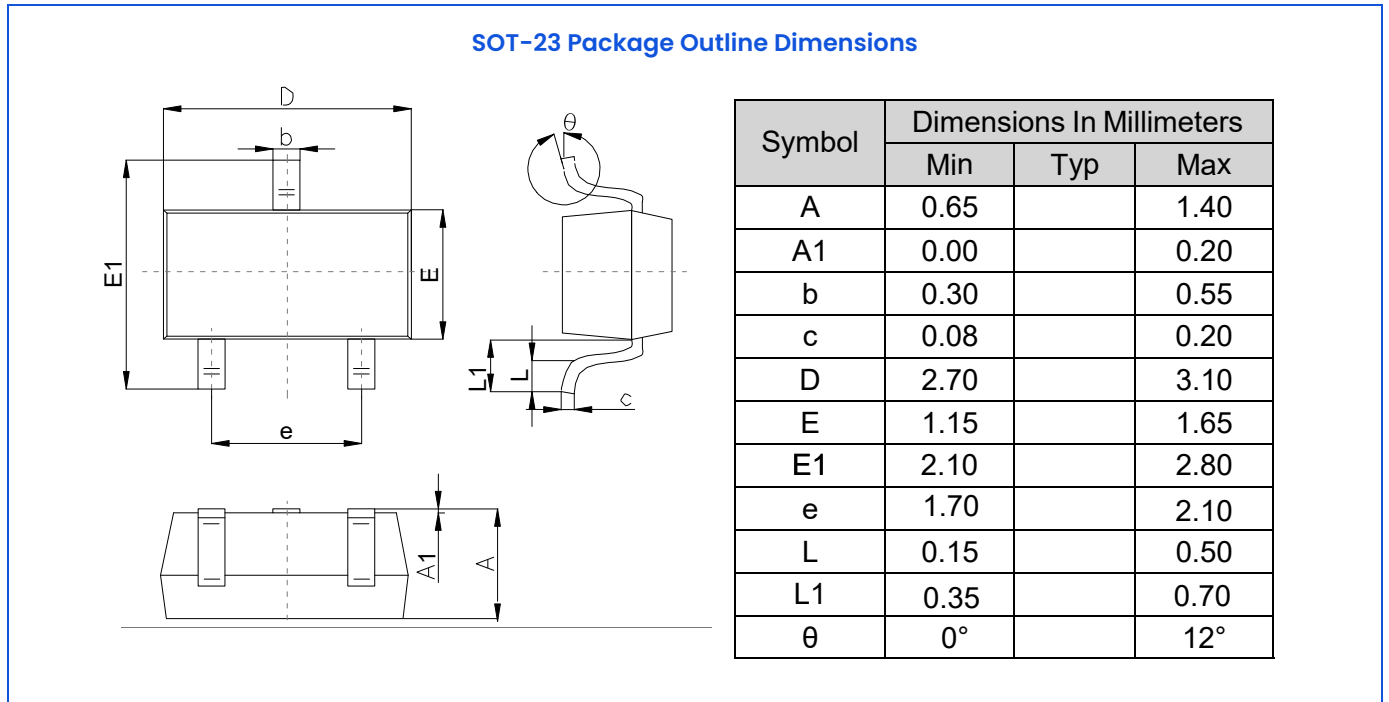


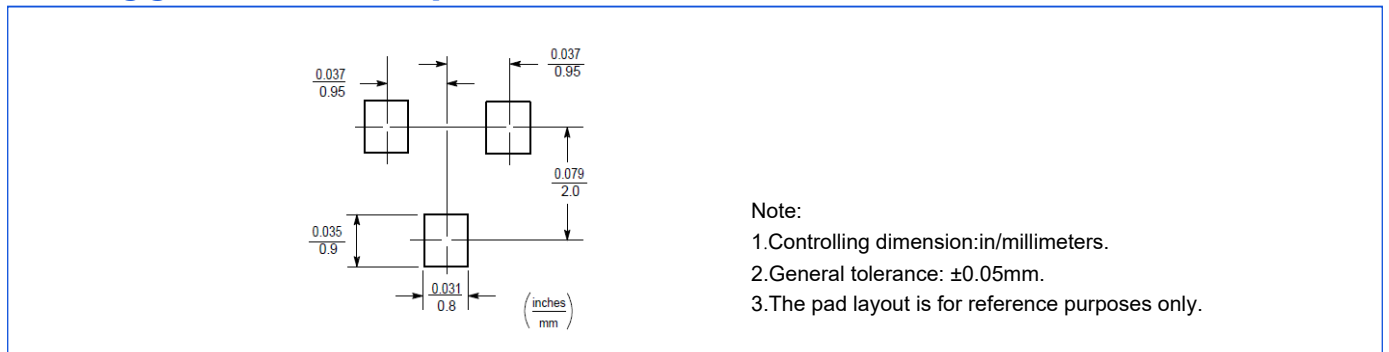
Fig 6. Typ. Threshold Voltage Characteristics

The curve above is for reference only

12. Outline Drawing



13. Suggested Pad Layout



14. Important Notice and Disclaimer

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