



QNHCHIP

QNN100N03C

Product Specification

QNN100N03C

30V N-Channel MOSFET



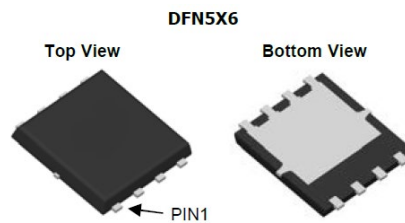
FEATURES

- 30V, 90A
 $R_{DS(ON)} = 4.6 \text{ m}\Omega @ V_{GS} = 10\text{V (Typ.)}$
 $R_{DS(ON)} = 7.1 \text{ m}\Omega @ V_{GS} = 4.5\text{V (Typ.)}$
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Halogen-free; RoHS-compliant
- Pb-free plating

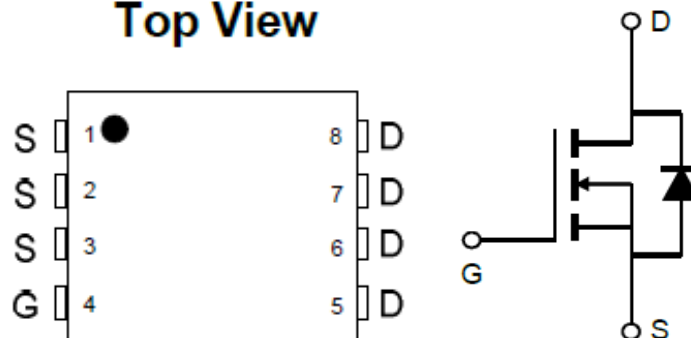
Applications

- Load Switch
- PWM Application
- Power Management

Pin Description



Top View



NO.	Symbol	Description
1	S	SOURCE
2	S	SOURCE
3	S	SOURCE
4	G	GATE
5	D	DRAIN
6	D	DRAIN
7	D	DRAIN
8	D	DRAIN



Absolute Maximum Ratings

(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V_{DS}	Drain-to-Source Voltage	30	V	
V_{GS}	Gate-to-Source Voltage	± 20	V	
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	90	A
		$T_C=100^\circ\text{C}$	60	
I_{DM}	Pulsed Drain Current ⁽¹⁾	360	A	
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	110	mJ	
P_D	Power Dissipation	$T_C=25^\circ\text{C}$	50	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.5	$^\circ\text{C}/\text{W}$	
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$	



Electrical Characteristics

($T_J = 25^\circ\text{C}$ unless otherwise specified)

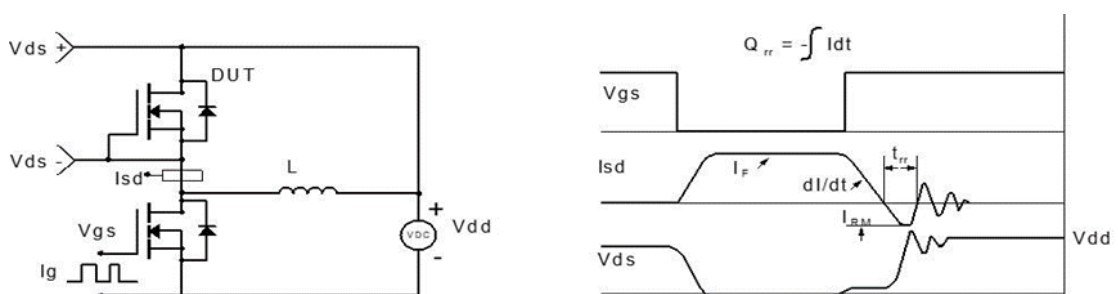
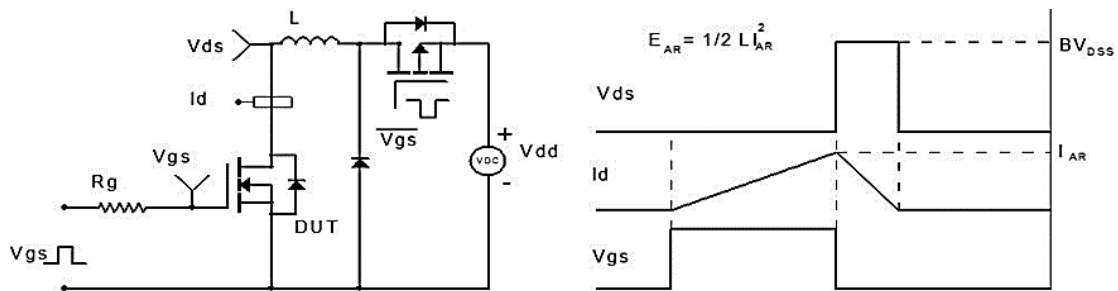
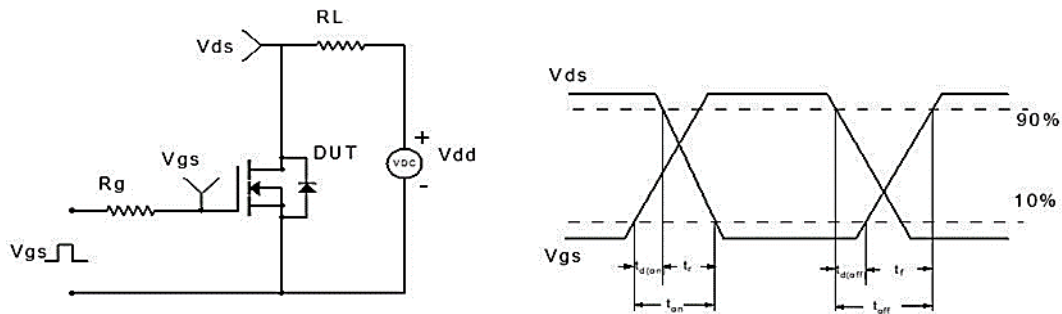
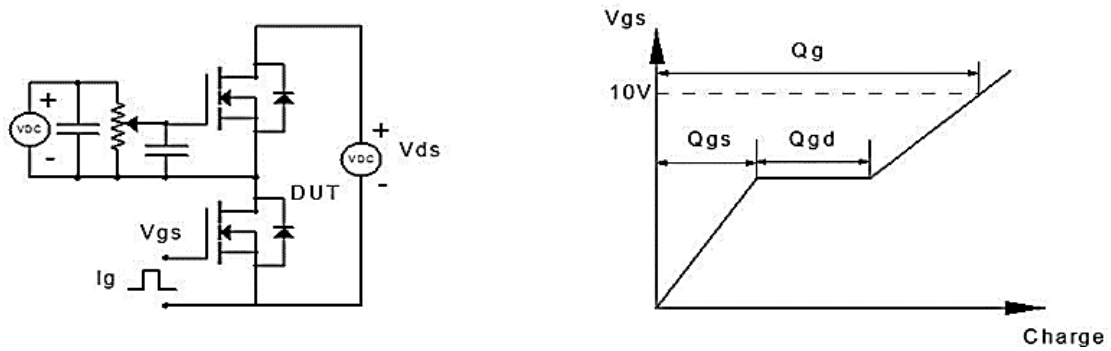
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.0	1.5	2.0	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS}=10\text{V}, I_D=30\text{A}$	-	4.6	7.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=20\text{A}$	-	7.1	8.8	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$	-	2084	-	pF
C_{oss}	Output Capacitance		-	235	-	pF
C_{rss}	Reverse Transfer Capacitance		-	210	-	pF
Q_g	Total Gate Charge	$V_{GS}=0\sim 10\text{V}$ $V_{DS}=15\text{V}, I_D=30\text{A}$	-	42	-	nC
Q_{gs}	Gate Source Charge		-	9	-	nC
Q_{gd}	Gate Drain ("Miller") Charge		-	10	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS}=10\text{V}, V_{DD}=15\text{V}, I_D=30\text{A}, R_{GEN}=3\Omega$	-	9	-	ns
t_r	Turn-On Rise Time		-	15	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	36	-	ns
t_f	Turn-Off Fall Time		-	11	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	90	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	360	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}, I_S=30\text{A}$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F=20\text{A}, di/dt=100\text{A}/\mu\text{s}$	-	11	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	2.5	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting $T_J=25^\circ\text{C}$, $V_{DD}=15\text{V}$, $V_G=10\text{V}$, $R_G=25\Omega$, $L=0.5\text{mH}$, $I_{AS}=21\text{A}$
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

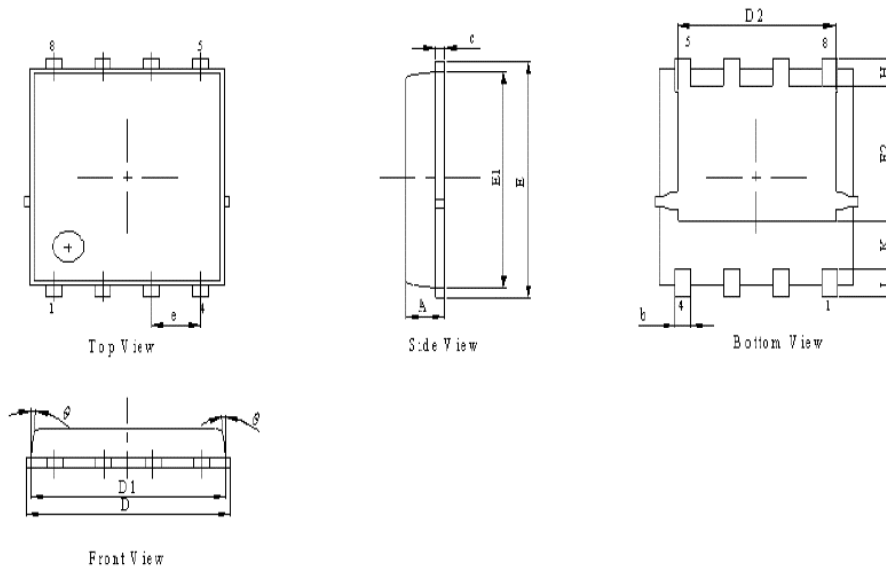


Test Circuit

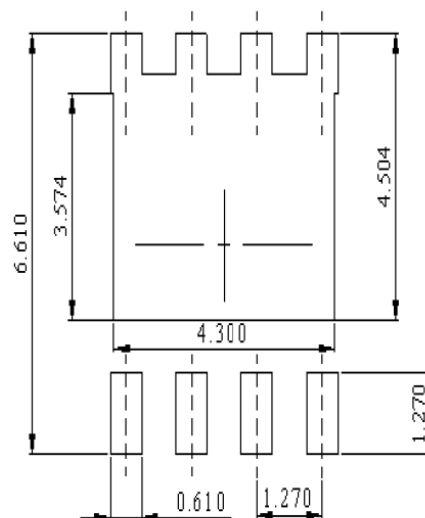




Package Mechanical Data(PDFN 5x6-8L)



Symbol	Dimensions In Millimeters		
	Min.	NOM.	Max.
A	0.9	1	1.15
b	0.31	0.41	0.51
C	0.24	0.32	0.4
D	5	5.2	5.4
D1	4.95	5.05	5.15
D2	4	4.1	4.2
E	6.05	6.15	6.25
E1	5.5	5.6	5.7
E2	3.42	3.53	3.63
e	1.27 BSC		
H	0.6	0.7	0.8
L	0.5	0.7	0.8
K	1.23 BEF		
O			10



DIMENSIONS: MILLIMETERS



Ordering information

Order Code	Package	V _{DS} (V)	I _D (A)	R _{DS(ON)} (m Ω)	
QNN100N03C	PDFN 5x6-8	30	90	V _{GS} =10V	4.6
				V _{GS} =4.5V	7.1