



QNHCHIP

QNN2401

Product Specification

QNN2401

15V P-Channel MOSFET



Absolute Maximum Ratings

(@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Units	
V_{DS}	Drain-to-Source Voltage	-15	V	
V_{GS}	Gate-to-Source Voltage	± 12	V	
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	-12	A
		$T_A=100^\circ\text{C}$	-7.2	
I_{DM}	Pulsed Drain Current ⁽¹⁾	-48	A	
P_D	Power Dissipation	$T_A=25^\circ\text{C}$	5	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	25	$^\circ\text{C}/\text{W}$	
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$	



Electrical Characteristics

(T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D =-250uA, V _{GS} =0V	-15	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-15V, V _{GS} =0V	-	-	1.0	uA
I _{GSS}	Gate-Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.65	-1	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽²⁾	V _{GS} =-4.5V, I _D =-3A	-	16.7	21.8	m Ω
		V _{GS} =-2.5V, I _D =-2A	-	22.6	29.5	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-10V, f=1MHz	-	901	-	pF
C _{oss}	Output Capacitance		-	173	-	pF
C _{rss}	Reverse Transfer Capacitance		-	150	-	pF
Q _g	Total Gate Charge	V _{GS} =0~-4.5V V _{DS} =-10V, I _D =-4.1A	-	8	-	nC
Q _{gs}	Gate Source Charge		-	1.2	-	nC
Q _{gd}	Gate Drain("Miller") Charge		-	1.6	-	nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} =-4.5V, V _{DD} =-10V, I _D =-3.3A, R _{GEN} =1 Ω	-	13	-	ns
t _r	Turn-On Rise Time		-	35	-	ns
t _{d(off)}	Turn-Off DelayTime		-	32	-	ns
t _f	Turn-Off Fall Time		-	10	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-12	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-48	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =-3A	-	-	-1.2	V
trr	Body Diode Reverse Recovery Time	I _F =-5.8A, di/dt=100A/us	-	20	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	9	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. Pulse Test: Pulse Width ≤ 300us, Duty Cycle ≤ 0.5%.



Test Circuit

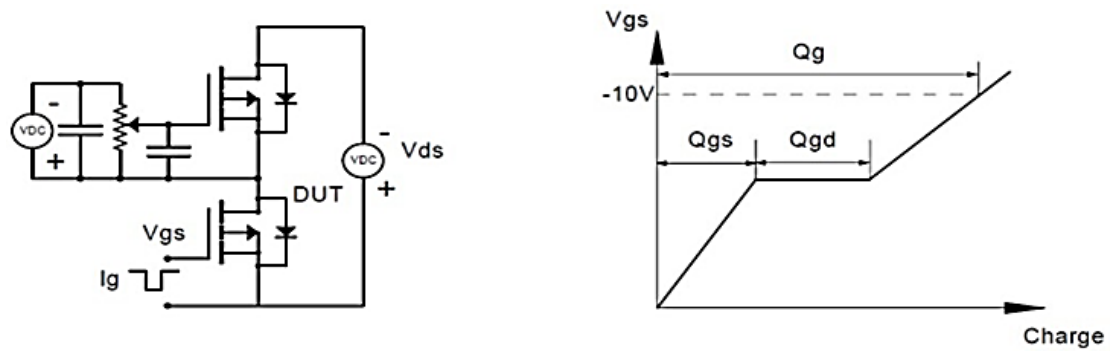


Figure 1: Gate Charge Test Circuit & Waveform

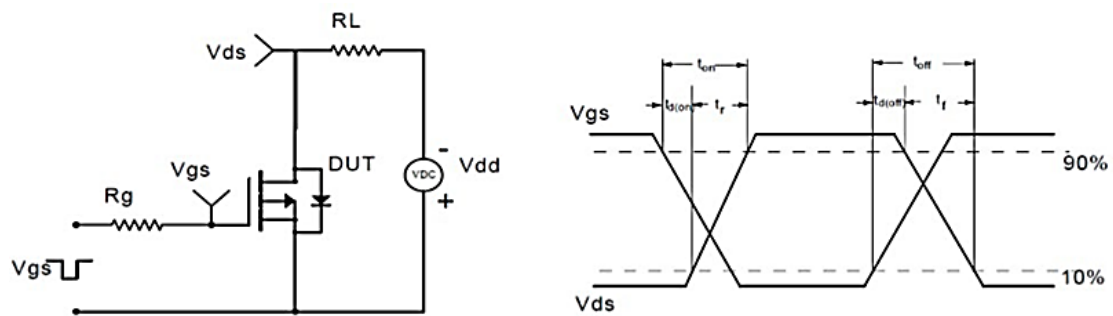


Figure 2: Resistive Switching Test Circuit & Waveform

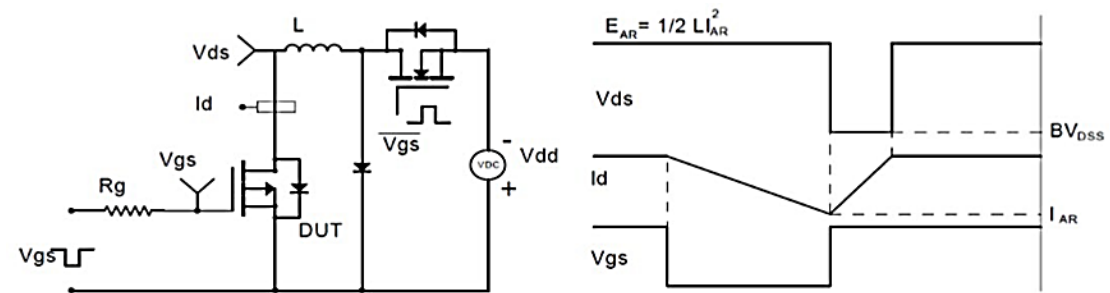
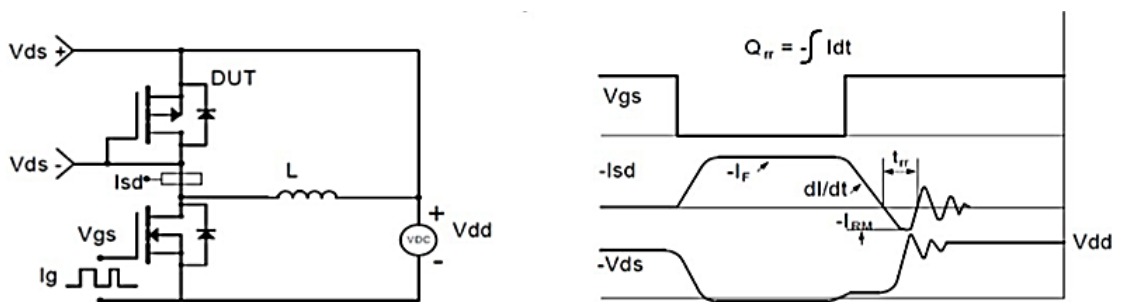
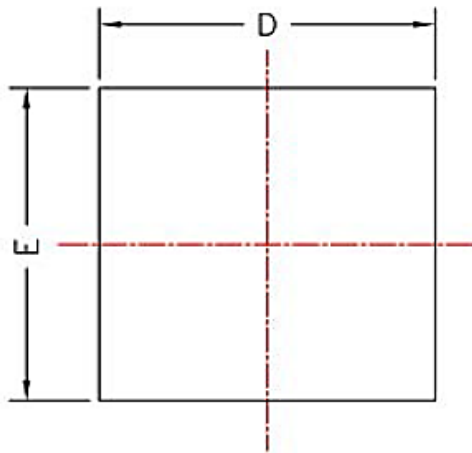


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

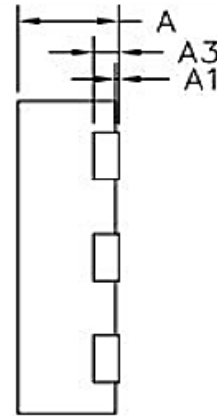




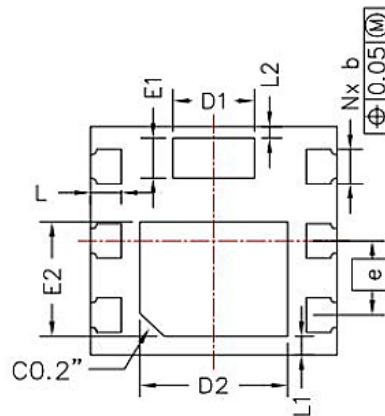
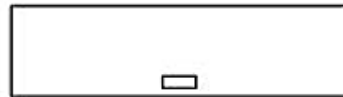
Package Mechanical Data(PDFN 2x2-6)



Top View



Side View



Bottom View

Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	NOM.	Max.	MIN.	NOM.	MAX.
A	0.700	0.750	0.800	0.028	0.030	0.031
A1	-	-	0.050	-	-	0.002
A3	0.195	0.203	0.211	0.008	0.008	0.008
b	0.250	0.300	0.350	0.010	0.012	0.014
e	0.65 BSC			0.026 BSC		
D	1.900	2.000	2.100	0.075	0.079	0.083
E	1.900	2.000	2.100	0.075	0.079	0.083
D1	0.560	0.660	0.760	0.022	0.026	0.030
E1	0.250	0.350	0.450	0.010	0.014	0.018
D2	1.100	1.200	1.300	0.043	0.047	0.051
E2	0.900	1.000	1.100	0.035	0.039	0.043
L	0.150	0.250	0.350	0.006	0.010	0.014
L1	0.065	0.165	0.265	0.003	0.006	0.010
L2	0.000	0.100	0.200	0,000	0,004	0,008



Ordering information

Order Code	Package	V _{DS} (V)	I _D (A)	R _{DS(ON)} (m Ω)	
QNN2401	PDFN 2x2-6	-15V	-12	V _{GS} =-4.5V	16.7
				V _{GS} =-2.5V	22.6