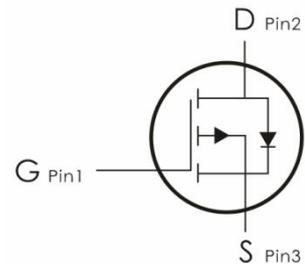
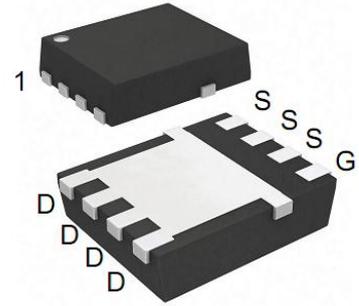


Description:

This P-Channel MOSFET uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=-60V, I_D=-55A, R_{DS(ON)}<15m\ \Omega @V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
NE015TPG	E015TP	DFN5*6-8	5000 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	-55	A
	Continuous Drain Current- $T_C=100^\circ\text{C}$	-35	
I_{DM}	Pulsed Drain Current ¹	-220	
P_D	Power Dissipation	100	W
E_{AS}	Single pulse avalanche energy ²	337	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^\circ\text{C}$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.25	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	60	$^\circ\text{C}/\text{W}$

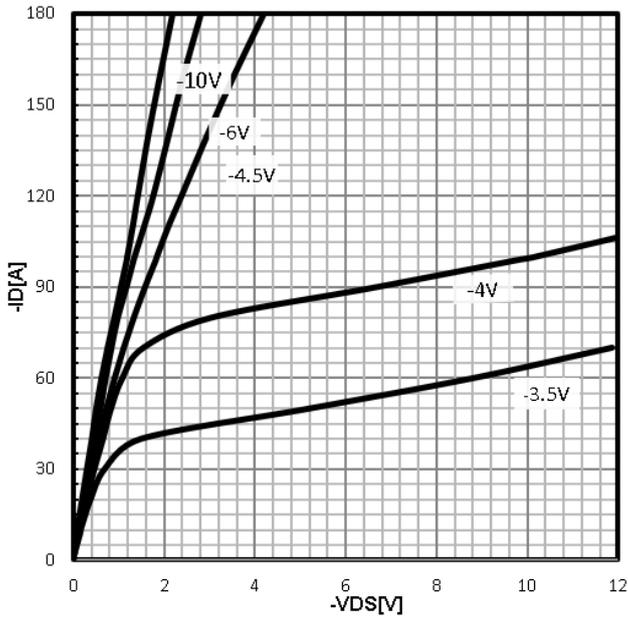
Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	-60	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=-60V$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0A$	---	---	± 100	nA
On Characteristics						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	-1.3	-1.8	-2.3	V
$R_{DS(on)}$	Drain-Source On Resistance	$V_{GS}=-10V, I_D=-20A$	---	11	15	$\text{m}\Omega$
		$V_{GS}=-4.5V, I_D=-10A$	---	15	20	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, f=1\text{MHz}$	---	2498	---	pF
C_{oss}	Output Capacitance		---	459	--	
C_{rss}	Reverse Transfer Capacitance		---	9.87	---	
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DS}=-30V, I_D=-10A,$ $R_G=3\ \Omega, V_{GS}=-10V$	---	21	---	ns
t_r	Rise Time		---	26.25	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	63	---	ns
t_f	Fall Time		---	31.5	---	ns
Q_g	Total Gate Charge	$V_{GS}=-10V, V_{DS}=-30V,$ $I_D=-10A$	---	39.9	---	nc
Q_{gs}	Gate-Source Charge		---	7.2	---	nc
Q_{gd}	Gate-Drain "Miller" Charge		---	5.2	---	nc
Drain-Source Diode Characteristics						
I_S	Continuous Drain Current	$V_D=V_G=0V$	---	---	-55	A
I_{SM}	Pulsed Drain Current		---	---	-220	A
T_{rr}	Reverse Recovery Time	$I_F=10A, T_J=25, dI/dt=100A/\mu\text{s}$	---	50	---	ns
Q_{rr}	Reverse Recovery Charge		---	80	---	nc
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_{SD}=-5A$	---	---	-1.2	V

Notes:

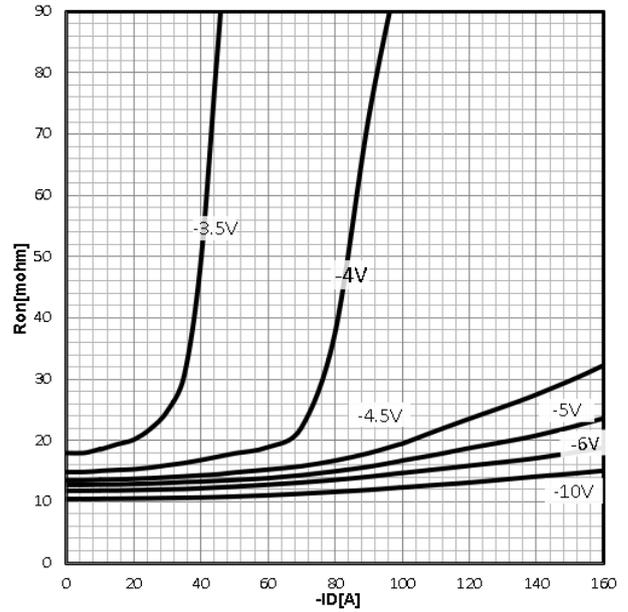
- 1.Repetitive rating; pulse width limited by maximum junction temperature
2. $V_{DD} = -30V$, $L = 1.0mH$, $R_G = 25\Omega$, Starting $T_j = 25^\circ C$

Typical Characteristics: ($T_c = 25^\circ C$ unless otherwise noted)



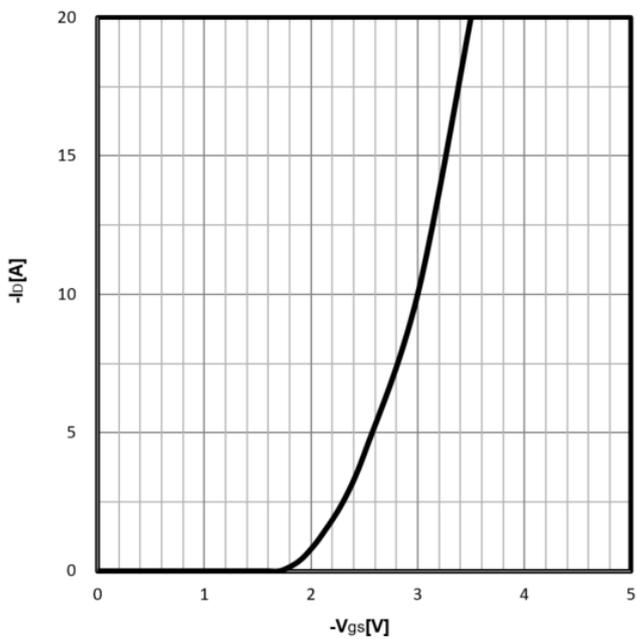
$$I_D = f(V_{GS})$$

Fig1. Typ. output characteristics



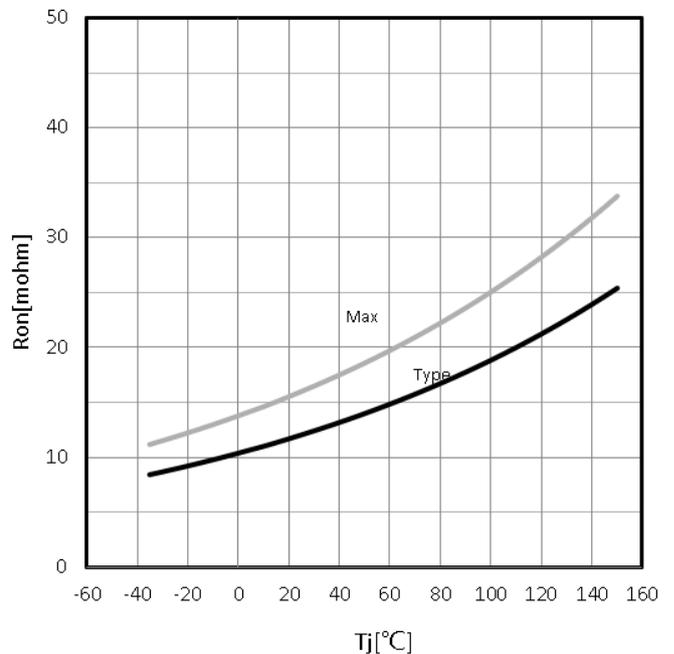
$$R_{DS(on)} = f(-I_D)$$

Fig2. Typ. drain-source on resistance



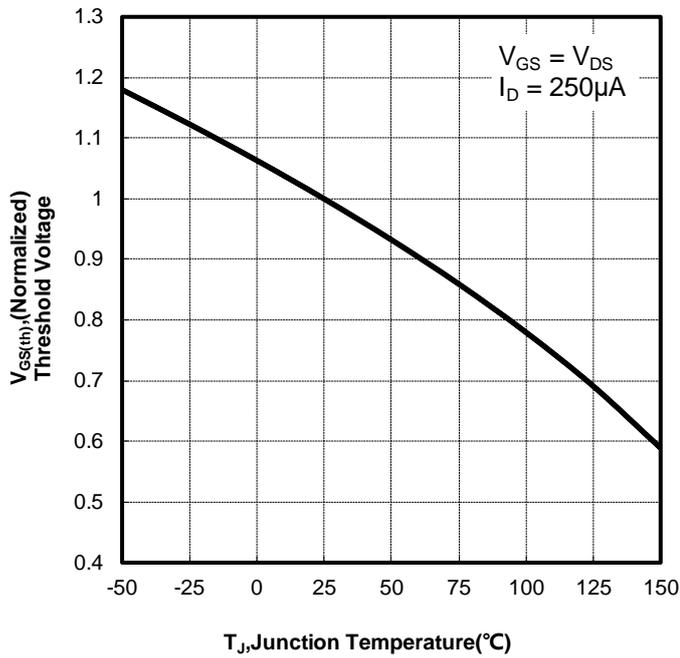
$$-I_D = f(-V_{GS})$$

Fig3. Typ. transfer characteristics

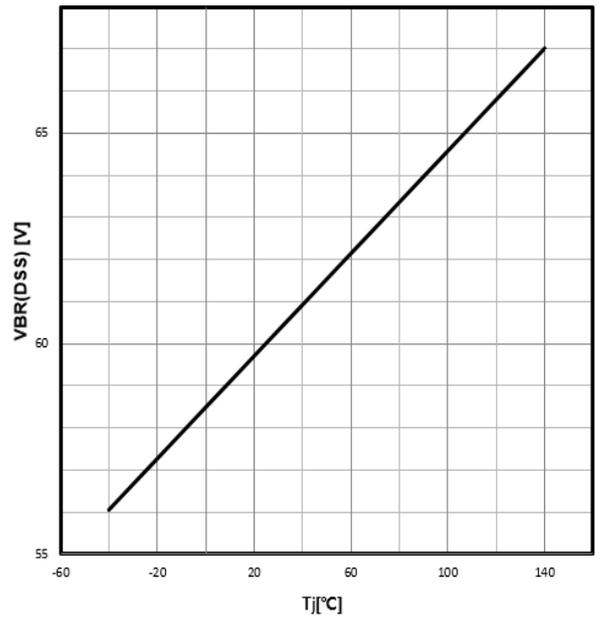


$$R_{DS(on)} = f(T_j); I_D = -20A; V_{GS} = -10V$$

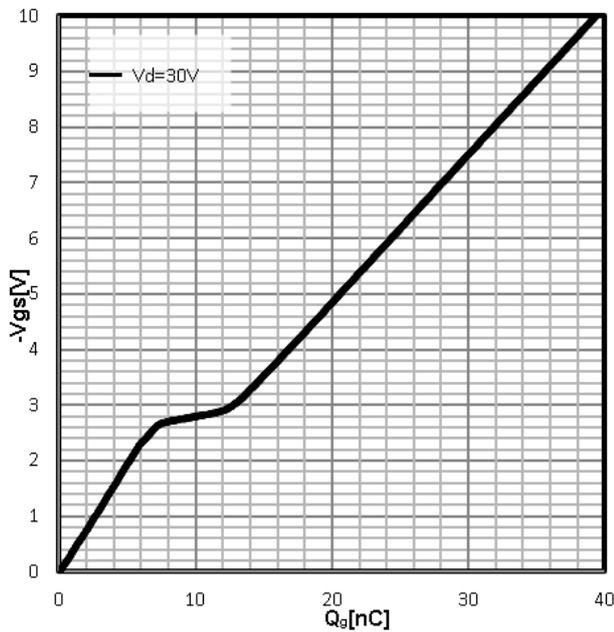
Fig4. Drain-source on-state resistance



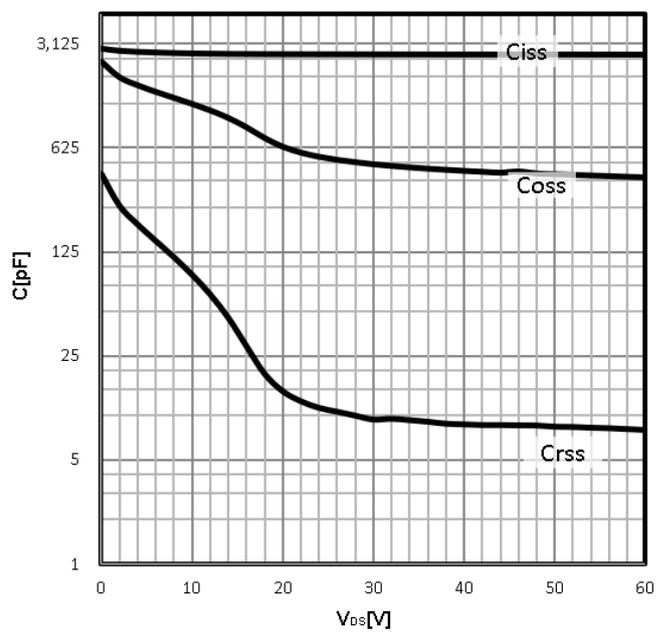
$-V_{TH}=f(T_j); I_D=-250\mu A$
Fig5. Gate Threshold Voltage



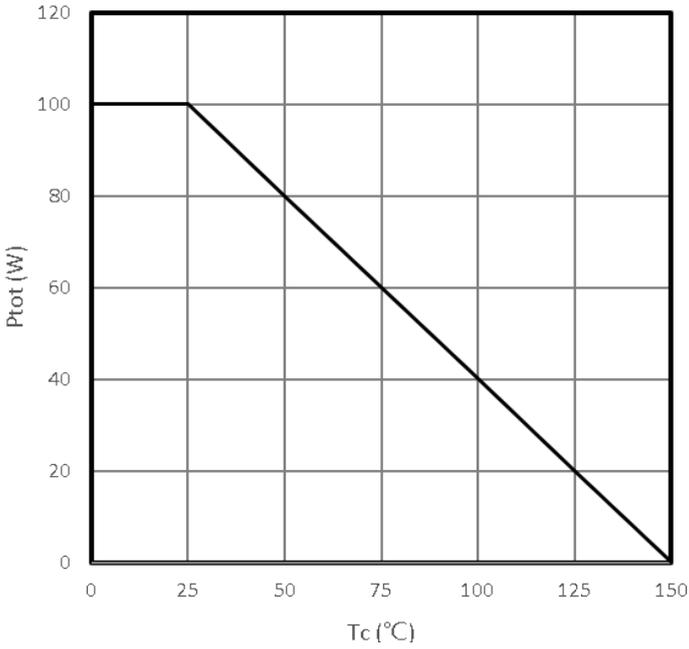
$-V_{BR(DSS)}=f(T_j); I_D=-250\mu A$
Fig6. Drain-source breakdown voltage



$V_{GS}=f(Q_{gate}); I_D=-10A$
Fig7. Typ. gate charge

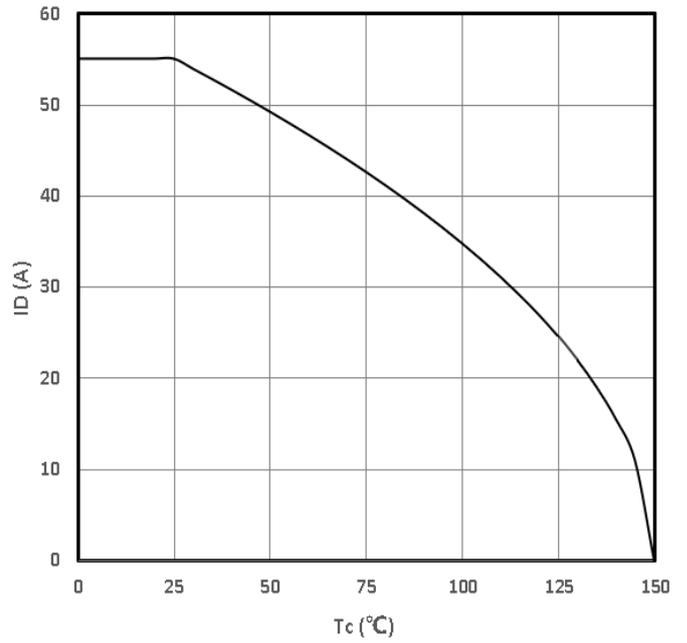


$C=f(-V_{DS}); V_{GS}=0V; f=1MHz$
Fig8. Typ. Capacitances



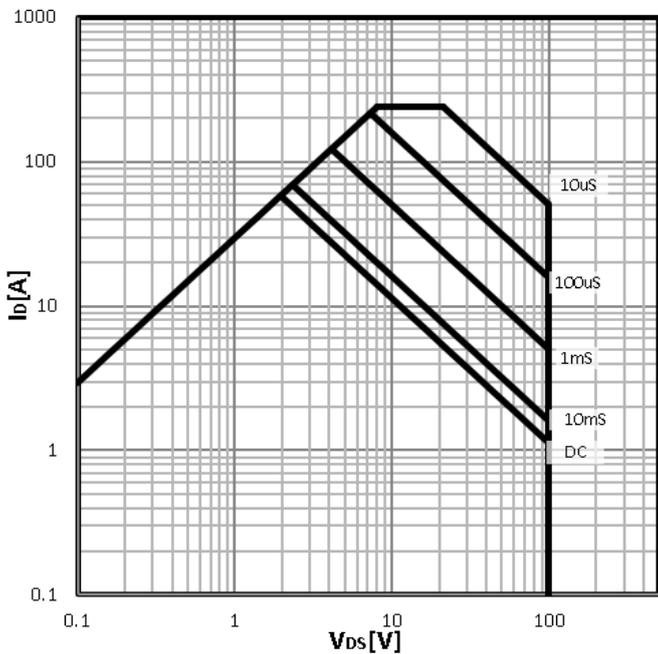
$$P_{tot}=f(T_c)$$

Fig9. Power Dissipation



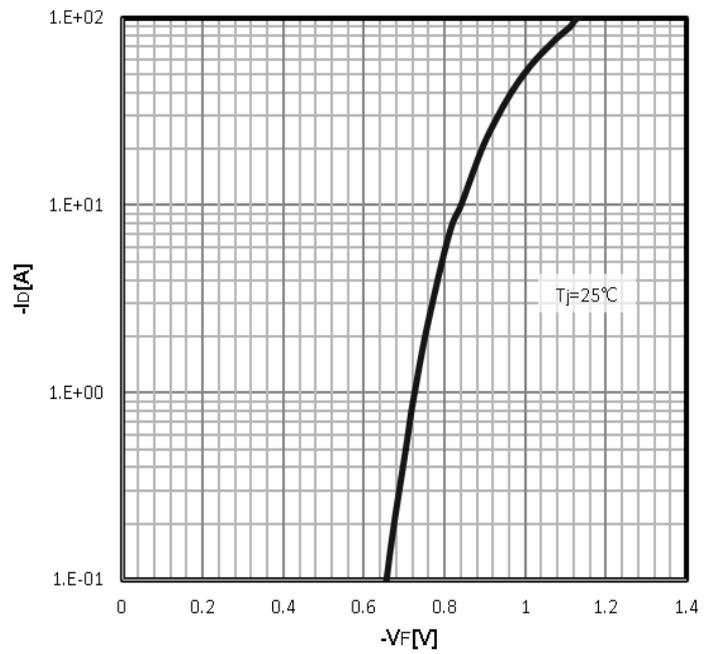
$$-I_D=f(T_c)$$

Fig10. Maximum Drain Current



$$-I_D=f(-V_{DS})$$

Fig11. Safe operating area



$$-I_F=f(-V_{DS})$$

Fig12. Body Diode Forward Voltage Variation

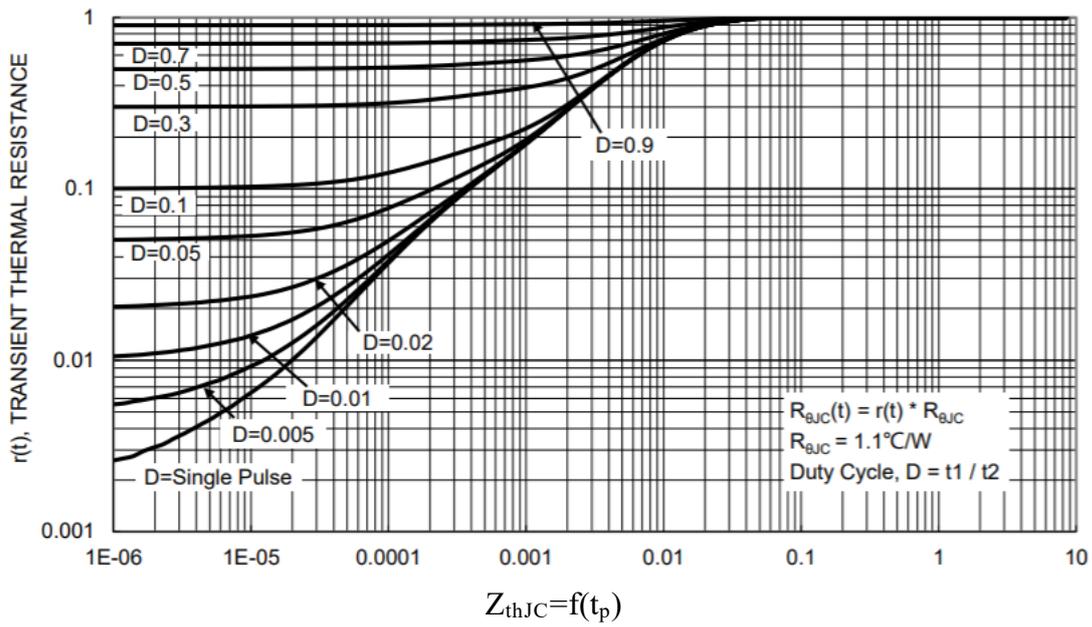
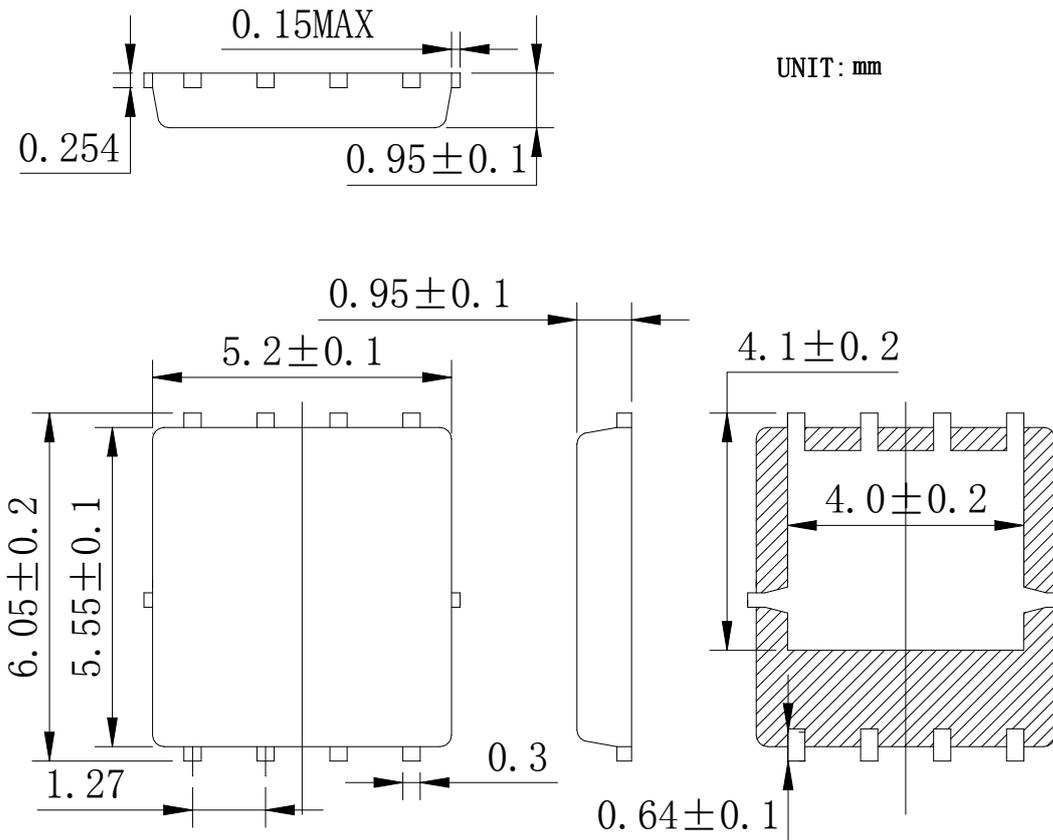


Fig13. Max. transient thermal impedance

DFN5x6-8 Package Information:



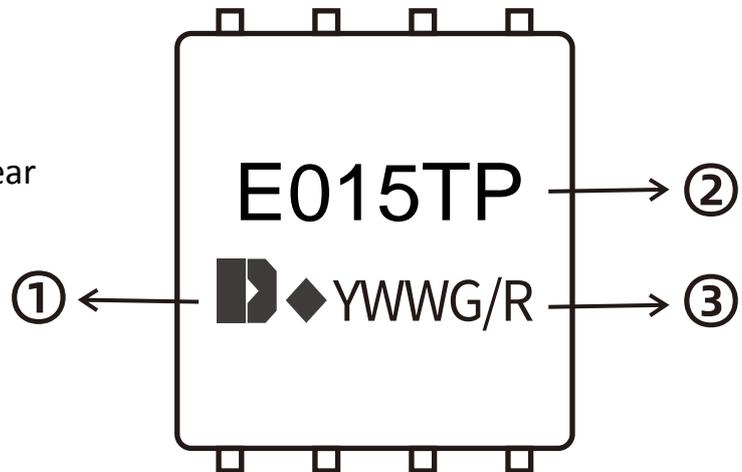
Marking Information:

- ①. Doingter LOGO
- ②. Part NO.
- ③. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2024-02-10	Release of final version

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