



QNHCHIP

QNN180N04AX

Product Specification

QNN180N04AX

40V N-Channel MOSFET



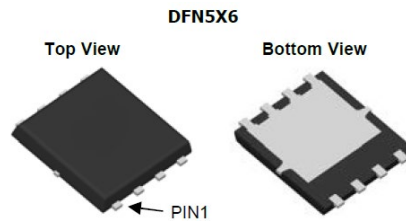
FEATURES

- 40V,100A
 $R_{DS(ON)} = 2.2m\Omega @ V_{GS} = 10V$ (Typ.)
 $R_{DS(ON)} = 3m\Omega @ V_{GS} = 4.5V$ (Typ.)
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Halogen-free; RoHS-compliant
- Pb-free plating

Applications

- Load Switch
- PWM Application
- Power Management

Pin Description



Top View



NO.	Symbol	Description
1	S	SOURCE
2	S	SOURCE
3	S	SOURCE
4	G	GATE
5	D	DRAIN
6	D	DRAIN
7	D	DRAIN
8	D	DRAIN



Absolute Maximum Ratings

(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	
V_{DS}	Drain-to-Source Voltage	40	V	
V_{GS}	Gate-to-Source Voltage	± 20	V	
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	100	A
		$T_C = 100^\circ\text{C}$	65	
I_{DM}	Pulsed Drain Current ⁽¹⁾	400	A	
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	225	mJ	
P_D	Power Dissipation	$T_C = 25^\circ\text{C}$	61	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.0	$^\circ\text{C}/\text{W}$	
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$	



Electrical Characteristics

($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	120	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$	-	-	1.0	μA
I_{GSS}	Gate-Body Leakage Current	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.4	1.5	2.5	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance ⁽³⁾	$V_{GS}=10\text{V}, I_D=20\text{A}$	-	2.2	2.8	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=10\text{A}$	-	3	3.9	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=20\text{V}, f=1\text{MHz}$	-	5595	-	pF
C_{oss}	Output Capacitance		-	411	-	pF
C_{rss}	Reverse Transfer Capacitance		-	340	-	pF
Q_g	Total Gate Charge	$V_{GS}=0\sim 10\text{V}, V_{DS}=20\text{V}, I_D=30\text{A}$	-	59	-	nC
Q_{gs}	Gate Source Charge		-	12.5	-	nC
Q_{gd}	Gate Drain ("Miller") Charge		-	15	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	$V_{GS}=10\text{V}, V_{DD}=20\text{V}, I_D=30\text{A}, R_{GEN}=3\Omega$	-	12	-	ns
t_r	Turn-On Rise Time		-	16	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	39	-	ns
t_f	Turn-Off Fall Time		-	15	-	ns
Drain-Source Diode Characteristics and Max Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	100	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	400	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}, I_S=30\text{A}$	-	-	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$I_F=30\text{A}, di/dt=100\text{A}/\mu\text{s}$	-	22	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge		-	11	-	nC

Notes:

- 1.Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
- 2.E_{AS} condition: Starting $T_J=25^\circ\text{C}$, $V_{DD}=15\text{V}$, $V_G=10\text{V}$, $R_G=25\Omega$, $L=0.5\text{mH}$, $I_{AS}=30\text{A}$
- 3.Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.



Typical Performance Characteristics

Figure 1: Output Characteristics

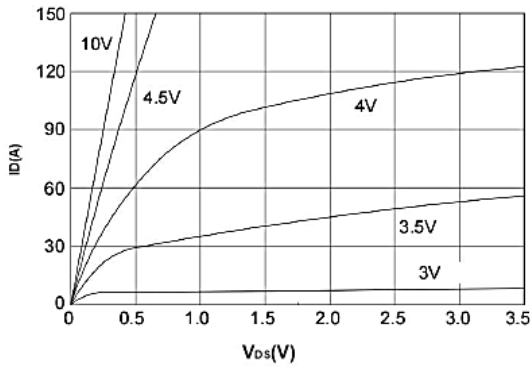


Figure 2: Typical Transfer Characteristic

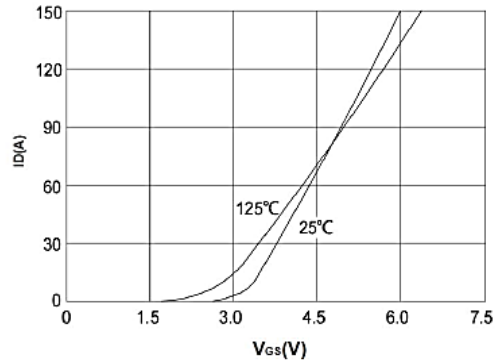


Figure 3: On-resistance vs. Drain Current

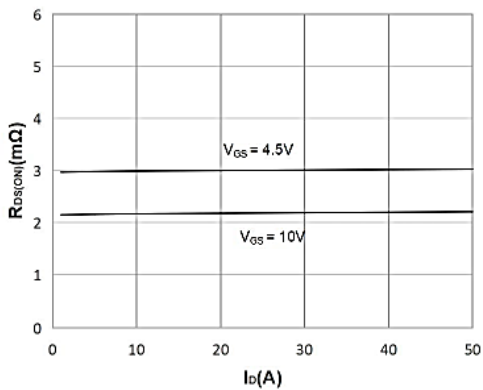


Figure 4: Body Diode Characteristics

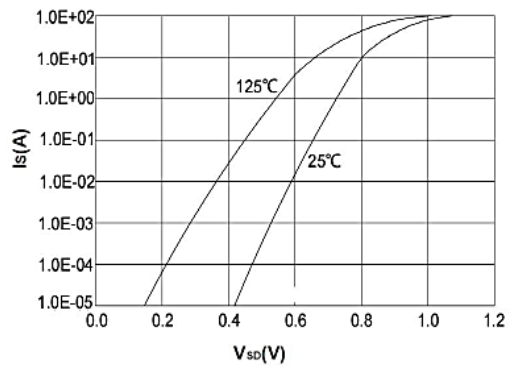


Figure 5: Gate Charge Characteristics

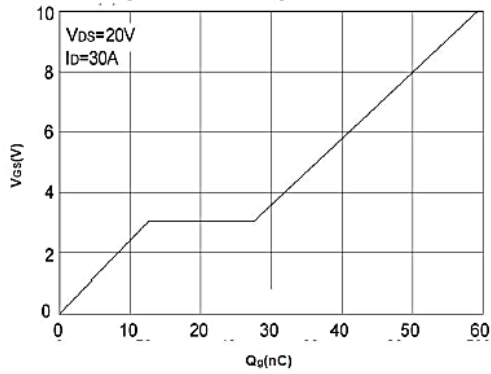


Figure 6: Capacitance Characteristics

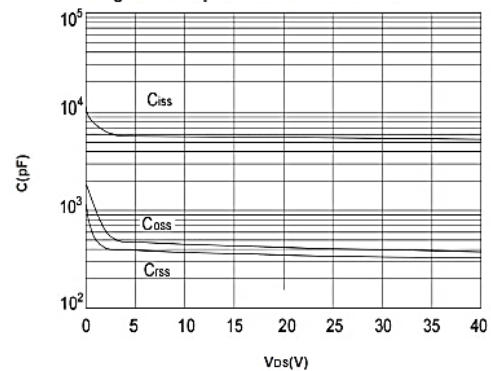


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

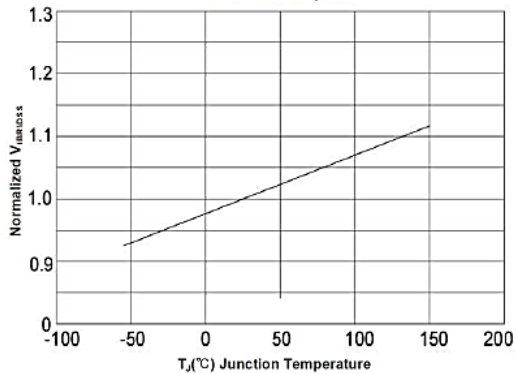
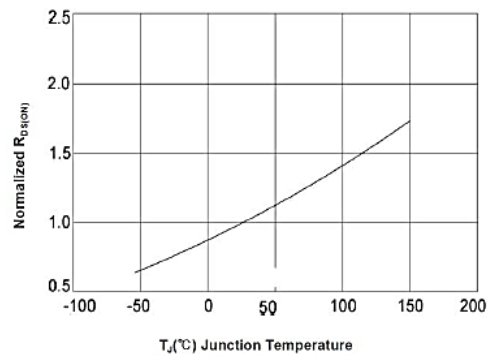


Figure 8: Normalized on Resistance vs. Junction Temperature



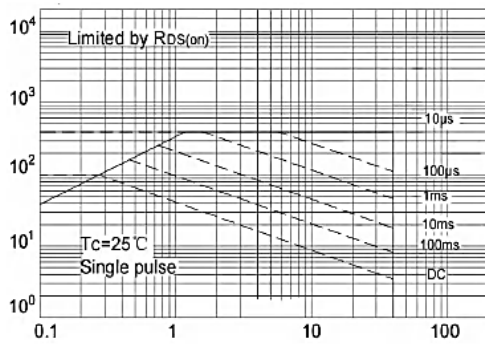


Figure 11: Normalized Maximum Transient Thermal Impedance

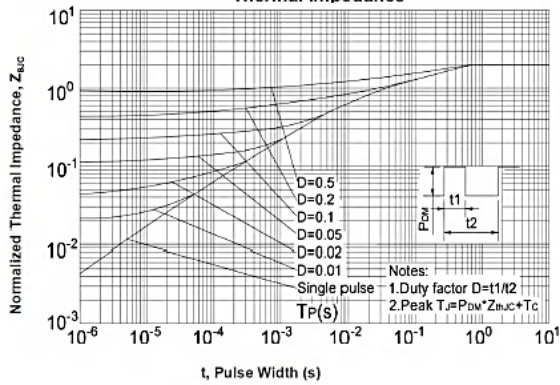


Figure 11: Normalized Maximum Transient Thermal Impedance

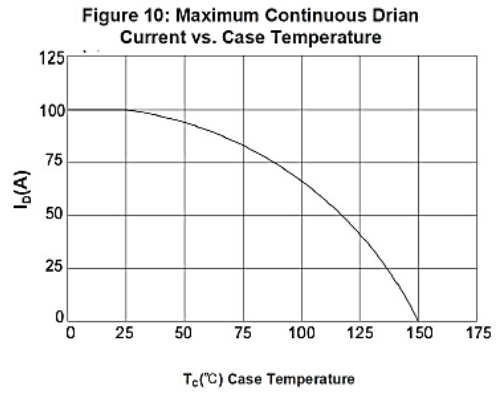


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

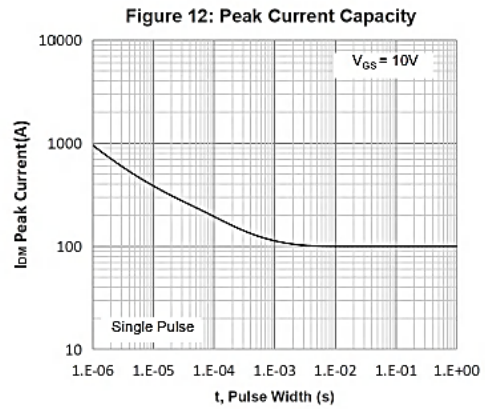


Figure 12: Peak Current Capacity



Test Circuit

Figure 1: Gate Charge Test Circuit & Waveform

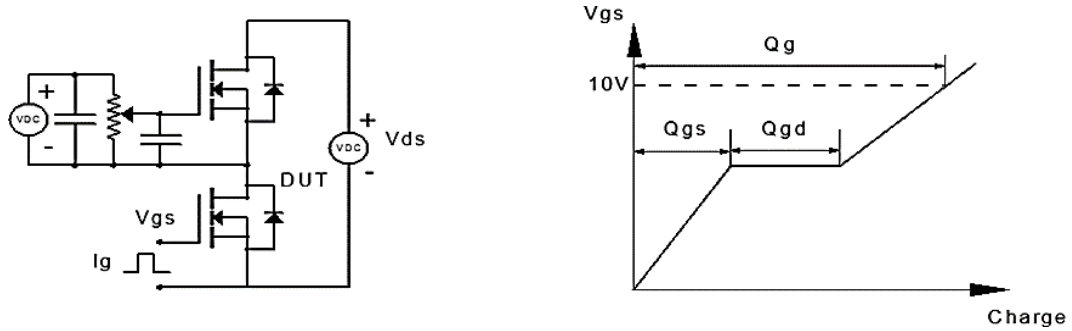


Figure 2: Resistive Switching Test Circuit & Waveform

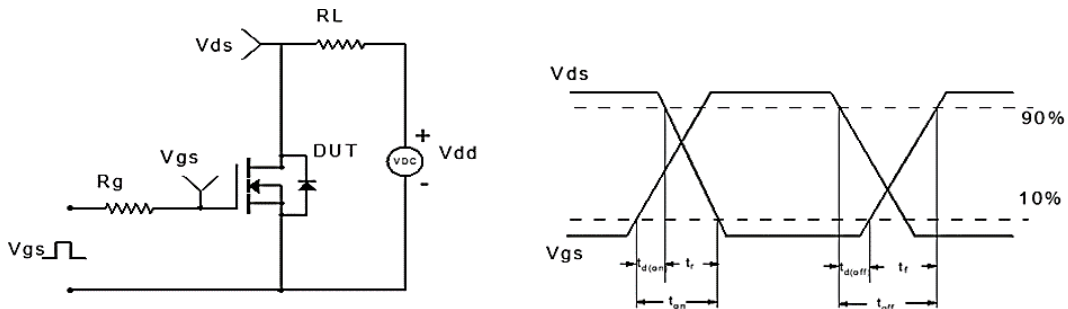


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

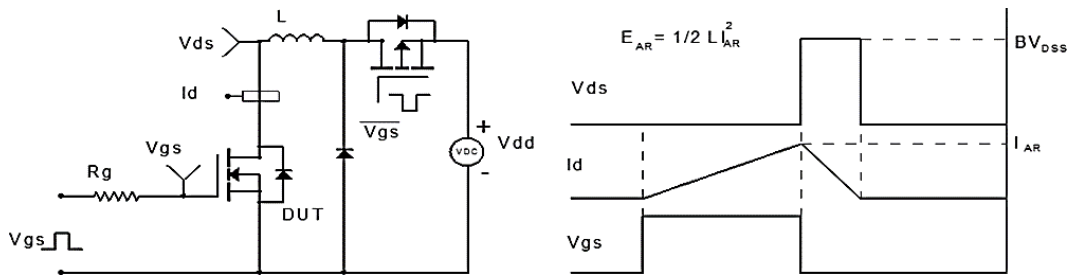
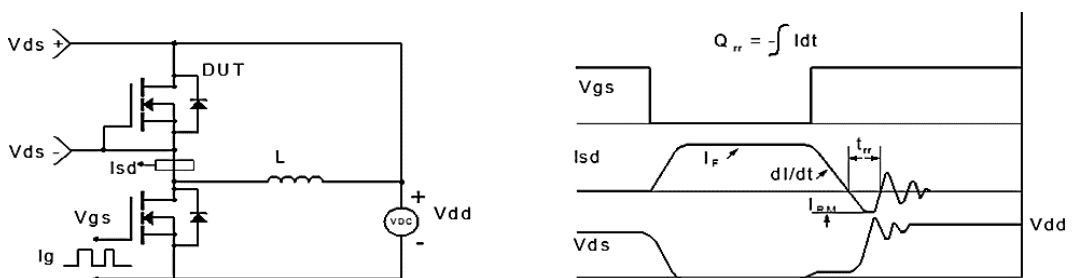
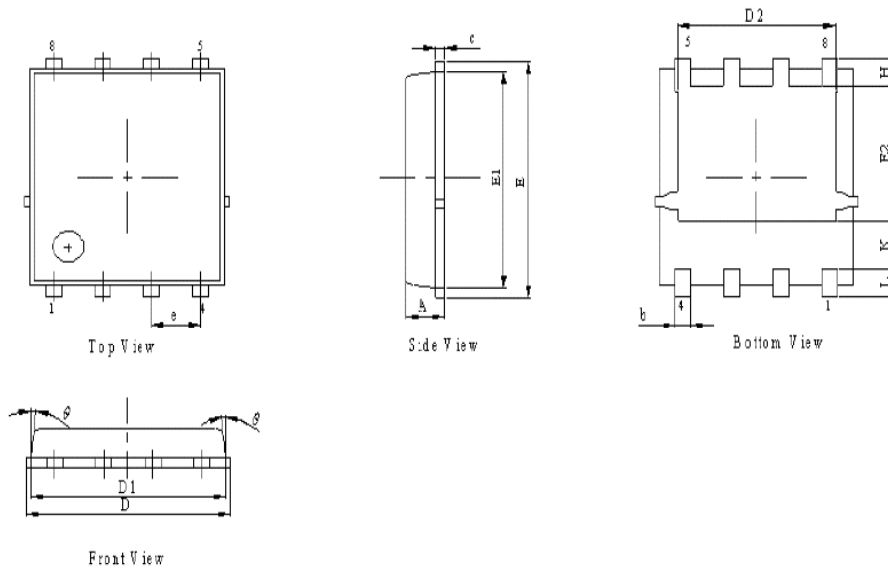


Figure 4: Diode Recovery Test Circuit & Waveform

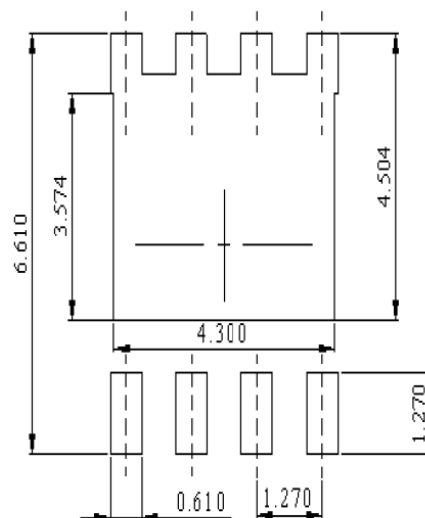




Package Mechanical Data(PDFN 5x6-8L)



Symbol	Dimensions In Millimeters		
	Min.	NOM.	Max.
A	0.9	1	1.15
b	0.31	0.41	0.51
C	0.24	0.32	0.4
D	5	5.2	5.4
D1	4.95	5.05	5.15
D2	4	4.1	4.2
E	6.05	6.15	6.25
E1	5.5	5.6	5.7
E2	3.42	3.53	3.63
e	1.27 BSC		
H	0.6	0.7	0.8
L	0.5	0.7	0.8
K	1.23 BEF		
O			10



DIMENSIONS: MILLIMETERS



Ordering information

Order Code	Package	V _{DS} (V)	I _D (A)	R _{DS(ON)} (mΩ)	
QNN180N04AX	PDFN 5x6-8	40	100	V _{GS} =10V	2.2
				V _{GS} =4.5V	3