



QNHCHIP

QNN150N06

Product Specification

QNN150N06

60V N-Channel MOSFET



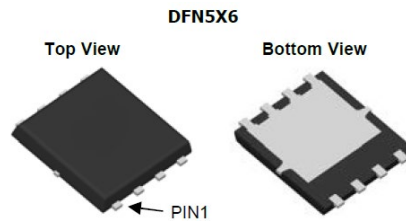
FEATURES

- 60V, 114A
 $R_{DS(ON)} = 3.7\text{ m}\Omega @ V_{GS} = 10\text{V (Typ.)}$
 $R_{DS(ON)} = 4.5\text{m}\Omega @ V_{GS} = 8.0\text{V (Typ.)}$
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Halogen-free; RoHS-compliant
- Pb-free plating

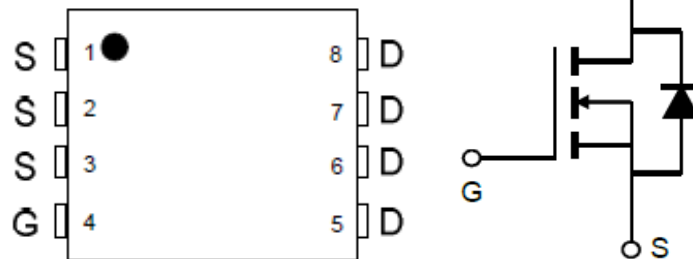
Applications

- Load Switch
- PWM Application
- Power Management

Pin Description



Top View



NO.	Symbol	Description
1	S	SOURCE
2	S	SOURCE
3	S	SOURCE
4	G	GATE
5	D	DRAIN
6	D	DRAIN
7	D	DRAIN
8	D	DRAIN



Absolute Maximum Ratings

(@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	
V_{DS}	Drain-to-Source Voltage	60	V	
V_{GS}	Gate-to-Source Voltage	± 20	V	
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	114	A
		$T_C=100^\circ\text{C}$	72	
I_{DM}	Pulsed Drain Current ⁽¹⁾	411	A	
E_{AS}	Single Pulsed Avalanche Energy ⁽²⁾	324	mJ	
P_D	Power Dissipation	$T_C=25^\circ\text{C}$	118	W
		$T_C=100^\circ\text{C}$	47	
T_J, T_{STG}	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$	

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient ⁽³⁾	41	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.1	



Electrical Characteristics

(T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	I _D =250uA, V _{GS} =0V	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =48V, V _{GS} =0V	-	-	1.0	uA
I _{GSS}	Gate-Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2.0	2.8	3.9	V
R _{DS(ON)}	Static Drain-Source ON-Resistance ⁽⁴⁾	V _{GS} =10V, I _D =30A	-	3.7	4.4	mΩ
		V _{GS} =8.0V, I _D =30A	-	4.5	5.3	mΩ
Dynamic Characteristics						
R _g	Gate Resistance	f = 1MHz	-	1.0	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, f=1MHz		5114		pF
C _{oss}	Output Capacitance			363		pF
C _{rss}	Reverse Transfer Capacitance			318		pF
Q _g	Total Gate Charge	V _{GS} =0~10V V _{DS} =30V, I _D =20A		98		nC
Q _{gs}	Gate Source Charge			12		nC
Q _{gd}	Gate Drain("Miller") Charge			32		nC
Switching Characteristics						
t _{d(on)}	Turn-On DelayTime	V _{GS} =10V, V _{DD} =30V, I _D =20A, R _{GEN} =3 Ω	-	9	-	ns
t _r	Turn-On Rise Time			6.1	-	ns
t _{d(off)}	Turn-Off DelayTime			33	-	ns
t _f	Turn-Off Fall Time			9	-	ns
Body Diode Characteristics						
I _S	Maximum Continuous Body Diode Forward Current		-	-	114	A
I _{SM}	Maximum Pulsed Body Diode Forward Current		-	-	411	A
V _{SD}	Body Diode Forward Voltage	V _{GS} =0V, I _S =20A	-		1.2	V
t _{rr}	Body Diode Reverse Recovery Time	I _F =20A, di/dt=100A/us	40	57	76	ns
Q _{rr}	Body Diode Reverse Recovery Charge			-	69	-

Notes:

- 1.Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
- 2.E_{AS} condition: Starting T_J=25°C, V_{DD}=30V, V_G=10V, R_G=25 Ω, L=3mH, I_{AS}=14.7A, V_{DD}=0V during time in avalanche.
- 3.R_{θJA} is measured with the device mounted on a 1 inch² pad of 2oz copper FR4 PCB.
- 4.Pulse Test: Pulse Width ≤ 300us, Duty Cycle ≤ 0.5%.



Test Circuit

Figure 1: Gate Charge Test Circuit & Waveform

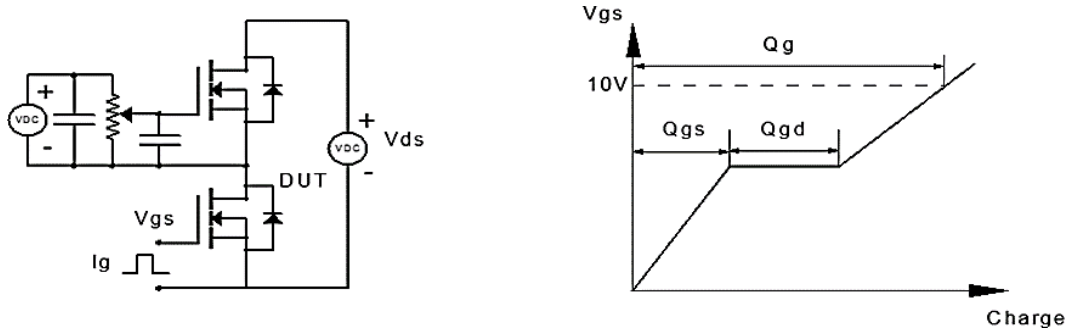


Figure 2: Resistive Switching Test Circuit & Waveform

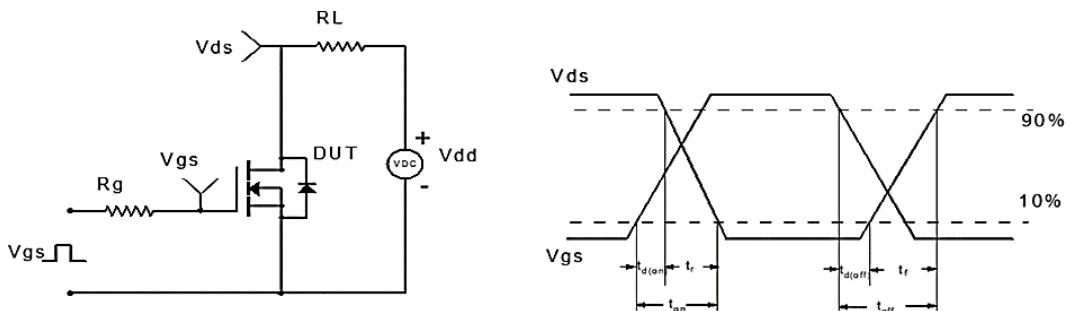


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

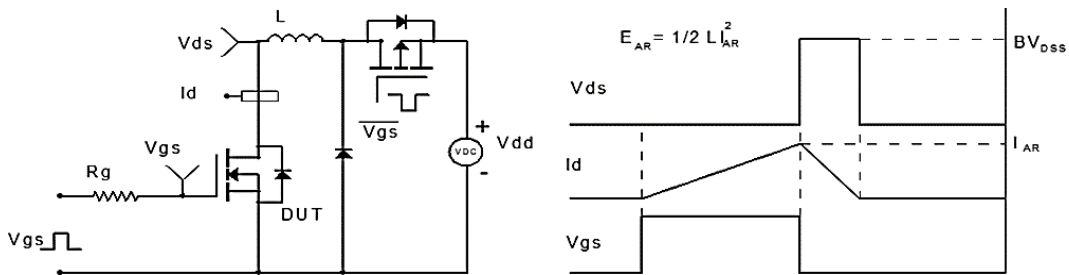
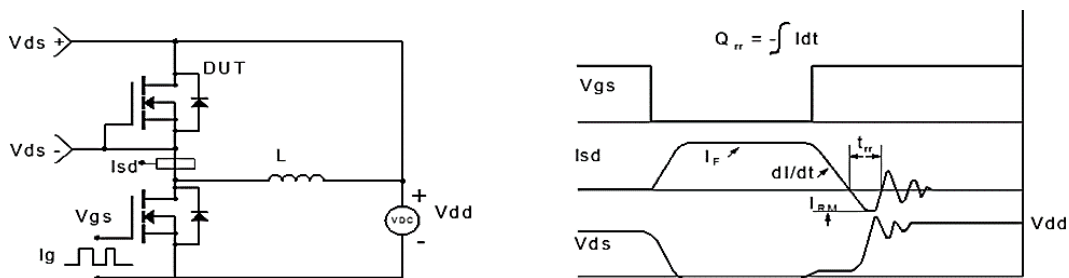
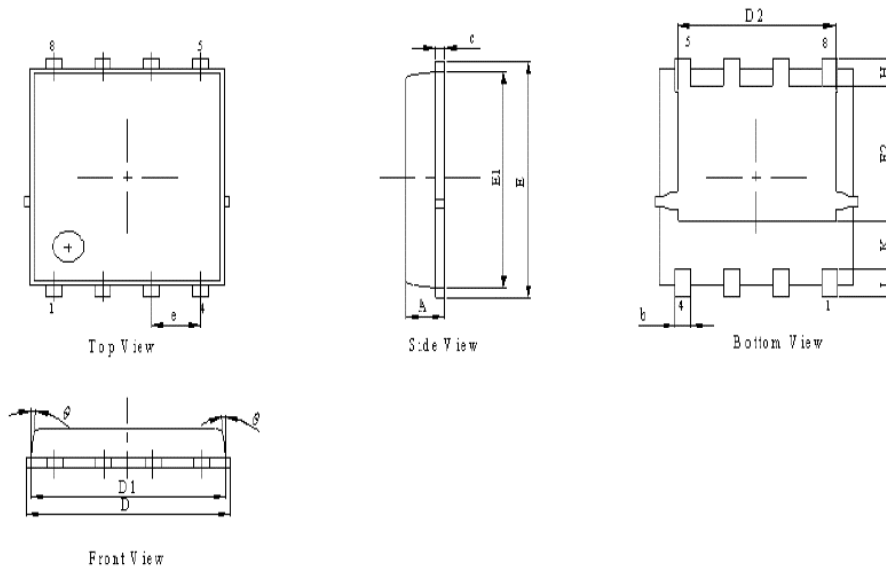


Figure 4: Diode Recovery Test Circuit & Waveform

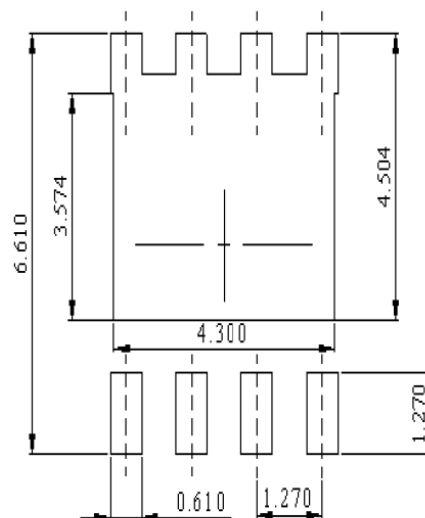




Package Mechanical Data(PDFN 5x6-8L)



Symbol	Dimensions In Millimeters		
	Min.	NOM.	Max.
A	0.9	1	1.15
b	0.31	0.41	0.51
C	0.24	0.32	0.4
D	5	5.2	5.4
D1	4.95	5.05	5.15
D2	4	4.1	4.2
E	6.05	6.15	6.25
E1	5.5	5.6	5.7
E2	3.42	3.53	3.63
e	1.27 BSC		
H	0.6	0.7	0.8
L	0.5	0.7	0.8
K	1.23 BEF		
O			10



DIMENSIONS: MILLIMETERS



Ordering information

Order Code	Package	V _{DS} (V)	I _D (A)	R _{DS(ON)} (mΩ)	
QNN150N06	PDFN 5x6-8	60	114	V _{GS} =10V	3.7
				V _{GS} =8.0V	4.5